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**ELECTRICAL & COMPUTER  
ENGINEERING**  
TEXAS A & M UNIVERSITY

## SEMINAR

**Room 1020 ETB**

March 29, 2019, 1:50 – 2:50 P.M.

### **A System Level View of Design of Modern Analog Front End**

by

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**Abstract:** Design of Analog Front often entail multi-level optimization that span architecture and circuit and system level innovations. This talk outlines the process in context of two representative receivers operating at different data rates. A top down approach is used to deconstruct system specifications into block level requirements. Design tradeoffs across the analog front end are presented while outlining various decisions that go into choosing optimum circuit architectures.

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**Manisha Gambhir** is Principal Analog Design Engineer at Marvell Semiconductor in Storage and Wireline applications. She received her PhD and M.Sc. degrees from Texas A&M University in 2009 and 2006 respectively. Dr. Gambhir's area of interest and past work include design of continuous time filters, data converters, digitally assisted analog circuit, timing circuits, PLLs and system level optimization.