

Access provided by:  
**Texas A M University**  
 Sign Out

BROWSE

MY SETTINGS

GET HELP

WHAT CAN I ACCESS?

Browse Journals &amp; Magazines &gt; Circuits, Devices and Systems, ...

## Circuits, Devices and Systems, IEE Proceedings -

Popular

Current Issue

Past Issues

About Journal

### Popular Articles (May 2015)

Includes the top 50 most frequently downloaded documents for this publication according to the most recent monthly usage statistics.

 Select All Results

Sort by: Most Popular

**1. CMOS transconductance amplifiers, architectures and active filters: a tutorial**

Sánchez-Sinencio, E. ; Silva-Martinez, J.  
 Publication Year: 2000 , Page(s): 3 - 12  
 Cited by: Papers (99) | Patents (2)

  Abstract | PDF (892 KB)



**2. Development of an electromagnetic micro-generator**

Williams, C.B. ; Shearwood, C. ; Harradine, M.A. ; Mellor, P.H. ; Birch, T.S. ; Yates, R.B.  
 Publication Year: 2001 , Page(s): 337 - 342  
 Cited by: Papers (36) | Patents (9)

  Abstract | PDF (799 KB)



**3. Novel CMOS differential voltage current conveyor and its applications**

Elwan, H.O. ; Soliman, A.M.  
 Publication Year: 1997 , Page(s): 195 - 200  
 Cited by: Papers (84)

  Abstract | PDF (448 KB)



**4. Input impedance and output impedance of feedback amplifiers**

Cherry, E.M.  
 Publication Year: 1996 , Page(s): 195 - 201  
 Cited by: Papers (5)

  Abstract | PDF (492 KB)




























**5. Nanoengineering of materials for field emission display technologies**

Silva, S.R.P. ; Carey, J.D. ; Chen, G.Y. ; Cox, D.C. ; Forrest, R.D. ; Poa, C.H.P. ; Smith, R.C. ; Tang, Y.F. ; Shannon, J.M.  
 Publication Year: 2004 , Page(s): 489 - 496

  Abstract | PDF (694 KB)




- 6. Low-voltage low-power CMOS full adder** 
- Radhakrishnan, D.  
 Publication Year: 2001 , Page(s): 19 - 24  
 Cited by: Papers (36)
-  |  Abstract | PDF (556 KB)
- 
- 7. Active noise cancellation with a fuzzy adaptive filtered-X algorithm** 
- Chang, C.-Y. ; Shyu, K.-K.  
 Publication Year: 2003  
 Cited by: Papers (7)
-  |  Abstract | PDF (456 KB)
- 
- 8. High-performance low-power current sense amplifier using a cross-coupled current-mirror configuration** 
- Yeo, K.S. ; Goh, W.L. ; Kong, Z.-H. ; Zhang, Q.-X. ; Yeo, W.-G.  
 Publication Year: 2002 , Page(s): 308 - 314  
 Cited by: Papers (5)
-  |  Abstract | PDF (989 KB)
- 
- 9. Analysis of a composite amplifier technique for optimum noise performance** 
- Peyton, A.J. ; Abou-Fakher, Y. ; Wilson, B.  
 Publication Year: 1994 , Page(s): 210 - 214  
 Cited by: Papers (1)
-  |  Abstract | PDF (344 KB)
- 
- 10. Thin film polycrystalline silicon solar cell on ceramics with a seeding layer formed via aluminium-induced crystallisation of amorphous silicon** 
- Ornaghi, C. ; Stoger, M. ; Beaucarne, G. ; Poortmans, J. ; Schattschneider, P.  
 Publication Year: 2003  
 Cited by: Patents (1)
-  |  Abstract | PDF (349 KB)
- 
- 11. High-performance FPGA implementation of DES using a novel method for implementing the key schedule** 
- McLoone, M. ; McCanny, J.V.  
 Publication Year: 2003  
 Cited by: Papers (6) | Patents (1)
-  |  Abstract | PDF (254 KB)
- 
- 12. Impact of technology scaling on the  $1/f$  noise of thin and thick gate oxide deep submicron NMOS transistors** 
- Chew, K.W. ; Chew, K.W. ; Yeo, K.S. ; Chu, S.-F.  
 Publication Year: 2004 , Page(s): 415 - 421  
 Cited by: Papers (9)
-  |  Abstract | PDF (395 KB)
- 
- 13. General synthesis of complex analogue filters** 
- Cuyppers, C. ; Voo, N.Y. ; Teplechuk, M. ; Sewell, J.I.  
 Publication Year: 2005 , Page(s): 7 - 15  
 Cited by: Papers (6) | Patents (1)
-  |  Abstract | PDF (784 KB)
- 
- 

**14. Design of an area-efficient CMOS multiple-valued current comparator circuit**

Kong, Z.-H. ; Yeo, K.-S. ; Chang, C.-H.

Publication Year: 2005 , Page(s): 151 - 158

Cited by: Papers (1)

 |  Abstract | PDF (1992 KB) **15. Reduced complexity 1-bit high-order digital delta-sigma modulator for low-voltage fractional-N frequency synthesis applications**

Bornoosh, B. ; Afzali-Kusha, A. ; Dehghani, R. ; Mehrara, M. ; Atarodi, S.M. ; Nourani, M.

Publication Year: 2005 , Page(s): 471 - 477

Cited by: Papers (6)

 |  Abstract | PDF (303 KB) **16. Approximation of sigmoid function and the derivative for hardware implementation of artificial neurons**

Basterretxea, K. ; Tarela, J.M. ; del Campo, I.

Publication Year: 2004 , Page(s): 18 - 24

Cited by: Papers (16)

 |  Abstract | PDF (611 KB) **17. CMOS differential difference current conveyors and their applications**

Chiu, W. ; Liu, S.-I. ; Tsao, H.W. ; Chen, J.-J.

Publication Year: 1996 , Page(s): 91 - 96

Cited by: Papers (90)

 |  Abstract | PDF (452 KB) **18. Low-voltage low-power fast-settling CMOS operational transconductance amplifiers for switched-capacitor applications**

Yavari, M. ; Shoaie, O.

Publication Year: 2004 , Page(s): 573 - 578

Cited by: Papers (11)

 |  Abstract | PDF (556 KB) **19. CMOS floating active inductor and its applications to bandpass filter and oscillator designs**

Thanachayanont, A. ; Payne, A.

Publication Year: 2000 , Page(s): 42 - 48

Cited by: Papers (23)

 |  Abstract | PDF (544 KB) **20. Low-power CMOS integrated circuits for radio frequency applications**

Deen, M.J. ; Murji, R. ; Fakhr, A. ; Jafferalli, N. ; Ngan, W.L.

Publication Year: 2005 , Page(s): 509 - 522

Cited by: Papers (21) | Patents (2)


























 |  Abstract | PDF (1328 KB) **21. Comments on "CMOS differential difference current conveyors and their applications"**

Gupta, S.S. ; Senani, R.


Publication Year: 2001 , Page(s): 335 - 336

Cited by: Papers (4)

 |  Abstract | PDF (84 KB)



- 
- 22. Output buffer design for low noise and load adaptability** 
- Yang, L. ; Jiann-Shiun Yuan  
Publication Year: 2005 , Page(s): 146 - 150  
Cited by: Papers (4) | Patents (2)
-  |  Abstract | PDF (888 KB)
- 
- 23. Low voltage, low power, high performance current mirror for portable analogue and mixed mode applications** 
- Rajput, S.S. ; Jamuar, S.S.  
Publication Year: 2001 , Page(s): 273 - 278  
Cited by: Papers (12) | Patents (1)
-  |  Abstract | PDF (607 KB)
- 
- 24. Series-L/parallel-tuned comparison with shunt-C/series-tuned class-E power amplifier** 
- Mury, T. ; Fusco, V.F.  
Publication Year: 2005 , Page(s): 709 - 717  
Cited by: Papers (5)
-  |  Abstract | PDF (233 KB)
- 
- 25. Linearity analysis and design optimisation for 0.18  $\mu\text{m}$  CMOS RF mixer** 
- Li, Q. ; Jiann-Shiun Yuan  
Publication Year: 2002 , Page(s): 112 - 118  
Cited by: Papers (18)
-  |  Abstract | PDF (608 KB)
- 
- 26. 6-bit 500 MHz flash A/D converter with new design techniques** 
- Hsu, C.-W. ; Kuo, T.-H.  
Publication Year: 2003  
Cited by: Papers (4) | Patents (1)
-  |  Abstract | PDF (298 KB)
- 
- 27. Noise performance analysis of bipolar operational amplifier based on the noise matrix superposition expression** 
- Dai, Y.  
Publication Year: 1998 , Page(s): 343 - 346
-  |  Abstract | PDF (316 KB)
- 
- 28. Analysis and implementation of a delay-compensated deadbeat current controller for solar inverters** 
- Hung, G.-K. ; Chang, C.C. ; Chen, C.-L.  
Publication Year: 2001 , Page(s): 279 - 286  
Cited by: Papers (10) | Patents (16)
-  |  Abstract | PDF (726 KB)
- 
- 29. Low-power single- and double-edge-triggered flip-flops for high-speed applications** 
- Rasouli, S.H. ; Khademzadeh, A. ; Afzali-Kusha, A. ; Nourani, M.  
Publication Year: 2005 , Page(s): 118 - 122  
Cited by: Papers (12)
-  |  Abstract | PDF (869 KB)
- 
- 30. Smart image sensing in CMOS technology** 


Ni, Y.  
 Publication Year: 2005 , Page(s): 547 - 555  
 Cited by: Papers (3)

 |  Abstract | PDF (4587 KB)

**31. Thin film silicon materials and solar cells grown by pulsed PECVD technique** 

Das, U. ; Morrison, S. ; Centurioni, E. ; Madan, A.  
 Publication Year: 2003  
 Cited by: Papers (2) | Patents (1)

 |  Abstract | PDF (349 KB)

**32. High frequency and high resolution capacitance measuring circuit for process tomography** 

Yang, W.Q. ; Stott, A.L. ; Beck, M.S.  
 Publication Year: 1994 , Page(s): 215 - 219  
 Cited by: Papers (16)

 |  Abstract | PDF (272 KB)

**33. Improved neuron MOS-transistor structures for integrated neural network circuits** 


Rantala, A. ; Franssila, S. ; Kaski, K. ; Lampinen, J. ; Aberg, M. ; Kuivalainen, P.  
 Publication Year: 2001 , Page(s): 25 - 34  
 Cited by: Papers (3) | Patents (1)

 |  Abstract | PDF (788 KB)

**34. X-ray detector with on-pixel amplification for large area diagnostic medical imaging** 

Karim, K.S. ; Nathan, A. ; Rowlands, J.A. ; Kasap, S.O.  
 Publication Year: 2003  
 Cited by: Papers (10) | Patents (1)

 |  Abstract | PDF (450 KB)

**35. Fast and energy-efficient Manchester carry-bypass adders** 

Perri, S. ; Corsonello, P. ; Pezzimenti, F. ; Kantabutra, V.  
 Publication Year: 2004 , Page(s): 497 - 502  
 Cited by: Papers (4)

 |  Abstract | PDF (752 KB)

**36. Finite element modelling of thermal fatigue effects in IGBT modules** 

Shammas, N.Y.A. ; Rodriguez, M.P. ; Plumpton, A.T. ; Newcombe, D.  
 Publication Year: 2001 , Page(s): 95 - 100  
 Cited by: Papers (5)

 |  Abstract | PDF (960 KB)



**37. Parallel event-driven logic simulation algorithms: tutorial and comparative evaluation** 

Baker, W.I. ; Mahmood, A. ; Carlson, B.S.  
 Publication Year: 1996 , Page(s): 177 - 185  
 Cited by: Papers (3) | Patents (2)

 |  Abstract | PDF (1020 KB)

**38. 10-bit switched-current digital-to-analogue converter** 


Tsai, C.-C. ; Lai, C.-H. ; Lee, W.-T. ; Wu, J.-O.  
Publication Year: 2005 , Page(s): 287 - 290  
Cited by: Papers (2)

 |  Abstract | PDF (1273 KB)

**39. Ferroresonant oscillations in capacitor voltage transformers** 

Chakravarthy, S.K. ; Nayar, C.V.  
Publication Year: 1995 , Page(s): 30 - 36  
Cited by: Papers (7)

 |  Abstract | PDF (376 KB)

**40. Fourth-order PLL loop filter design technique with invariant natural frequency and phase margin** 

Thompson, I.V. ; Brennan, P.V.  
Publication Year: 2005 , Page(s): 103 - 108  
Cited by: Papers (6)

 |  Abstract | PDF (928 KB)

**41. Simulation technique for noise and timing jitter in electronic oscillators** 


Zhang, C.W. ; Wang, X.Y. ; Forbes, L.  
Publication Year: 2004 , Page(s): 184 - 189  
Cited by: Papers (4)

 |  Abstract | PDF (1393 KB)

**42. Low-voltage cmos current-mode circuits: topology and characteristics** 

Yuan, F.  
Publication Year: 2006 , Page(s): 219 - 230

 |  Abstract | PDF (676 KB)

**43. Voltage-variable attenuator MMIC using phase cancellation** 


Saavedra, C.E. ; Jackson, B.R.  
Publication Year: 2006 , Page(s): 442 - 446  
Cited by: Papers (2)

 |  Abstract | PDF (567 KB)

**44. Accelerating matrix product on reconfigurable hardware for image processing applications** 

Bensaali, F. ; Amira, A. ; Bouridane, A.  
Publication Year: 2005 , Page(s): 236 - 246  
Cited by: Papers (6) | Patents (1)

 |  Abstract | PDF (4823 KB)

**45. High performance 5 : 2 compressor architectures** 

Menon, R. ; Radhakrishnan, D.  
Publication Year: 2006 , Page(s): 447 - 452  
Cited by: Papers (5)

 |  Abstract | PDF (383 KB)

**46. Approach to the design of low-voltage SC filters** 

Filoramo, P. ; Giustolisi, G. ; Palmisano, G. ; Palumbo, G.  
Publication Year: 2000 , Page(s): 196 - 200  
Cited by: Papers (3)

[Abstract](#) | [PDF \(308 KB\)](#)

- 47. 2 GHz controllable power amplifier in standard CMOS process for short-range wireless applications**



Hella, M.M. ; Ismail, M.

Publication Year: 2002 , Page(s): 363 - 368

Cited by: Papers (2)

[Abstract](#) | [PDF \(685 KB\)](#)

- 48. Switched capacitor triple slope capacitance to digital converter**



George, B. ; Kumar, V.J.

Publication Year: 2006 , Page(s): 148 - 152

Cited by: Papers (1)

[Abstract](#) | [PDF \(164 KB\)](#)

- 49. New path balancing algorithm for glitch power reduction**



Kim, S. ; Kim, J. ; Hwang, S.-Y.

Publication Year: 2001 , Page(s): 151 - 156

Cited by: Papers (4)

[Abstract](#) | [PDF \(692 KB\)](#)

- 50. Low power high-speed CMOS dual-modulus prescaler design with imbalanced phase-switching technique**



Yu, X.P. ; Do, M.A. ; Ma, J.-G. ; Yeo, K.S. ; Wu, R. ; Yan, G.Q.

Publication Year: 2005 , Page(s): 127 - 132

Cited by: Papers (6)

[Abstract](#) | [PDF \(984 KB\)](#)

## Further Links

[Personal Sign In](#) | [Create Account](#)

### IEEE Account

- » [Change Username/Password](#)
- » [Update Address](#)

### Purchase Details

- » [Payment Options](#)
- » [Order History](#)
- » [Access Purchased Documents](#)

### Profile Information

- » [Communications Preferences](#)
- » [Profession and Education](#)
- » [Technical Interests](#)

### Need Help?

- » [US & Canada: +1 800 678 4333](#)
- » [Worldwide: +1 732 981 0060](#)
- » [Contact & Support](#)

[About IEEE Xplore](#) | [Contact Us](#) | [Help](#) | [Terms of Use](#) | [Nondiscrimination Policy](#) | [Sitemap](#) | [Privacy & Opting Out of Cookies](#)

A not-for-profit organization, IEEE is the world's largest professional association for the advancement of technology.

© Copyright 2015 IEEE - All rights reserved. Use of this web site signifies your agreement to the terms and conditions.