



Alberto Valdes-Garcia Born in 1978, grew up in San Mateo Atenco, Mex., Received the B.S. in Electronic Systems Engineering degree from the Monterrey Institute of Technology (ITESM), Campus Toluca, Mexico in 1999 (highest honors as best score from all majors) and the Ph.D. degree in Electrical Engineering from Texas A&M University in 2006.

In 2000 he was a Design Engineer with Motorola, Broadband Communications Sector. From 2001 to 2004 he was a Semiconductor Research Corporation (SRC) research assistant at the Analog & Mixed-Signal Center (AMSC), Texas A&M University. He held internships with Agere Systems (former Lucent Technologies, now LSI) and IBM Research in 2002 and 2004, respectively. Since January 2006 he has been a Research Staff Member with the Communication Technologies Department, IBM T. J. Watson Research Center. His present research work is on silicon integrated millimeter-wave communication systems and carbon electronics. He currently serves as a voting member and technical contributor to the IEEE 802.15.3c 60GHz standardization committee. He is an Associate Editor of the Journal of VLSI Design. He has authored or co-authored more than 30 peer-referred journal and conference publications, and 3 book chapters. He holds 1 US patent and has 4 more pending.

He represented Mexico in the 1994 'Odyssey of the Mind' world creativity competition and in the 1997 International Exhibition for Young Scientists. From 2000 to 2005, Dr. Valdes-Garcia was the recipient of a scholarship from the Mexican National Council for Science and Technology (CONACYT). He is the winner of the 2005 Best Doctoral Thesis Award presented by the IEEE Test Technology Technical Council (TTTC). In 2008 he was a co-recipient of an IBM Corporate Outstanding Innovation Award for the demonstration of wireless high definition video links with 60GHz SiGe radios. He is the recipient of the 2007 National Youth Award for Outstanding Academic Achievements, presented by the Federal Government of Mexico.