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**ELECTRICAL & COMPUTER
ENGINEERING**
TEXAS A & M UNIVERSITY

SEMINAR

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Verification – Key to Design Success

by

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Abstract: As the technology shrinks, there is a push to integrate complex functionality into Si to reduce system costs. As designs get more complex, it is crucial to implement a robust verification strategy to ensure minimal Si bugs and avoid costly re-spins. The talk is focused on what full chip verification is and how they help in delivering a bug-free product to the market. It will also touch upon the importance of regression and various components needed to complete successful verification.

Sirisha Adluri received the B.E (Hons.) from Birla Institute of Technology and Science, India in 2000 and M.S.E.E from Oregon State University, Corvallis in 2003. She has been with Texas Instruments for the past nine years. She has been involved in Automotive Sensor IC design for the past 9 years and currently working as a Verification lead for the Audio and Imaging Products group.