# **Errata**

Title & Document Type: 8082A Pulse Generator Operating and Service Manual

Manual Part Number: 08082-90003

Revision Date: September 1983

# **HP References in this Manual**

This manual may contain references to HP or Hewlett-Packard. Please note that Hewlett-Packard's former test and measurement, semiconductor products and chemical analysis businesses are now part of Agilent Technologies. We have made no changes to this manual copy. The HP XXXX referred to in this document is now the Agilent XXXX. For example, model number HP8648A is now model number Agilent 8648A.

# **About this Manual**

We've added this manual to the Agilent website in an effort to help you support your product. This manual provides the best information we could find. It may be incomplete or contain dated information, and the scan quality may not be ideal. If we find a better copy in the future, we will add it to the Agilent website.

# **Support for Your Product**

Agilent no longer sells or supports this product. You will find any other available product information on the Agilent Test & Measurement website:

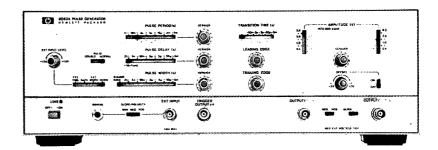
# www.tm.agilent.com

Search for the model number of this product, and the resulting product page will guide you to any available information. Our service centers may be able to perform calibration if no repair parts are needed, but no other support from Agilent is available.



S

# PULSE GENERATOR 8082A





OTE

JOHNSON, RICHARD E

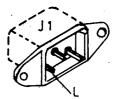
SUPERSEDES:

# HP MODEL 8062A FULSE GENERATOR

Serial Numbers: from 1822G2936 to 1822G3145

FL 1 LINE FILTER WIRING

It is possible that the Line Filter (FL 1) in instruments with S.N. as shown above may be wired incorrectly, causing F 1 to be in the neutral (return) side. To ensure that the Line Fuse F 1 is in the phase (Line) side of the line supply, perform the following whenever an instrument is received for service or repair.

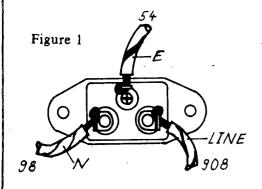


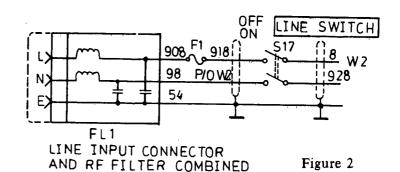
Remove the Power Cord and Fuse Cover. Using an Ohmmeter, check that there is a low resistance path (0 Ohm) between the line terminal (L) of the line connector and the tip of the fuse (Ensure that the fuse is not blown). If this is correct, the Line Filter (FL 1) is correctly installed and no further action is required.

If not, check the Line Filter wiring against Figure 1 and correct the wiring as necessary.

Figure 1 shows the correct Line Filter wiring.

Figure 2 shows a part of Power Supply Schematic





MI/cz/WA

1/84-B1



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JOHNSON, RICHARD E

ENOTE

SUPERSEDES:

None

# **MODEL 8082A PULSE GENERATOR**

Serial Numbers: 1822G3805 and below

# RECOMMENDED REPLACEMENT OF A5 R218 / R221

On BD AY OFFSET 08082-66505, resistors R218 and R221 may be damaged by excessive power in worst case.

To improve the reliability of the current sources:

Replace R218 and R221 365 Ohm .25 Watt with 365 Ohm .5 Watt HP PART NUMBER 0757-0810.

Whenever an instrument with the SN mentioned above is received for service or repair, perform the following procedure:

Remove

A5R218/R221

365 Ohm

Replace with

A5R218/R221

365 Ohm (P/N 0757-0810)

After replacement, the performance checks and if necessary the adjustments must be done.

Please update your O/S Manual to reflect this change.



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SUPERSEDES:

None

# MODEL 8160A PROGRAMMABLE PULSE GENERATOR

Serial Numbers: 2047G0605 and below

# RECOMMENDED REPLACEMENT OF THE FUSEHOLDER

For the obsolete fuseholder, body (P/N 2110-0470) and fuseholder, cap (P/N 2110-0465) the below shown parts are the recommended replacement.

### New parts:

XF1 Fuseholder, body	P/N 2110-0564
XF1 Fuseholder, cap	P/N 2110-0565
XF1 Nut Hex	P/N 2110-0569
XF1 Sleeve	P/N 08160-21701
XF1 Washer	P/N 1400-0090

Whenever an instrument with the SN mentioned above is received for repair because of a defective fuseholder, replace the complete fuseholder. Refer to Figure 1 for the assembly of the new fuseholder parts.

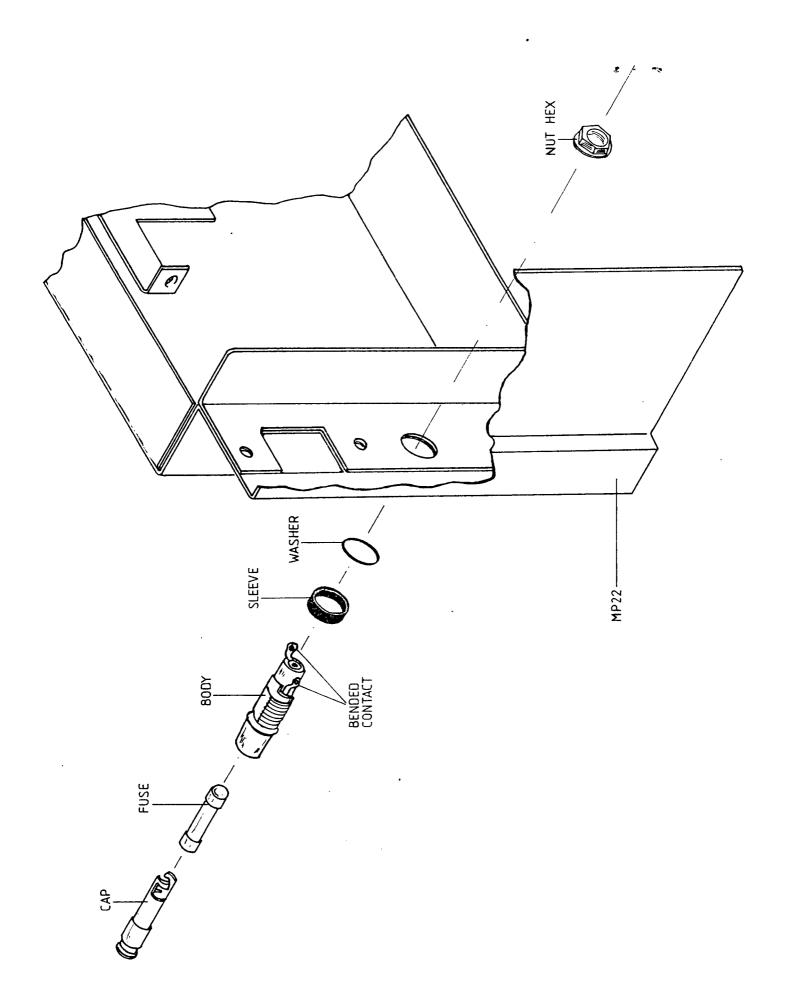
#### NOTE

The contacts of the fuseholder, body must be bended slightly to fit into the holes of board A13.

Please update your O/S Manual to reflect this change.

MI/mi/WO 12/84-B1





SUPERSEDES:

None

# MODEL 8161A PROGRAMMABLE PULSE GENERATOR

Serial Numbers: 2419G0535 and below

# RECOMMENDED REPLACEMENT OF THE FUSEHOLDER

For the obsolete fuseholder, body  $(P/N\ 2110-0470)$  and fuseholder, cap  $(P/N\ 2110-0465)$  the below shown parts are the recommended replacement.

# New parts:

XF1 Fuseholder, body	P/N 2110-0564
XF1 Fuseholder, cap	P/N 2110-0565
XF1 Nut Hex	P/N 2110-0569
XF1 Sleeve	P/N 08160-21701
XF1 Washer	P/N 1400-0090

Whenever an instrument with the SN mentioned above is received for repair because of a defective fuseholder, replace the complete fuseholder. Refer to Figure 1 for the assembly of the new fuseholder parts.

# NOTE:

The contacts of the fuseholder, body must be bended slightly to fit into the holes of board A13.

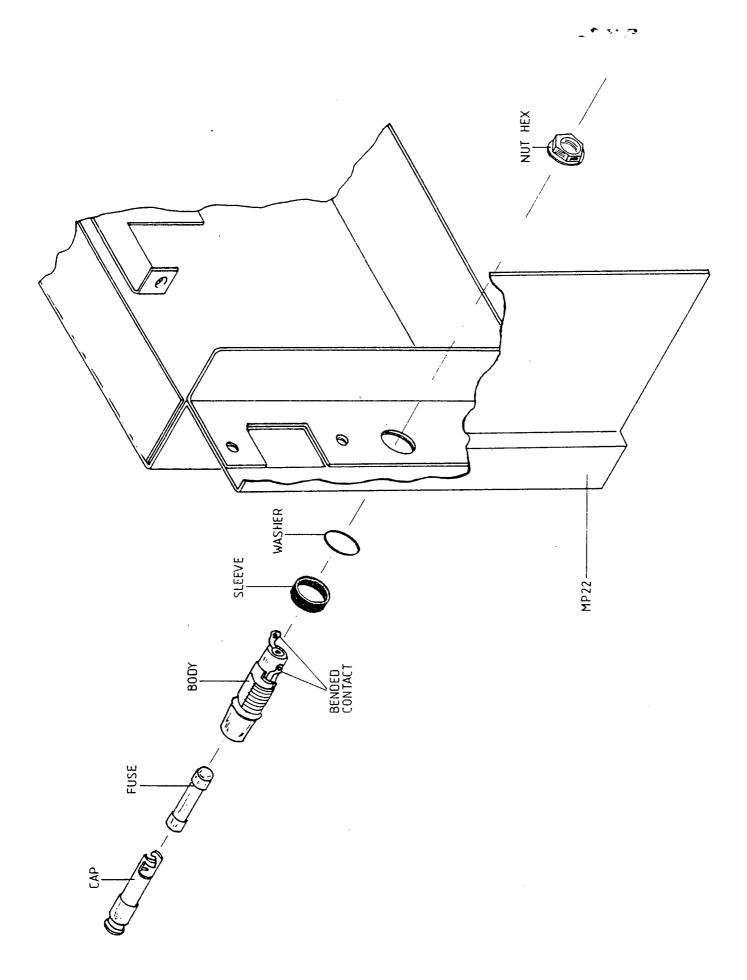
Please update your O/S Manual to reflect this change.

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# MANUAL CHANGES

Manual for Model Number	8082A
Manual printed on	September 1983
Manual Part Number	08082-90003

Make all ERRATA corrections.

Check the following table for your instrument serial prefix/serial number and make the listed changes to your manual.

# New Item

ERRATA 1822G03146 and above 1		
1822G03146 and above 1		
•		
	·	
	·	
		•
,		•
•		
· .		

MISCELL.	Page 6-18			
FRAME	XF3, XF4		MP34,35 MP36	
A9				
A8				
A7				-
A6				
A5	X4,(X4)			
A4	U2,U3 X2, (X1,X2)			
A3				
A2		,		
A1				. •
MANUAL CHANGE	ERRATA		-	

# **ERRATA**

On Table 6-3, Replaceable Frame Parts List:

Add:

XF3

2110-0569

NUT HEX

XF4

1400-0090

WASHER NEOPRENE

On Page 6-9/10, Replaceable Parts List:

A4U2

should read

A4U3 **←** 

1826-0111

IC-DUAL OP AMPL

A4U3

should read

A4U2 **←** 

5081-3009

IC SEALED PKG

A4X2

should read 1200-0548

A4X1 **←** 

SOCKET-IC 16 CONT

Add:

A4 (X1)

5040-9314

LOCK CLIP 14 POL

A4 (X2)

5040-9316

LOCK CLIP 16 POL

On Page 6-12, Replaceable Parts List:

To

A5X4

1200-0588

SOCKET-IC 16 CONT

Add:

A5 (X4)

5040-9316

LOCK CLIP 16 POL

On Page 6-18, change Grid Location G-2 from CR33 to CR39

and

L-3 from R38 to R4

Add:

D-2 R245

# MANUAL CHANGE 1

On Page 6-5, Replaceable Parts List:

Qty
Add: MP34 2360-0201 SCREW 4
MP35 2190-0918 WASHER 4
MP36 3050-0016 WASHER 4

# **Specification**

# **PULSE CHARACTERISTICS**

(Source and load impedance 50  $\Omega$ )

Transition Times: ≤ 1 ns to 0.5 ms in 6 ranges. First range from ≤ 1 ns to 5 ns controls leading and trailing edges simultaneously; risetime and falltime may differ up to 25 % of the faster edge. For all other ranges, edges are independently variable up to 1:10.

Overshoot and Ringing:  $\leq \pm 6$  % of pulse amplitude, may increase to  $\pm 10$  % with amplitude vernier CCW.

Preshoot:  $\leq \pm 5\%$  of pulse amplitude.

Linearity: Linearity aberration for both slopes  $\leq 5$  % for transition times  $\geq 5$  ns.

Output: Maximum amplitude is 5 V from 50  $\Omega$  into 50  $\Omega$ . Maximum output voltage is  $\pm$  5 V (amplitude + offset).

Offset:  $\pm 2 \text{ V}$ , into 50  $\Omega$ .

Baseline: 0 V ± 150 mV (max. amplitude range, offset switched off).

DC-Source Impedance:  $50 \Omega + 5 \%$ , -10 %.

Reflection Coefficient (typical):

Attenuator setting

ECL	5 %
0.5 - 1.0 V	5 %
1.0 - 2.0 V	8 %
2.0 - 5.0  V	15 %

Output Protection: Cannot be damaged by open or short circuits or application of external signals  $\leq \pm 6$  volts or  $\pm 200$  mA.

Attenuator: Two separate three step-attenuators reduce the outputs to 1 V. Vernier is common for both outputs and reduces the output to 0.4 V minimum. A further position provides ECL-compatible outputs (-0.9 V to -1.7 V typ. open circuit).

**TIMING** 

Repetition Rate: > 250 MHz to < 1 kHz in 6 ranges.

Period Jitter: < 0.1 % + 50 ps.

Delay: <2 ns to > 0.5 ms in 6 ranges plus typ 17 ns with respect to trigger output.

Delay Jitter: < 0.1 % + 50 ps.

Double Pulse: Up to 125 MHz max (simulates 250 MHz).

Variable Delay Time (max): > 50 % of period - 2 ns.

Pulse Width: < 2.3 ns min to > 0.5 ms max (6 ranges).

Width Jitter: < 0.1 % + 50 ps.Width Duty Cycle (max): > 50 %.

Square Wave: A further position of the Pulse Width switch provides Square Wave output. (Delay and double pulse are disabled, max. Rep. Rate 250 MHz). Duty cycle is  $50\% \pm 10\%$  up to 100 MHz,  $50\% \pm 15\%$  for > 100 MHz.

Trigger Output: Negative-going square wave (50 % duty cycle typ.) > 500 mV from 50  $\Omega$  into 50  $\Omega$ . Internal 50  $\Omega$  load can be switched off by slide-switch on PC-board. Amplitude increases to  $\geq$  1 V into 50  $\Omega$  up to 200 MHz.

Trigger Output Protection: Cannot be damaged by short circuit or application of external ± 200 mA.

**EXTERNALLY CONTROLLED OPERATION** 

**External Input** 

Input Impedance:  $50 \Omega \pm 10 \%$ . DC coupled.

Maximum Input: ±6 V.

Trigger Level: Adjustable -1.5 V to +1.5 V.

Slope Control: Positive, negative or manual selectable. In the MAN-position all ext functions can be controlled by push button. Button pushed in simulates an "on-signal".

Sensitivity: Sine-wave > 200 mVpp, pulses > 200 mV.

Repetition Rate: 0 to > 250 MHz.

**Ext - Controlled Modes** 

Ext Trigger: Rep rate is determined by external trigger signal. Trigger output delay 7 ns typical. Square wave mode is disabled.

Synchronous Gating: Rate generator starts with a halfperiod delay. Last pulse is of normal width even if gate ends during the pulse.

External Width: Output pulse width determined by width of drive input. Rep rate and delay are disabled.

GENERAL

Power Requirements: 100 V, 120 V, 220 V, 240 V rms (+5 %, -10 %) 48-440 Hz. Power consumption 85 VA max.

**Environmental:** 

Operating Temperature: 0 to 55°C. Storage Temperature: -40 to +70°C. Humidity Range: 95 % R.H., 0 to 40°C.

Weight: Net 7.9 kg (17.44 lbs), shipping 8.9 kg (19.63 lbs).

Dimensions: 133 mm high, 426 mm wide, 345 mm deep

 $(5.2 \times 16.75 \times 13.6 \text{ in}).$ 

**OPTIONS** 

Option 907 Front Handle Kit
Option 908 Rack Flange Kit
Option 909 Rack Flange plus Front

Handle Kit

Option 910 Additional Instrument Manual

Specifications describe the instrument's warranted performance. Supplement characteristics — identified by the word "typical"—are intended to provide information useful in applying the instrument by giving typical, but non-warranted, performance parameters.

Data subject to change.

For more information, call your local HP Sales Office or East (301) 948-63 70 : Midwest (312) 255-98 00 · South (404) 955-15 00 · West (213) 877-12 82. Or write: Hewlett-Packard, 1501 Page Mill Road, Palo Alto, California 94304. In Europe: Hewlett-Packard SA, P. O. Box, CH 1217 Meyrin 2, Geneva, Switzerland. In Japan: Yokogawa-Hewlett-Packard Ltd.,29-21 Takaido-Higashi 3-chome, Suginami-ku, Tokyo 168.

OPERATING AND SERVICE MANUAL

# 8082A PULSE GENERATOR

# SERIAL NUMBERS

This manual applies directly to instrument with serial number 1822G02846 and higher. Any change made in instruments having serial numbers higher than the above number will be found in a "Manual Changes" supplement supplied with this manual. Be sure to examine the supplement for changes which apply to your instrument and record these changes in the manual. Backdating information for instruments with lower serial numbers can be found in Section 7 (yellow pages).

c HEWLETT-PACKARD GMBH 1983 HERRENBERGER STR. 110, D-7030 BOBLINGEN FEDERAL REPUBLIC OF GERMANY

MANUAL PART No. 08082-90003 MICROFICHE PART No. 08082-90503 PRINTED: SEP 1983

Printed in the Federal Republic of Germany

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# SAFETY SUMMARY

The following general safety precautions must be observed during all phases of operation; service, and repair of this instrument. Failure to comply with these precautions or with specific warnings essentially in this manuar violates parely startiants or design, manufacture, and thinded use of the instrument. Hewlett-Packard Companies with the product of the contribute to comply with these requirements.

GENERAL — This is a Safety Class I instrument (provided with terminal of Buyes tive tarthing) and has been manufactured and tested according to international safety stabilized.

OPERATION - BEFORE APPLYING POWER comply with the installation section. Additionally the following shall be observed:

Do not remove institutent covers when operating

Before the instrument is switched on, all protective earth terminals, extension cords, auto-transformers and devices connected to it should be connected to a protective earth yia a ground socket. Any interruption of this protective earth grounding will cause a potential shock hazard that could result in serious personal injury.

Whenever it is likely that the protection has been impaired, the instrument must be made inoperative and be secured against any unintended operation.

Make sure that only fuses with the required rated current and of the specified type (normal blow, time delay, etc.) are used for replacement. The use of repaired fuses and the short-circuiting of fuseholders must be avoided.

Adjustments described in the manual are performed with power supplied to the instrument while protective covers are removed. Energy available at many points may, it contacted result in personal injury.

Any adjustment, maintenance, and repair of the opened instrument under voltage should be avoided as much as possible, and when inevitable, should be carried out only by a skilled person who is aware of the hazard involved. Do not attempt internal service or adjustment unless another person, capable of rendering first aid and resuscitation is present. Do not replace components with power cable connected.

Do not operate the instrument of the presence of flammable asset de innes operation of any encounter in such an environment constitutes a definite safety hazard.

Do not install substitute parts of perform any unauthorized modification to the machine in the property of the machine in the control of the machine in the control of the

Capacitors inside the instrument may still be charged even if the instrument has been disconnected from its source of supply.

# SAFETY SYMBOLS

The apparatus will be marked with this symbol when it is necessary for the user to refer to the instruction manual in order to protect the apparatus against deniage.

indicates dangerous voltages.

Earth terminal:

#### WARNING

The WARNING sign denotes a hazard. It calls attention to a procedure, practice or the like, which, if not correctly performed or adhered to, could result in injury or loss of life. Do not proceed beyond WARNING sign until the indicated conditions are fully understood and met.

#### CAUTION

The CAUTION sign denotes a hazard. It calls attention to an operating procedure, practice, or the like, which, if not correctly performed or adhered to, could result in damage to or destruction of part or all of the equipment. Do not proceed beyond a CAUTION sign until the indicated conditions are fully understood and met.



Dangerous voltages, capable of causing serious personal injury, are present in this instrument. Use extreme caution when handling, testing, and adjusting.

1		SECTION 1
1		
1		
1		
ı		
ı		
ı		
ı	CENEDAL	INICODALATION
•	GENERAL	INFORMATION—

# 1-1 INTRODUCTION

- 1-2 The 8082A is a 250 MHz dual channel pulse source with variable leading and trailing edge transition times as fast as 1ns. It also has variable pulse frequency, delay, width, offset and amplitude. The normal/complement relationship and the polarity of either output can be reversed. Single pulse, double pulse and square wave operation are available. There are also four trigger modes:
- 1-3 Normal Mode. In this mode the 8082A operates as a self-contained pulse source with full control of the pulse parameters from the front panel controls.
- 1-4 Ext Trig Mode. In this mode the pulse and trigger output frequencies are determined by the frequency of an externally applied signal. The other pulse parameters are varied from the front panel controls.

- 1-5 Gate Mode. In this mode a gating signal enables the pulse and trigger outputs.
- 1-6 External Width Mode. In this mode the pulse frequency and width are determined by the frequency and width of an externally applied signal. The delay between input and output is fixed. The trigger output is the shaped trigger input signal.

# 1-7 ECL OUTPUT

1–8 The 8082A has an ECL position on each of its amplitude range switches. When either or both of the switches are set to this position, both 8082A outputs automatically deliver a fixed voltage swing of -0.9V to -1.7V typical (into an open circuit) for driving ECL logic.

Table 1-1 Specifications

These specifications apply when:

1) both outputs are terminated by a 50- $\Omega$  load,

# PULSE CHARACTERISTICS (Source and load impedance $50\Omega$ )

**Transition Times:** ≤ 1ns to 0.5ms in 6 ranges. First range from ≤ 1ns to 5ns controls leading and trailing edges simultaneoulsy. For all other ranges transition times variable independently up to 1:10.

Difference between risetime and falltime is less than 25% of the faster transition time of the two.

Overshoot and Ringing:  $\leq \pm 5\%$  of pulse amplitude may increase to  $\pm 10\%$  with amplitude vernier CCW.

**Preshoot:**  $\leq \pm 5\%$  of pulse amplitude.

**Linearity:** Linearity aberration for both slopes  $\leq$  5% for transition times > 5ns.

**Output:** Maximum amplitude is 5V from  $50\Omega$  into  $50\Omega$ . Maximum output voltage is  $\pm$  5V (amplitude + offset).

Offset:  $> \pm 2V$ , into  $50\Omega$ 

**Baseline:** OV ± 150mV with offset switched off and amplitude range set to maximum. Other amplitude ranges reduce baseline proportionately.

**DC-Source Impedance:**  $50\Omega \pm 5\%$ . **Reflection Coefficient:** Reflection is 2% typical for steps with 1ns rise time applied to output connector on all amplitude ranges except 5V range. On the 5V range, the reflection may be 15%.

**Output protection:** Cannot be damaged by open or short circuits or application of ext  $\leq \pm$  6 volts or  $\pm$  200mA independent of control settings.

**Attenuator:** Two separate three stepattenuators reduce the outputs to 1V. Vernier is common for both outputs and reduces the output to 0.4V minimum. A further position provides ECL-compatible outputs (-0.9V) to -1.7V typ. open circuit).

#### **TIMING**

**Repetition Rate:** > 250 MHz to < 1 kHz in 6 ranges.

**Period Jitter:** < 0.1% + 50ps

Delay: < 2ns to > 0.5ms in 6 ranges plus typ. 18ns fxd. with respect to trigger output.

Delay Jitter: < 0.1% + 50ps

**Double Pulse:** Up to 125 MHz max. (simulates 250MHz). Min pulse spacing ≥ 4ns.

Delay Duty Cycle: > 50%

**Pulse Width:** < 2ns to > 0.5ms in 6 ranges.

Width Jitter: < 0.1% + 50ps

Width Duty Cycle: > 50%

**Square Wave:** A further position of the Pulse Width switch provides Square Wave output. (Delay and double pulse are disabled, max. Rep. Rate 250 MHz). Duty cycle is  $50\% \pm 10\%$  up to 100 MHz,  $50\% \pm 15\%$  for > 100 MHz.

**Trigger Output:** Negative going Square Wave (50% duty cycle typ.) > 500 mV from  $50\Omega$  into  $50\Omega$ . Internal  $50\Omega$  load can be switched off by slide-switch on PC-board. Amplitude increases to  $\geq 1 \text{V}$  into  $50\Omega$  up to 200 MHz.

**Trigger Output Protection:** Cannot be damaged by short circuit or application of external ± 200mA.

# EXTERNALLY CONTROLLED OPERATION

**External Input** 

Input Impedance:  $50\Omega \pm 10\%$ . DC

coupled.

Maximum Input: ± 6V

**Trigger Level:** Adjustable -1.5V to +1.5V.

2) the internal 50- $\Omega$  source impedance is selected.

**Slope Control:** Positive, negative or manual selectable. In the MAN-position all ext. functions can be controlled by push button. Button pushed in simulates an "on-signal".

**Sensitivity:** Sine-wave > 200 mVpp,

pulses > 200 mV.

Repetition Rate: 0 to > 250 MHz.

### **Ext.-Controlled Modes**

**Ext. Trigger:** There are approximately 7ns delay between the external input and the trigger output. Rep.-Rate is ext. controlled (is triggered by external signal). Trigger output provides the pulse-shaped input signal. Square wave mode is disabled.

**Synchronous Gating:** Gating signal turns rep. rate generator on. Last pulse is of normal width even if gate ends during the pulse.

**External Width:** Output pulse width determined by width of drive input. Rep. Rate and Delay are disabled. Trigger output provides shaped input signal.

#### **OPTIONS**

Option 907 Front Handle Kit
Option 908 Rack Flange Kit
Option 909 Rack Flange plus Front
Handle Kit
Option 910 Additional Instrument

Manual

# GENERAL

**Power Requirements:** 100V, 120V, 220V, 240V (+5%, -10%) 48 - 440 Hz. Power consumption 85VA max.

**Weight:** Net 7.9 kg (17.44 lbs), shipping 8.9 kg (19.63 lbs).

**Dimensions:** 426mm wide, 145mm high, 380mm deep (16 3/4 ins. x 5 11/16 ins. x 15 ins.).

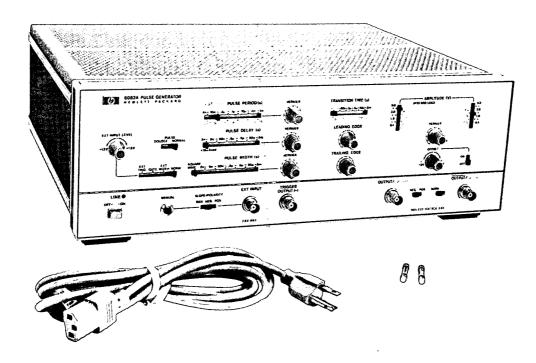


Figure 2-1. 8082A and Supplied Accessories

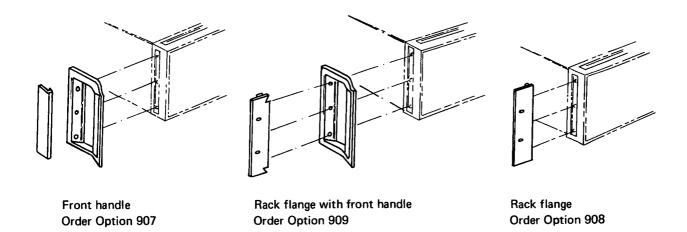


Figure 2-2. Available Accessories

INSTALLATION-

# 2-1 GENERAL

# 2-2 Initial Inspection

2-3 Inspect the instrument and accessories for physical damage, and if damage is evident, refer to paragraph 2-19 for the recommended claim procedure and repacking information.

# 2-4 Accessories

2-5 The following accessories are supplied with the standard instrument (Figure 2-1):

HP Part Number
1A fuse (for 220/240V operation)
2A fuse (for 110/120V operation)
2110-0202
Power cord
Operating and Service Manual

HP Part Number
2110-0207
22110-0202
see Figure 2-3

For an additional manual, order option 910.

Handles are rack mounting flanges are delivered with the instrument only if the appropriate option (Figure 2–2) is ordered.

# 2-6 Power Cords

2–7 The instrument is supplied with one of the power cords shown in Figure 2–3.

# 2-8 INSTALLATION

# 2-9 Power Cord

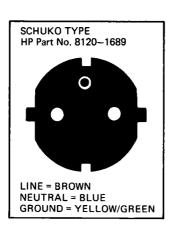
2–10 The 3-wire power cable supplied with the 8016A, when connected to the appropriate power outlet, grounds the instrument cabinet and panels. To preserve this safety feature when operating the instrument from an outlet without a ground connection, use an appropriate adapter and connect the ground lead (green/yellow) to an external ground.

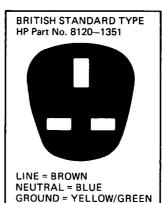
2-11 If the plug on the cable does not fit your power outlet, then cut the cable at the plug end and connect a suitable plug. The plug should meet local safety requirement and include the following features:

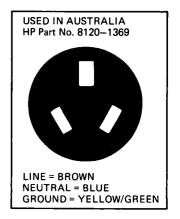
- a. Minimum current rating of 2A
- b. Ground connection
- c. Cable clamp

The colour coding used in the cable will depend on the cable supplied (see Figure 2–3).









# **WARNING**

To avoid the possibility of injury or death, the following precautions must be followed before the instrument is switched on:

- a. If this instrument is to be energized via an autotransformer for voltage reduction, make sure that the ground connection is not interrupted.
- b. The power cable plug shall only be inserted into a socket outlet provided with a protective earth contact. The protective action must not be negated by the use of an extension cord without a protective conductor (grounding).
- c. The safety check (Table 5–27) shall be executed before connecting the instrument to the supply.

# 2-12 Power Source requirements

2-13 The instrument will operate from nominal ac line supplies of 100V, 120V, 220V or 240V (-10%, + 5%) at 48 Hz to 66 Hz. Two switches on the rear panel allow one of the four voltages to be selected.

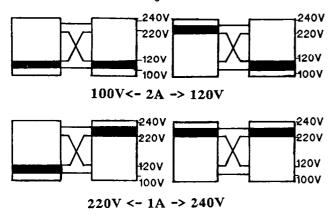


Figure 2–4. Switch Settings for the various Norminal Powerline Voltages

# CAUTION-

Before applying power to the instrument, check on the rear panel that the switch is set in accordance with local supply conditions.

- 2-14 To check the power requirements proceed as follows:
  - a. Remove the fuse and check its value: for 220V/240V operation 1A for 100V/120V option 2A
  - b. Check that the line selector switch positions corresponds to the local supply voltage. If they do not correspond use a screwdriver to change the switch positions.
  - c. Insert the correct fuse into the fuseholder.
  - d. Connect the power cable to the rear connector.

# 2-15 Temperature Requirements

2–16 The instrument operates within specifications when the ambient temperature is between  $0^{\circ}$ C (32°F) and  $50^{\circ}$ C (122°F). The word generator may be stored between  $-40^{\circ}$ C ( $-40^{\circ}$ F) and  $75^{\circ}$  ( $167^{\circ}$ F).

# 2-17 RACK MOUNTING

2—18 Figure 2—2 shows the possible handle/rack-mounting configurations. If handles are fitted and subsequently need to be removed, the plastic trim must first be taken off as shown in Figure 2—5.

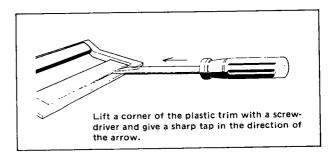


Figure 2-5. Removing Plastic Trim

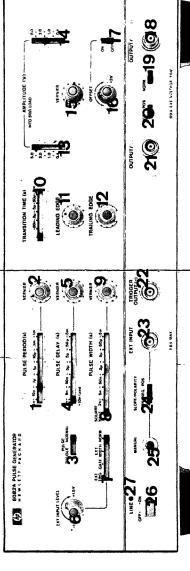
# -19 CLAIMS AND REPACKAGING

# 2-20 Claims for Damage

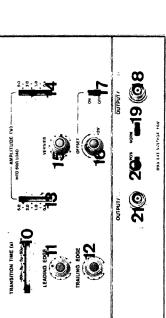
2–21 If physical damage is evident or if the instrument does not meet specification when received, notify the carrier and the nearest Hewlett-Packard Sales/Service Office. The Sales/Service Office will arrange for repair or replacement of the unit without waiting for settlement of the claim against the carrier.

# 2–22 Repackaging for Shipment and Storage

2–23 If the instrument is to be shipped to a Hewlett-Packard Sales/Service Office, attach a tag showing owner, address, model and serial number, and the repair required. The original shipping carton and packaging material may be re-usable but the Hewlett-Packard Sales/Service Office will also provide information and recommendations on materials to be used if the original packing is not available or re-usable.

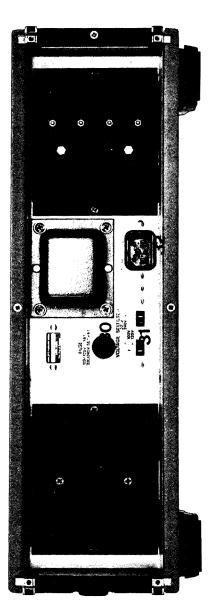


- RATE switch: for selecting the range of the pulse rate.
- 2 Rate VERNIER: for continuous adjustment of the repetition rate within the range selected on the RATE switch. Clockwise rotation increases the pulse period (reduces the rate).
- position the 8082A delivers two pulses for every trigger pulse one pulse in phase with the trigger output and one delayed by the amount set on the PULSE DELAY controls. DOUBLE PULSE is not available one pulse which is delayed on the trigger pulse by the amount set on the PULSE DELAY controls. in the EXT WIDTH mode and is automatically inhibited if selected. In the NORMAL position, for each trigger pulse, the 8082A delivers PULSE DOUBLE/NORMAL switch: in the DOUBLE PULSE
- 4 PULSE DELAY switch: for selecting the range of the pulse delay with respect to the trigger output in NORM, GATE and EXT TRIG modes. Has no effect in the EXT WIDTH and SQUARE WAVE
- 5 Pulse delay VERNIER: for continuous adjustment of the pulse delay within the range selected on the PULSE DELAY switch. Clockwise rotation increases the delay.
- EXT INPUT LEVEL control: defines the threshold level of the EXTERNAL INPUT over a range -1.5V to +1.5V
- 7 Mode switch: selects either the internal (NORM) mode or one of three external modes (EXT WIDTH, GATE or EXT TRIG).
- frequency of the square wave depends on the PULSE PERIOD setting. PULSE WIDTH switch: selects the range of the pulse width required in all modes except EXT WIDTH. When SQUARE WAVE is selected a square wave output of 50% duty cycle is produced. The
- Pulse width VERNIER: for continuous adjustment of the pulse width within the range set on the PULSE WIDTH switch.
- TRANSITION TIME switch: for selecting the range of leading 10 TRANSITION 11ME switch. and trailing edge transition times.
- leading edge transition time within the range selected on the TRANSI-TION TIME switch. On the fastest range this vernier controls both 11 LEADING EDGE vernier: for continuous adjustment of the leading and trailing edges.
- trailing edge transition time within the range selected on the TRAN-12 TRAILING EDGE vernier: for continuous adjustment of the SITION TIME switch.
- 13 AMPLITUDE switch: for selecting the range of the output pulse amplitude available at the OUTPUT / <u>OUTPUT</u> connector. In the ECL position the OUTPUT / <u>OUTPUT</u> connector delivers pulses of
- Figure 3-1. Controls and Connectors



amplitude (-0.9V to -1.7V into an open circuit) and the amplitude vernier and the offset control are disabled fixed

- fixed amplitude (-0.9V to -1.7V into an open circuit) and the ampliposition the OUTPUT / OUTPUT connector delivers pulses of 4 AMPLITUDE switch: for selecting range of the output pulse mplitude available at the OUTPUT / OUTPUT connector. In the ude vernier and the offset control are disabled 딦
- Amplitude VERNIER: for continuous adjustment of pulse amolitude from both pulse outputs simultaneously within the ranges et on the AMPLITUDE switches. ō
- OFFSET vernier: for adjustment of the baseline of both output sulses simultaneously over the range -2V to +2V. 9
- OFFSET switch: for enabling/disabling the OFFSET vernier. in the OFF position the baseline of both outputs is zero volts.
- OUTPUT / OUTPUT connector: BNC connector. 8
- outputs, what was the normal output becomes the complement and NORM / COMPL switch: reverses the duty cycle of the two ice versa. 6
- NEG/POS switch: determines the polarity of both output pulses ഉ
- OUTPUT / OUTPUT connector: BNC connector.
- regative square wave at a rate determined by the setting of the pulse shaped version of the trigger input. In GATE mode it will deliver pulses at the rate set on the pulse period controls for as long as the period controls. Pulse delay is refered to the negative going edge of the trigger. In EXT TRIG and EXT WIDTH modes it will deliver a TRIGGER OUTPUT (-) connector: BNC connector, supplies gate is open. 2
- EXT INPUT connector: BNC connector to which trigger pulses are applied in the EXT TRIG, GATE and EXT WIDTH modes. 23
- or falling (NEG) signal will trigger or gate the external input on. MAN SLOPE / POLARITY switch determines whether a rising (POS) position means that the external signal can be simulated by pressing the MANUAL button. 24
- 25 MANUAL button provides a means of initiating a single pulse (EXT TRIG mode) each time the button is pressed, a train of pulses (GATE mode) while the button is pressed, or a pulse whose width s equal to the time the button is pressed (EXT WIDTH mode).
- LINE ON/OFF switch: press-for-on, press-for-off switch. 2
- LINE lamp: glows when LINE ON/OFF switch is ON.



REAR PANEL

Fuse 32 32

Line voltage selector

Line connector

See Section 2

INTERNAL (See Figure 6-2)

A3S1 Trigger output 50 $\Omega$  internal load on/off,

**OPERATING INSTRUCTIONS** 

## 3-1 GENERAL

3–2 This section is divided into two parts. The first part gives some general notes on the operation of the 8082A together with operating instructions for each of the four operating modes:

NORM operating mode EXT WIDTH operating mode GATE operating mode EXT TRIG operating mode

Full setting-up instructions are given for Normal mode followed by any changes in control settings required for the other three modes. Stylized waveforms are given for each mode to show the resultant pulse shapes. For ease of operation the instructions will refer to Figure 3–1 which shows the controls identified by a reference number in a circle. The same reference numbers are used in the text when each control is mentioned.

3-3 The second part of this section gives applications information.

# 3-4 EXTERNAL INPUT CHARACTERISTICS

3–5 The SLOPE/POLARITY switch determines whether a rising (POS) or falling (NEG) signal will trigger or gate the external input on. Figure 3–2 shows the effects of these controls in the External Width mode.

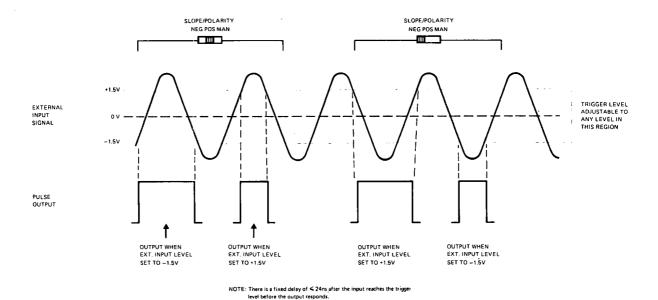


Figure 3-2. Effect of External Input Controls

- 3-6 Any external input pulses must have an amplitude of at least 200mV peak-to-peak and must be at least 2ns wide at the level at which triggering is to occur.
- 3–7 If the SLOPE/POLARITY switch is set to MAN, the external signal can be simulated by pressing the MANUAL pushbutton. This button provides a means of initiating a single pulse (EXT TRIG mode) each time the button is pressed, a train of pulses (GATE mode) while the button is pressed, or a pulse whose width is equal to the time the button is pressed (EXT WIDTH mode).

# 3-8 SQUARE WAVE OPERATION

3–9 There is a Square Wave facility on the 8082A which produces a square wave output of 50% duty cycle in NORMAL mode. If Square Wave is selected in External Trigger or External Width modes, the output is a pulse shaped version of the trigger input (the output waveforms are the same as for External Width mode, see Figure 3–4). If Square Wave is selected in Gate mode, the output is a gated square wave, the repetition rate of which is set up on the pulse period controls.

## 3-10 OUTPUT AMPLITUDE CONTROLS

# 3-11 Vernier

3–12 Because the amplitude vernier is common to both outputs, the amplitude relationship of one output to the other is 1:1, 1:2 or 1:5.

# 3-13 ECL Outputs

3–14 To obtain normal and complement ECL compatible pulses from the two outputs, either one or both amplitude range switches should be set to the ECL position. The ECL levels supplied are -0.9V to -1.7V into an open circuit, i.e. without an external 50 ohm load. These output levels can be altered by changing the values of resistors R 5 (ECL amplitude) and R60 (ECL-DC offset) on board A5 (Amplitude Vernier and DC Offset board).

# 3-15 INCOMPATIBLE CONTROL SETTINGS

3–16 When operating the 8082A, the layout of the Pulse Period, Delay and Width controls helps to avoid incompatible settings as shown in Figure 3–3. Generally, the Pulse Period control should be farthest right but the controls can all be in a straight vertical line if the Pulse Period vernier is more clockwise than the other two verniers.

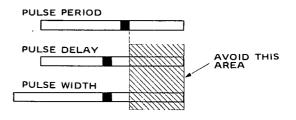
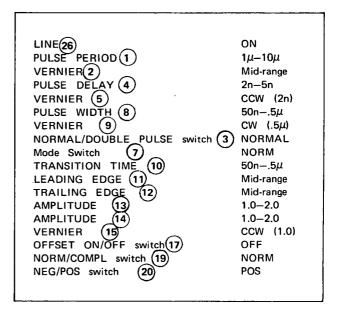


Figure 3-3. Positioning of Controls

# 3-17 NORM OPERATING MODE

- 3–18 In this mode the 8082A requires no external trigger signal to produce an output. Pulse rate, width, delay, transition times, amplitude and offset are all adjusted by the front panel controls.
- 3–19 The initial settings (listed below) are given to obtain a normal pulse waveform (Figure 3–4) for someone unfamiliar with the operation of the 8082A. Both pulse outputs and the trigger output should be connected to a high-frequency oscilloscope using a 50 ohm system. The oscilloscope (an HP 180C mainframe with 1810A plug-in or similar 1 GHz bandwidth sampling oscilloscope) should be set with the sweep time at 0.5µs/cm and with the sensitivity at 200mV/cm.



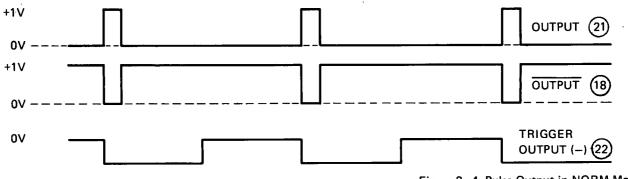


Figure 3-4. Pulse Output in NORM Mode

# 3-20 EXT WIDTH OPERATING MODE

3–21 In External Width mode, the pulse repetition rate and width are determined by the repetition rate and width (at the threshold set by the EXT INPUT LEVEL control) of an externally applied signal. In EXT WIDTH mode the PULSE PERIOD controls, the PULSE DELAY controls, the PULSE WIDTH controls and the DOUBLE/NORMAL PULSE switch have no effect on the pulse output. To obtain an output similar to that in Figure 3–5, adjust the controls as shown below. It is assumed that the controls are already set-up as described above for a Normal pulse; therefore only the alterations to these control settings will be given.

- a. Set the Mode switch (7) to EXT WIDTH.
- b. Apply an external trigger to the EXT INPUT (23). The input should have the following characteristics:
  Pulse shape sine or square wave
  Amplitude between 200mV and 6V
  Frequency 14kHz
- c. Set the EXT INPUT LEVEL 6 control as required to vary the switching threshold.
- d. Set the SLOPE/POLARITY switch as required to trigger off the rising (POS) or falling (NEG) edge of the trigger.

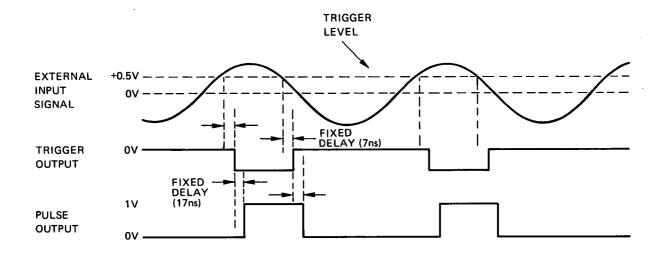


Figure 3-5. Pulse Output in External Width Mode

# 3–22 GATE OPERATING MODE

3–23 In Gate mode the repetition rate is defined by the rate controls but no output occurs until the voltage of an externally applied signal rises above (SLOPE/POLARITY switch set to POS) or falls below (SLOPE/POLARITY switch set to NEG) the level set on the EXT INPUT LEVEL control. The last pulse of a 'burst' is always of correct width even if the gate closes during

the pulse. To obtain an output similar to that in Figure 3–6, adjust the controls as shown below. It is assumed that the controls are already set-up as described above for a pulse in External Width mode; therefore only the alterations to these controls settings will be given. Switching to External Width mode when in Gate mode can be used to check for correct functioning of the gate signal.

a. Set the Mode switch (7) to GATE.

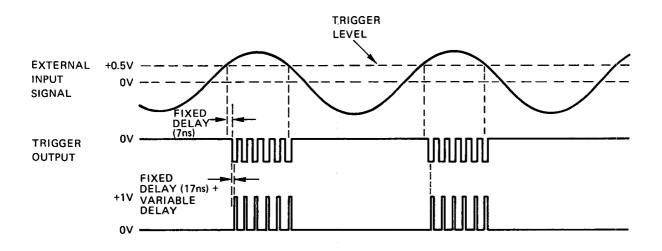


Figure 3-6. Pulse Output in Gate Mode ..

# 3-24 EXT TRIG OPERATING MODE

3–25 In External Trigger mode the pulse and trigger repetition rates are determined by the repetition rate of an externally applied signal. To obtain an output similar to that in Figure 3–7, adjust the controls as shown

below. It is assumed that the controls are already set-up as described above for a pulse in Gate mode; therefore only the alterations to these control settings will be given.

a. Set the Mode switch (7) to EXT TRIG.

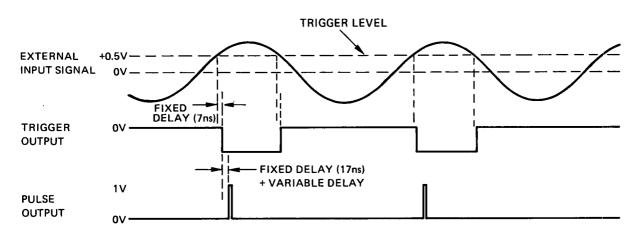


Figure 3-7. Pulse Output in External Trigger Mode

# 3-26 APPLICATIONS NOTES

3–27 The following section indicates some applications of the 8082A.

# 3—28 Digital Applications

3–29 The 8082A can be used to test the following digital integrated circuit (IC) logic families at their normal operating speeds:

Propagation delay per gate

 RTL
 12ns - 27ns

 DTL
 30ns

 TTL
 12ns

 Schottky TTL
 3ns

 ECL (including MECL III) 1ns - 4ns

For convenience of operation a special ECL output is available on the 8082A. This means that by simply setting either amplitude range switch to the ECL position, an output pulse width a voltage swing of -0.9V to -1.7V is produced into an open circuit.

When using the 8082A to test any of the above logic families, particularly the fast MECL III logic, it is important to operate with a 50 ohm transmission system. The coaxial cable does not need to be terminated at the IC and by a 50 ohm resistor; the internal 50 ohm termination of the 8082A is of sufficiently high quality to provide a clean pulse shape in almost all cases (see paragraph 3-31) without an external termination, even at the fastest transition times. This has the advantage that it enables the 50 ohm coaxial cable to be soldered directly to the pins of the IC under test without requiring a 50 ohm terminating resistor. It should be noted, however, that when no external termination is used, no connections can be made at any intermediate point along the transmission cable. For example, suppose the pulse on leaving the 8082A has 2V amplitude across an effective 25 ohms (50 ohm internal termination in parallel with 50 ohm cable); when the pulse reaches the IC its amplitude is doubled to 4V (open end reflection). This 4V is reflected back along the cable and is absorbed by the 50 ohm termination in the 8082A with only 2% typical reflection at amplitudes up to 4V. The effect of this action is to produce the stepped pulse shown in figure 3-8 at any intermediate point along the cable.

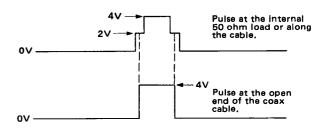


Figure 3-8. Stepped pulse with high-Z output

- 3-30 If a detailed analysis of IC waveshapes and timings is to be made, a 1 GHz sampling oscilloscope with a high impedance input probe should be used. The probe should be connected at the IC pin and not at any intermediate point along the 50 ohm cable.
- 3-31 If a number of IC's on one PC board are being driven from one point on the board and the printed circuit track is more than 10cm long, then an external 50 ohm resistor at the end of the 50 ohm system may be required to preserve the clean pulse shape at the IC input pins.
- 3–32 One point to remember, particularly when testing 1ns ECL, is the loss of edge speed due to the coaxial cable. However, the 8082A is fast enough to accommodate this edge speed degradation without exceeding the manufacturers specification. A 1.23 metre cable is available as HP Accessory number 10503A.
- When testing flip-flops (Motorola MC1666 for example), two pulse generators are required, one to provide the clock input and one to provide the data input. One pulse generator is run in square wave mode and the other is run in external trigger and double pulse mode and is synchronized from the trigger output of the first pulse generator (Figure 3-9). Allowance must be made for the differential delay that will occur between the two outputs. This is caused by the fact that there is an extra 7ns delay in the second 8082A (24ns against 17ns) due to the delay between the trigger input and the trigger output. To preserve the correct timing relationship, therefore, between the two sets of pulse outputs, the data pulse output must be delayed by a further 7ns. This can be achieved by increasing the length of the data output transmission cable (delay is about 5ns per metre).

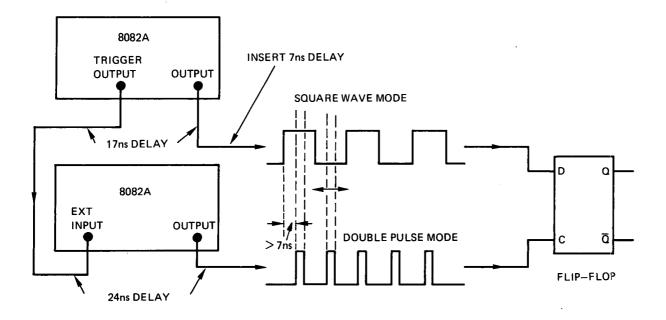


Figure 3-9. Flip-Flop Test Circuit

3–34 The minimum set-up time required for switching the flip-flop from '0' to '1' (or vice versa) can be measured as shown in Figure 3–10.

The pulse delay controls of the clock output are slowly decreased and because the output is in double pulse form, only the second pulse in each case advances to-

wards the leading edge of its data input (in this case a '1'). The minimum set-up time is found when the flip-flop ceases to switch properly from '0' to '1'. The minimum set-up time for switching from '1' to '0' can then be found by switching to the complement of the data input and repeating the exercise.

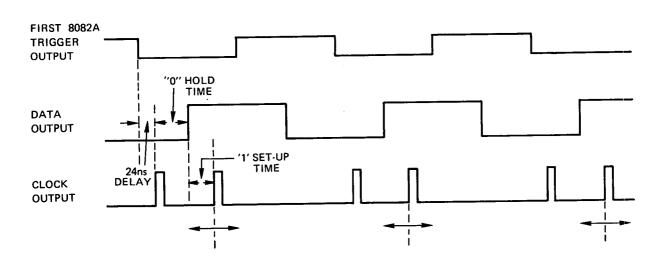


Figure 3-10. Flip-Flop Test Waveforms

- 3-35 The clock pulse transition times can be adjusted to observe the variation in the propagation delay of the flip-flop or to simulate edge degradation caused by a high fanout of the clock pulse line.
- 3-36 The 8082A can be used as a pulse shaper. When set to external width mode, an external signal (the output of a word generator for example) connected to the trigger input is available in pulse shaped form at the pulse output. Adjusting the trigger level control to the appropriate level helps to recover the shape of even badly distorted pulses.
- 3-37 The 8082A can also be used to generate noise pulses; the pulse width is set to minimum and the amplitude to 5V and then the transition times are increased. This has the effect of reducing the pulse amplitude and, in fact, the transition times can be increased until a spike of approximately 1ns width and 800mV amplitude (ECL amplitude) is produced (see Figure 3-11).

This can be set to the required dc level using the offset controls and connected to the logic circuit under

test to simulate noise. The amplitude and offset of the noise spike can be varied and their effect on the circuit monitored.



Figure 3-11. Noise Pulses

# 3-38 Analog Applications

3–39 The 8082A can also be used effectively in analog applications. Twisted pairs of transmission lines and differential amplifiers can be tested using the normal and complement outputs; the common amplitude vernier is very useful in this application for varying the amplitude of both outputs simultaneously. Trigger levels of Schmitt trigger circuits can be tested using output pulses with very slow transition times (as slow as 0.5ms).

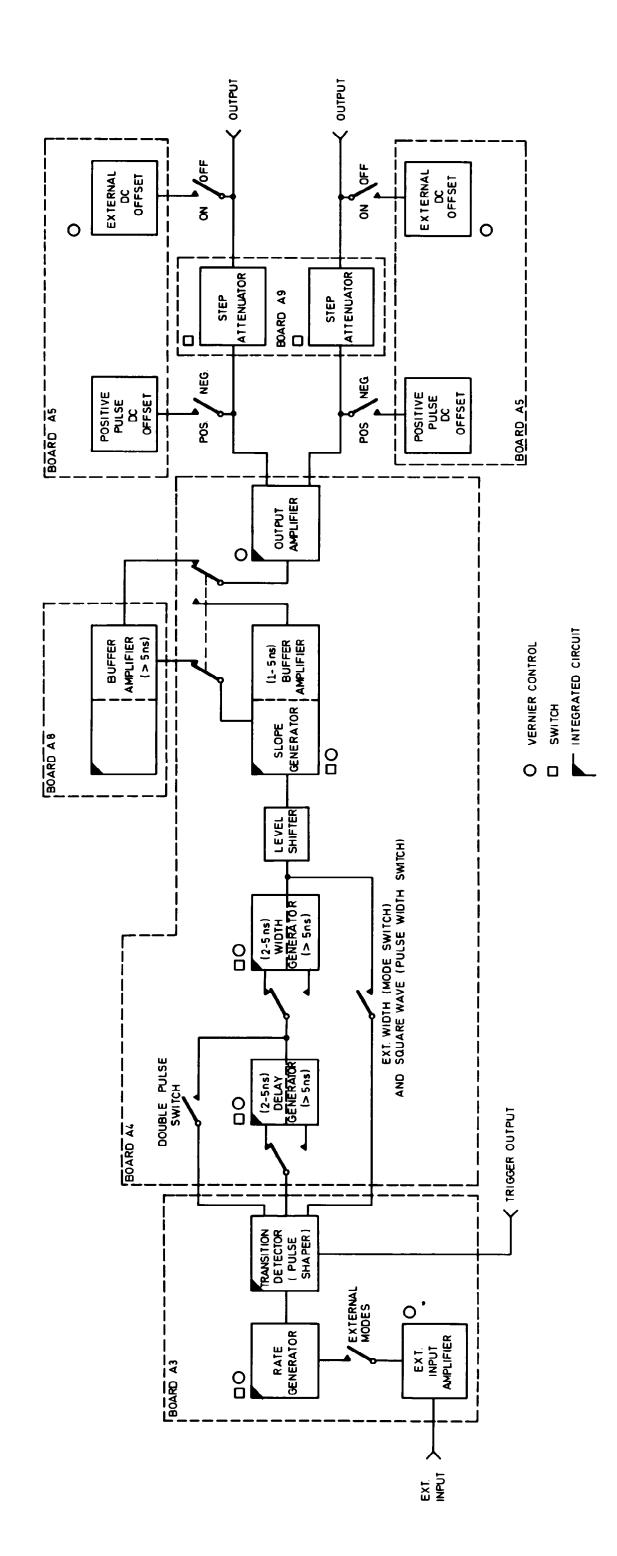


Figure 4-1, 8082A Pulse Generator - Block Diagram

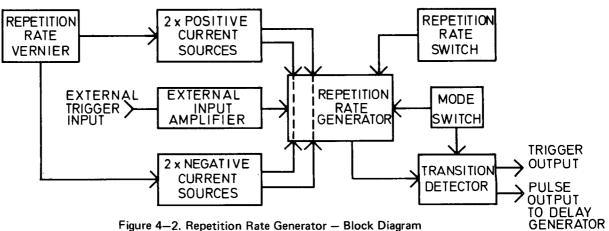
#### 4-1 INTRODUCTION

- 4-2 A basic block diagram of the 8082A is shown in figure 4-1 and this should be referred to when reading the following description. The pulse repetition rate is generated either internally by the Rate Generator or externally by an external input signal, depending on the mode of operation. The square wave output of the Rate Generator is input to the Transition Detector were it is pulse shaped to produce a train of 2ns wide spikes. These pulse spikes are then applied to the Delay and Width Generators. Each of these generators is divided into two parts for periods of 1-5ns or > 5ns and the incoming signal is routed into the appropriate part. The Output of the Width Generator is level shifted and input to the Slope Generator where the slope of the leading and trailing pulse edges is made variable (variable transition times).
- 4-3 If Double Pulse mode is selected, both the delayed and undelayed waveforms are input to the Width Generator. If External Width mode is selected, both the Delay and Width Generators are by-passed and the output of the Transition Detector is input to the level shifter.
- The output of the Slope Generator is input to one of two Buffer Amplifiers dependent on the transition time setting (1-5ns or > 5ns). Normal and complement outputs from the Buffer Amplifier are then input to the dual channel Output Amplifier. Here the amplitude variation within ranges is added in the form of an Amplifier Vernier, which is common to both channels.

- 4-5 The positive pulse DC Offset circuits are responsible for shifting the voltage level of both channels from a negative level to a positive level, using the NEG/ POS switch, if positive output pulses are required (the normal/complement relationship of the two channels is also automatically reversed when this action is performed) This means that either negative normal pulses or positive normal pulses are available from one output connector.
- After being set to the correct polarity, the amplitude of both signals is set to the appropriate range using an active Step Attenuator circuit.
- 4-7 Finally the External DC Offset circuit provides an offset voltage to shift the baseline of both output signals together over the range -2V to +2V if required. This circuit can be switched off in which case both signal baselines are at OV.

#### 4-8 REPETITION RATE GENERATOR

4-9 The function of the repetition rate generator is to provide a train of pulses, approximately 2ns wide, for the delay generator or a 50% duty cycle waveform if square wave is selected; also to provide a train of 50% duty cycle square wave pulses for the output trigger. A block diagram of the unit is given in Figure 4-2 and a schematic diagram in Service Sheet 1.



# 4-10 External Input Amplifier

4—11 In any mode except NORMAL mode, the output of the rate generator is controlled, either gated or triggered, by the external input amplifier. The amplitude of the external input signal is limited by a bridge circuit to approximately ± 2V. The signal then enters one side of a differential amplifier, the reference voltage on the other side of which is determined by the setting of the EXT. TRIG LEVEL control. Thus the threshold level of the input signal, i.e. the voltage level at which gating or triggering occurs, can be varied. The SLOPE/POLARITY switch determines whether a rising (POS) or falling (NEG)

input signal will cause triggering or gating. If set to MAN, the switch disables the external input signal and enables the MANUAL button so that pressing the button simulates one pulse from the external input.

# 4-12 Rate Generator

4–13 In the NORMAL mode, the output of the pulse generator is derived from the rate generator. The rate generator consists of a ramp generator which feeds a Schmitt trigger to produce a 50% duty cycle square wave output. A simplified diagram of the circuit is shown in Figure 4–3.

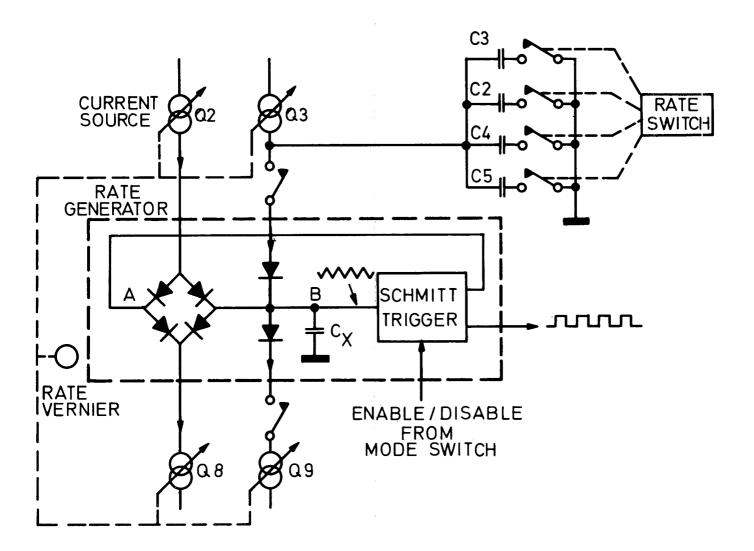


Figure 4-3. Rate Generator

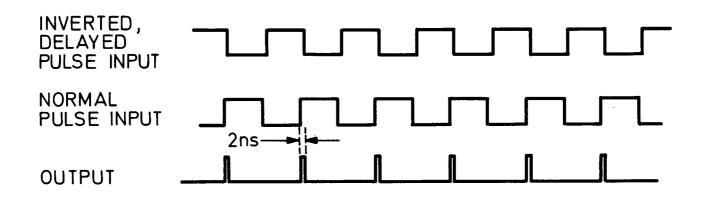


Figure 4-4. Transition Detector Pulse Output

- 4–14 Four current sources (Q2, Q3, Q8 and Q9) provide current for the rate generator; two of these sources (Q3 and Q9) are inhibited by logic switches (U6, Q4, Q5) when the repetition rate is set to the fastest range (100 250 MHz). Thus on the fastest range if capacitor  $C_X$  is discharged, point B is at a low level and the output of the Schmitt trigger is high. In this state current from source Q2 charges capacitor  $C_X$  and current from the Schmitt trigger flows into current sink Q8. When the charge on  $C_X$  has reached the threshold level of the Schmitt trigger, the output of the Schmitt trigger and hence the level at A goes to a low level. Current from Q2 now flows, via A back into the Schmitt trigger and  $C_X$  discharges into current sink Q8.
- 4–15 This action produces a triangular waveform at point B and a 50% duty cycle square wave at the Schmitt trigger output when both current sources  $\Omega 2 + \Omega 8$  are equal. When the rate switch is set to any of the lower repetition rate ranges, current sources  $\Omega 3$  and  $\Omega 9$  and the extra capacitors (C2 C5) are switched in. The circuit action is the same; the extra capacitors are required to provide the longer charge/discharge times. The repetition rate is adjusted within each range by the rate vernier, which adjusts the current from both current sources simultaneously.
- 4-16 The square wave output from the Schmitt trigger is used to drive the transition detector stage.

#### 4-17 Transition Detector

4–18 This circuit produces two outputs; a trigger output and a pulse output for the delay generator. The trigger output is merely an inversion of the input, i.e. a

negative 50% duty cycle square wave, and can be switched to either > 500mV or ≥ 1V amplitude. The pulse output is produced by inverting and delaying the pulse input (delay produced by 2ns fixed delay line) and then presenting this waveform, together with the normal pulse input, to an AND gate. The resultant waveform is as shown in Figure 4–4. The pulse spikes produced are of constant width regardless of repetition rate and are input to the Delay Generator. In Square Wave mode the Transition Detector has no effect on the signal, i.e. the square wave passes straight through.

#### 4-19 Repetition Rate Vernier

4–20 The repetition rate vernier produces a variable voltage (OV to 6.4V) into a differential amplifier (U5). The output of the differential amplifier drives a transistor Q1 which acts as a phase splitter for the two positive and two negative current sources.

# 4-21 DELAY AND WIDTH GENERATORS

- 4—22 The output of the Transition Detector is input to the Delay Generator integrated circuit (U2). The purpose of this circuit is to produce an output that is delayed on the input by the setting of the delay controls. The pulse width and shape remain unchanged. In double pulse mode, both the delayed and undelayed pulses are gated out to the Width Generator. The circuit is divided into two parts for delaying signals with different periods (2 to 5ns and > 5ns) and the input signal is input to the appropriate part.
- 4–23 Figure 4–5 is a simplified diagram of the Delay Generator and should be referred to when reading the following description.

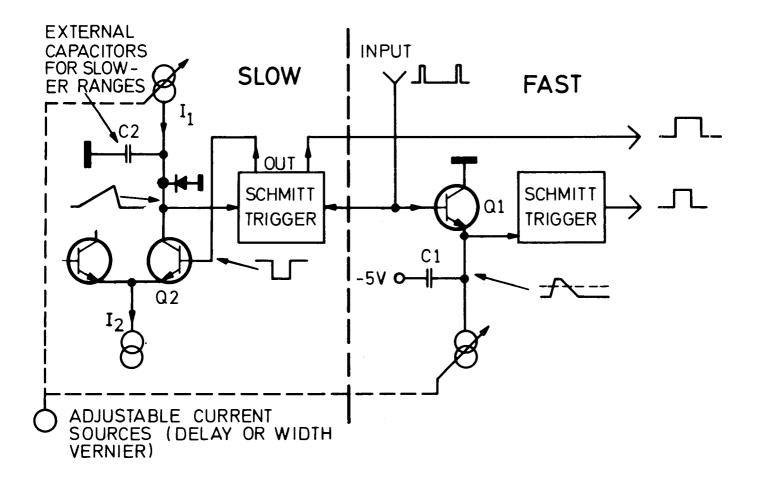


Figure 4-5. Delay Generator - Block Diagram

- 4–24 The input is common to both the slow and fast parts of the Delay Generator. If the delay range switch is set to 2–5ns, the fast section is used. In this case the 2ns wide pulse spike from the Transition Detector turns transistor Q1 on and rapidly charges internal capacitor C1. The Schmitt trigger turns on when its threshold level is reached. When the 2ns pulse goes low, transistor Q1 turns off and capacitor C1 discharges into the current sink. The rate of discharge is determined by the current setting (delay vernier). The Schmitt trigger turns off again when the voltage of C1 falls below the threshold level. Thus the output of the Schmitt trigger is a step wave of width dependent on the delay vernier setting.
- 4–25 If the delay range switch is set to any range > 5ns, the slow Delay Generator circuit is used. In this case, the 2ns wide pulse turns on the Schmitt trigger in the slow circuit and one of the outputs of this Schmitt trigger turns transistor Q2 off. Thus the current source connected to Q2 now starts to charge the external capacitor C2 (the value of this capacitor depends on the delay

range switch setting). The Schmitt trigger turns off again when the voltage on C2 has reached the threshold level. Therefore transistor Q2 turns on again and as current  $I_2$  is greater than  $I_1$ , capacitor C2 starts to discharge again. Thus the output of the Schmitt trigger is a square wave of width dependent on the delay range switch setting and the delay vernier setting.

- 4-26 The outputs of the two Schmitt triggers are OR'ed together and one of the two complementary outputs of the OR gate is passed through a 2ns delay line. The overall effect of the logic gating is to produce, at the Delay Generator output, a 2ns pulse that is delayed on the Delay Generator input by the delay control settings.
- 4–27 The output of the Delay Generator is input to the Width Generator integrated circuit (U4). The IC's used in both the Delay and Width Generator circuits are identical and the description of the Delay Generator operation in paragraphs 4–23 to 4–25 also applies to the Width Generator. The difference is in the output gating; the output of the Width Generator is a pulse of width

dependent on the width control settings. In Double Pulse mode, both the delayed and undelayed pulses are widened.

## 4-28 SLOPE GENERATOR

4-29 The function of the slope generator is to convert the leading and trailing edges of the input

signal - derived from the width circuit - from "fixed" to "variable" transition times with stable amplitude. The generator and its control circuits are distributed over three boards - A4, A5 and A8 - and two corresponding schematics - 3a and 2b. Since the slope generator operation is dependent on the control circuits, these will be described first.

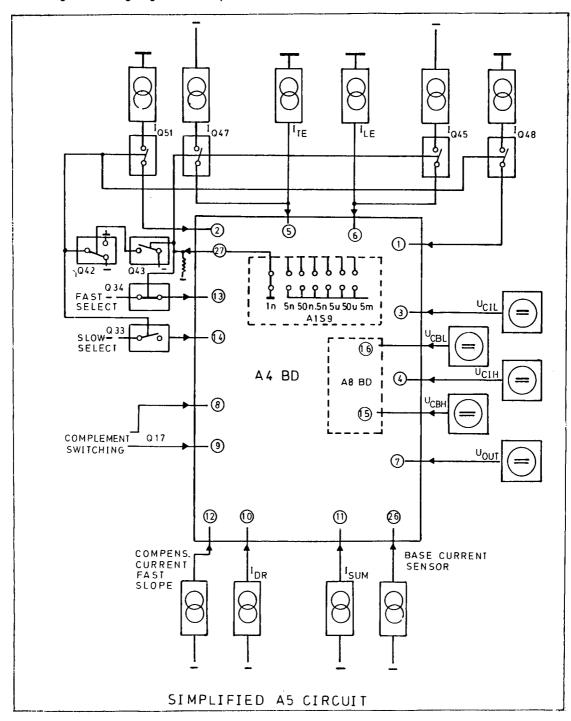


Figure 4-6a Simplified Control A5 Circuit

4-30 Reference to Schematic 3a and Figure 4-6a shows that the control circuit (A5) is comprised of several voltage controlled current sources (output dependent on LEE and TRE verniers), voltage sources and signal selector switches.

#### 4-30a Current Sources

For edges < 5 ns the two range selection current sources Q48 and Q51 are switched off (external slope capacitors A4 C15-C22 on schematic 2b not used). Only LEE vernier (A1 R5) controls the LEE and TRE constant current sources Q53 and Q56. Current sinks Q45 andQ47 are disabled (under control of Q44/Q46) and Q38 base is adjusted, under the control of U8, to sink the current-sumoutput by the slope generator. Variation of Base current is also controlled by U8. Compensation current for fast slope is supplied (sunk) by Q58. Emitter current output (sunk) by Q40.

- 4-30b For edges > 5 ns, control of the current sources is basically the same as for the fast ranges with the differences that both verniers are active, Q48 and Q51 are biased on to provide a constant current which is sunk by Q45 and Q47, these being enabled via Q44 and Q46. Q58 is switched off.
- 4-31a Reference to schematic 2b and Figure 4-6b shows the slope generator circuit to be comprised of a set of range capacitors and associated selection circuitry, a voltage level shifter, a slope generator buffer amplifier for slopes < 5 ns (A4 U5) and a buffer amplifier for slopes > 5ns (A8 U1).
- 4-31b The operation of the circuit is as follows:

The incoming signal (from width circuit) is level shifted by Q69, Q70 etc. and input to the slope generator section of U5 as a normal and a complementary signal. The operation of the slope generator part of the circuit is best explained by referring to Figure 4-6c and the following description which can then be applied to the actual circuit of schematic 2b.

4-32 For transition times from 1 ns - 4.9 ns the four constant current Sources (IS1, IS2 and IS4) are switched off.

Assume that the currents ITE and ILE, as fixed by the transition time settings are 10 mA and 20 mA respectively, then Isum = 30 mA (Isum=ILE+ITE). If the output from the previous stage, the level shifter, is input to the slope generator as shown at a and b, then at time t1 transistor Q1 turns on and Q2 turns off. Thus the only current source that can now supply constant current sink Isum is ITE (10 mA). Thus the deficiency of 20 mA is made up by the intrinsic capacitance of Q1 which discharges. This provides the slope of c from t1 to t2. When the voltage has dropped to the level at t2 (slightly lower than the potential of voltage source V2), diode CR3 starts to conduct and prevents the voltage from falling any further.

- 4-33 At time t3, transistor Q1 turns off and Q2 turns on. The current source ITE (10 mA) cannot now drain into Isum and so it starts to re-charge the intrinsic capacitance of Q1 (slope of c from t3 to t4.). When the voltage level of point c reaches t4 (slightly higher potential than voltage source V1), diode CR1 starts to conduct and holds the voltage at this level.
- 4-34 The same action as described above controls the voltage levels at point d. Thus it can be seen that the transition times of the pulses at points c and d depend on the currents from the two sources (ITE and ILE) which in turn depend on the transition time settings.
- 4-35 For transition times 5 to 50ns (slow range), only the current sources IS1, IS2, IS3 and IS4 are switched on. (IQ47 = IQ51; IQ45 = IQ48) This provides a constant current path as shown in Figure 4-6c in order to keep CR5 and CR6 forward biased. For transition times greater than 50ns, 4 pairs of additional external capacitors (C15 to C22) are switched in to supplement the intrinsic capacitances of Q1 and Q2. Reference to 4-6c shows these capacitors as CX1 and CX2 which must have the same value +/- 1%.

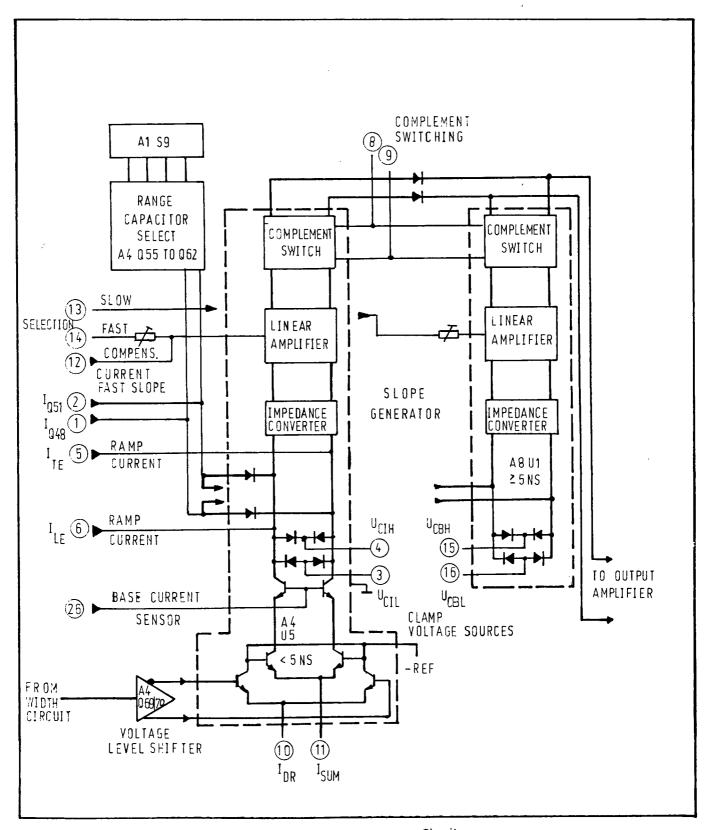


Figure 4-6b Simplified Slope Generator Circuit

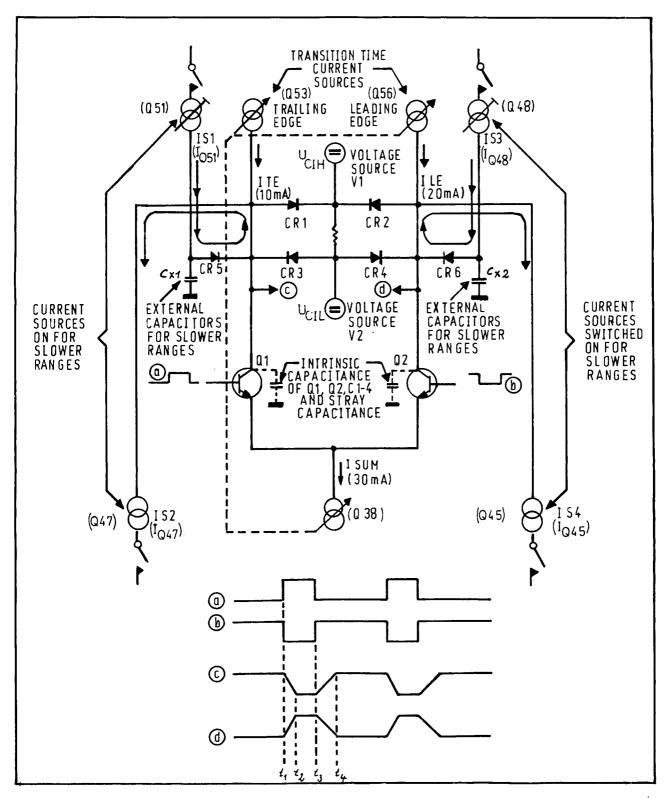


Figure 4-6c. Slope generator operation and waveforms. References in brackets are for Schematic 3a comparison.

#### 4-36 BUFFER AMPLIFIERS

4-37 The outputs of the slope generator (normal and complement) are input to one of two buffer amplifiers dependent on the transition time settings (1 - 4.9 ns or > 5 ns). The buffer amplifier for the fast ranges is in the same IC as the slope generator (U5). The buffer amplifier for the slow ranges is on sub-assembly A8. The changeover is accomplished by switching the current sources and -25 V supplies and by the fact that in the fast range the diodes within U5 (pins 14 and 15) are reversed biased so preventing signal flow between U5 and A8 U1. Refer to Q33, 34, 42, 43 on Service Sheet 3a. As shown in Service Sheet 2b, the circuits in the Buffer Amplifier provide a low-to-high impedance converter, a linear amplifier and a facility for normal/complement switching.

# 4-38 8082A OUTPUT AMPLIFIER AND VERNIER ATTENUATOR.

4-39 The function of the output amplifier and offset generator is to amplify the two signals output from the slope generator to required Amplitude and offset. The associated circuit components are distributed over three boards - A5, A4 and A9 - and three corresponding schematics - 3b, 2c and 4.

### 4-40 Complement switching (schematic 3b)

These signals 8 and 9 are input to the slope generator (schematic 2b) but are part of the output modes function (schematic 3 b). They provide UNC1 and UNC2.

4-41 Reference to schematic 2c, 3b and Figure 4-7 shows that the circuit consists basically of the output amplifier (A4 U6), three active vernier current sources, four offset generators, two attenuators (each selectable for 2 steps - 8dB or 14dB attenuation) and their control devices.

# 4-42 Output amplifier (also called active vernier) functions as follows:

The outputs from the slope generator (schematic 2b) are input to the output amplifier U6. There are two attenuating elements per attenuator stage (see Figure 4-8), one for normal and one for complement output, and three attenuator stages in parallel to increase the dynamic range of attenuation. The attenuator uses the current-sharing principle of a differential amplifier. As Figure 4-8 shows, each attenuating stage is a differential amplifier connected so that the signal current flows into (or out of) the common emitters. The output current is taken from one of the collectors and fed to the external resistor, which converts output current to voltage.

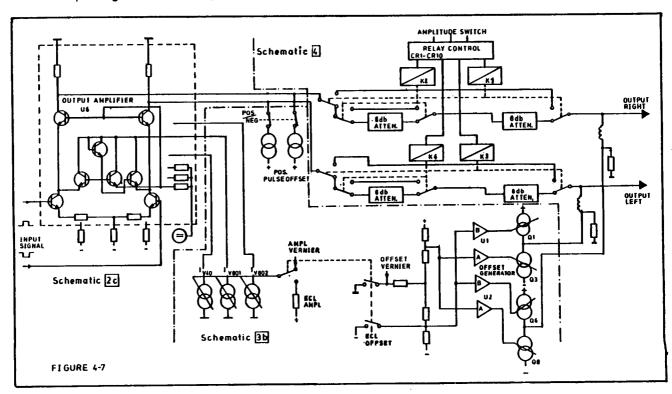


Figure 4-7

In a differential amplifier the current flow in each collector is proportional to the potential difference between the two bases. Thus, by varying the potential Vv (See Figure 4-8) on the base of one of the transistors, the current lout can be controlled. However, because of the characteristic of the base-emitter voltage of each transistor, the attenuation has a logarithmic characteristic.

The input signal controls the share of current source I which flows through each input transistor. Similarly, Vv controls the current in both attenuator transistors Q1, Q2 or Q3, Q4 (Figure 4-8). Suppose that Vv sets an attenuation factor of 2 and, for simplicity, I = 1 mA. If the input is such that I is shared equally, then I1 = 0.5 mA and I-I1 = 0.5 mA. Also Ix = 1/2 i1 = 0.25 mA and Iy = 1/2 (I-I1) = 0.25 mA. Now, suppose that the input changes the sharing of current I so that I1 = 0.2 mA, and I-I1 = 0.8 mA, then ly = 0.4 mA and lx = 0.1 mA. In either case, the sum Ix + Iy remains constant, and is in fact constant for all input signals and attenuation ratios. Therefore a constant current source (controlled by the amplitude vernier) can be used to supply the current Iy + Ix.

If Iv = I, then lout = lout = 0

If Iv = 0, then lout = lout = 0,5 I

4-43 An advantage of being able to use the one current source to supply both elements of each attenuator is that the relationship of input control current to output (signal) current is essentially linear but a slight non-linearity is due to emitter-bulk resistances and hFE and must be compensated for. This is done by a segmented approximation using U3A, U3B, U4B on A5 (Service Sheet 3b) and the three sets of attenuating differential amplifier in U6.

# 4-44 DC OFFSET FOR POSITIVE PULSE OUTPUTS

4-45 The signal levels from the Output Amplifier need to be level shifted from negative to positive if positive pulse outputs are required. This is achieved by the Positive Pulse DC Offset circuits (one for each channel - see Service Sheets 2c and 3b). If the NEG/POS switch is set to POS, two actions occur, the normal/complement relationship of the signals is switched in the Buffer Amplifier and the Positive Pulse DC Offset circuit is switched on to raise the signal levels from negative to positive.

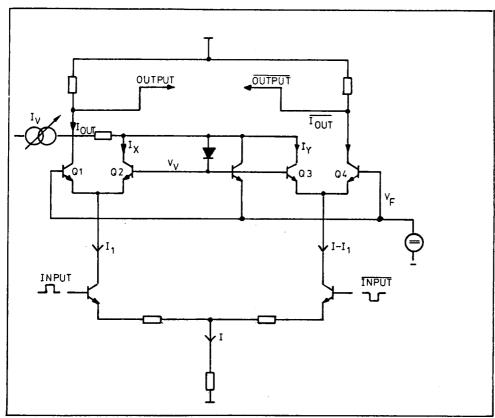


Figure 4-8 Operating principle for each stage of active vernier

#### 4-46 STEP ATTENUATOR

4-47 The amplitude of the output pulses is controlled by a Step Attenuator (one for each channel-see Service Sheet 4). The amplitude range switch selects different combinations of attenuators via a diode matrix (CR1 to CR19). Attenuators used provide 8dB (voltage -2.5) and 14 dB (voltage -5) of attenuation.

#### 4-48 EXTERNAL DC OFFSET

4-49 The output pulse baseline can be adjusted over the range -2 V to + 2V using the External DC Offset circuit (Service Sheet 3b). Both output channels are controlled by a common vernier and the circuit can also be switched off in which case the pulse baselines are at 0 V.

# 4-50 ECL MODE

4-51 Reference to schematic 3b shows that if Amplitude switch is set to ECL the Offset vernier A1 R8 is without any influence (Q14 switched on). Also Q15 is switched on and a fixed neg Offset is applied. The ECL amplitude is done by A5 R5 (Amplitude vernier A1 R7 is not active).

MAINTENANCE-

### 5-1 GENERAL

- 5-2 This section contains information on the removal of covers and assemblies, performance verification and recalibration procedures, safety checks and troubleshooting procedures.
- 5—3 Before attempting to remove covers, assemblies or components, disconnect the instrument from the ac line supply. It is advisable also to leave the instrument for a few minutes after disconnecting from the line to enable capacitors to discharge.

#### 5-4 REMOVAL OF COVERS

5-5 The top, bottom and side covers can be removal by releasing the captive screw at the rear of each cover and sliding the respective cover to the rear.

# 5-6 REMOVAL OF ASSEMBLIES (See Figure 6-1)

#### 5-7 General

5-8 Remove the instrument top cover and remove the metal retaining strip across the top rear of the boards.

# 5-9 Power Supply Board - Assembly 2

- 5-10 Cut the two plastic straps securing the connector to the rear of the board. Ease the connector off the end of the board.
- 5-11 Ease the board out of its connector on the rear of the Mother board (A1).

# 5-12 Repetition Rate Generator Board - Assembly 3

5-13 Disconnect the four coaxial cables from the Output Amplifier board (A3).

5-14 Ease the board out of its connector on the rear of the Mother board (A1).

# 5-15 Output Amplifier Board - Assembly 4

- 5-16 Disconnect the two coaxial cables from the Step Attenuator board (A9).
- 5-17 Disconnect the four coaxial cables from the Repetition Rate board (A4).
- 5-18 Disconnect the two flat cables from the Offset board (A5) at board A4 end.
- 5-19 Remove the two screws securing board A4 heat sink to the rear of the frame.
- 5-20 Carefully ease the board out of its connector on the Mother board (A1) and withdraw it through the cut-out in the rear of the frame.

# 5-21 Buffer Amplifier Board - Assembly 8

- 5-22 First remove the Output Amplifier board (A4) from the instrument.
- 5-23 Remove the screw securing board A8 to board A4 and carefully separate the two boards.

# 5-24 Output Amplifier Board (A4) --Hybrid Circuit Renewal

- 5-25 First remove the Output Amplifier board (A4) from the instrument.
- 5-26 Remove the four screws securing the heat sink to the board and remove the hybrid circuit.
- 5–27 When fitting the new hybrid circuit, thermal compound must be used to give good thermal contact between the circuit and the heat sink (compound part number 6040–0265).

# 5-28 Offset Board - Assembly 5

- 5–29 Disconnect the two flat cables from the Output Amplifier board (A4) at board A4 end.
- 5-30 Disconnect the flat cable from the Step Attenuator board (A9) at board A5 end.
- 5-31 Ease the board out of its connector on the rear of the Mother board (A1).

# 5-32 Mother Board - Assembly 1

- 5-33 First remove boards A2, A3, A4 and A5.
- 5-34 Remove the eight knobs from the front panel using an Allen key.
- 5-35 Remove the nuts securing the four BNC connectors to the front panel.

- 5-36 Disconnect the two wires from the Manual pushbutton at their connectors on the Mother board.
- 5-37 Remove the two screws securing the power ON/OFF switch to the Mother board.
- 5-38 Remove the two screws securing the Mother board to the front panel and remove the board.

## 5-39 PERFORMANCE CHECKS

5-40 Performance checks (Table 5-1 to 5-13) give the procedures for verifying that the 8082A is working to the specifications. The checks should be performed in sequence from 5-1 to 5-13.

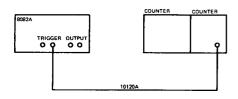
For Adjustments and Safety Check refer to paragraph 5–41 on page 5–13.

Table 5-1. Test Equipment and Accessories for Performance Checks

INSTRUMENT	BRIEF SPECIFICATION	RECOMMENDED MODEL
Pulse Generator	10 MHz square wave output with 50% duty cycle	HP 8011A
Counter	Frequency range 0-50 MHz	5245L
	Prescaler plug-in	5252A
Oscilloscope	Dual Channel, 50 MHz bandwidth, 5mV/div. sensitivity, sweep speeds 5ns/div. to 2s/div. with sweep delay.	HP 180A with plug-ins 1801A and 1820A
Sampling Oscilloscope	Dual Channel, 1 GHz bandwidth, 1mV/div. sensitivity, sweep speeds 10ns/div. to 2s/div. 50- $\Omega$ input impedance.	Tek 760 with 7T11 7S11 and S-3A
Digital Voltmeter	100V range to 4 significant figures. Accuracy ±0.05% ±1 digit.	HP 3440A with plug-in 3443A
Test Oscillator	Frequency range 10 Hz - 10 MHz	HP 651A
Test Oscillator	Frequency range 10 to 500 MHz	HP 3200B

ACCESSORIES	
$50\Omega$ co-axial cable terminated with BNC male connectors (4 required)	HP 10120A
Connector BNC male to N female (2 required)	HP 1250-0077
Connector BNC male to N male (2 required)	HP 1250-0780
$50\Omega$ Feed-through termination (2 required)	HP 11048B/C
Pulse Adder	HP 15104A
20dB Attenuator, 50 $\Omega$ (2 required)	HP 8491A

Table 5-2. Performance Check - Repetition Rate



#### STEP

1 8082A settings:

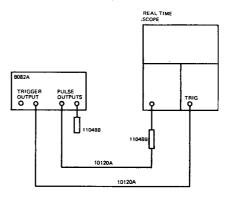
5252A settings:

1	REPETITION RATE	250M-100M	Max. count
2	VERNIER	CCW	rate
3	NORM/DOUBLE	NORM	
4	DELAY	2n5n	
5	VERNIER	CCW	
7	MODE SWITCH	NORM	
8	WIDTH	2n-5n	
9	VERNIER	CCW	
10	TRANSITION TIME	1n-5n	
11	LEADING VERNIER	CCW	
12	TRAILING VERNIER	CCW	

#### Measure the frequency as follows:

REPETITION RATE	VERNIER 2	COUNTER	RESULT
250M-100M 250M-100M 100M-10M 100M-10M 10M-1M 10M-1M 1M-100K 1M-100K 100K-10K 100K-10K 10K-1K	CCW CW CCW CCW CCW CCW CCW CCW CCW CCW	0.1m 0.1m 0.1m 0.1m 0.1m 0.1m 1m 1m 10m 10m 0.1s 0.1s	> 250M < 100M > 100M < 10M > 10M < 1M > 1M < 100K > 100K > 10K < 10K < 1K

Table 5-3., Performance Check - Delay (Slow)



STEP

8082A settings:

2 RATE VERNIER CW 13 AMPLITUDE 2.0-5.0

use scope neg. slope : Falling edge Set trigger pulse on first line of graticule and measure time between trig and output -pulse (leading edges) Scope - and ext trigger

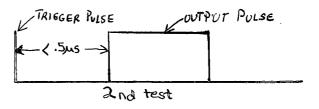
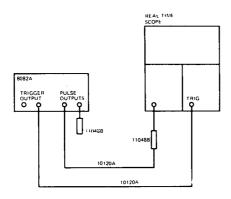


Table 5-4. Performance Check - Width (Slow)



#### STEP

1 8082A settings:

13 AMPLITUDE 2.0-5.0 14 AMPLITUDE 0.4-1.0 15 AMP. VERNIER CW 4 delay 5n-50n CCW 2 vote Vernier CW

2 Measure the width:

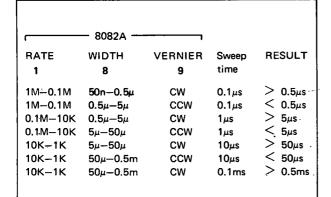
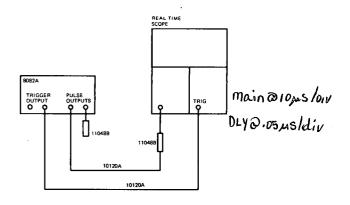


Table 5-5. Performance Check - Jitter



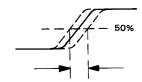
**STEP** 

Period jitter

1 8082A settings:

1 REPETITION RATE 0.1M-10K
4 DELAY 2n-5n
5 VERNIER CCW
8 WIDTH 0.5μ-5μ
9 VERNIER CCW

Turn rep. rate vernier (2) to get a 10 division period display on screen. Set scope delay until the second leading edge is visible



3 Check period jitter: <1 cm ≈ 0.1%

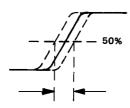
Delay jitter

4 8082A settings: Scope:

1 REP. RATE 10K-1K
2 VERNIER CW
4 DELAY 5μ-50μ
8 WIDTH 5μ-50μ

- 5 Turn delay vernier (5) for 50μs delay.
- 6 Set scope delay until leading edge is visible.

# Table 5-5. (cont'd)



7 Check delay jitter < 0.5 cm ≈ 0.1%

Width jitter

8 8082A settings:

scope settings:

1 REP. RATE

10K-1K Turn delay to 10 cm.

2 VERNIER

CW

8 WIDTH

 $5\mu - 50\mu$ 

9 VERNIER

for a 50 µs display

- 9 Set scope delay CCW until the trailing edge is visible.
- 10 Check width jitter:  $< 0.5 \text{ cm} \approx 0.1\%$

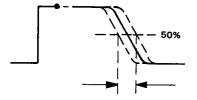
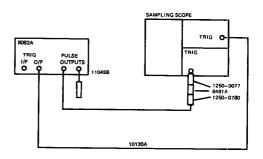


Table 5-6. Performance Check - Width (Fast)



# STEP

- 1 8082A settings:
  - 1 REPETITION RATE 250M-100M

2 VERNIER

CCW

4 DELAY 5 VERNIER 2n-5n CCW

2 Check the following:

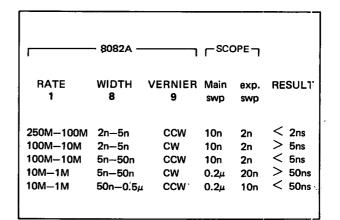


Table 5-7. Performance Check - Delay (Fast)

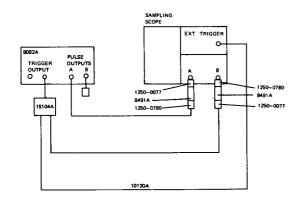
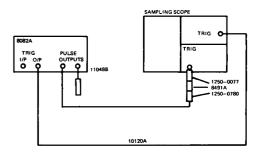


Table 5–8. Performance Check — Square Wave Duty Cycle



#### **STEP**

- 1 8082A settings:
  - 8 WIDTH

2n-5n

Set channel B on first line of graticule. Measure time between neg. trig. and pos. (leading edge) output pulse.

#### STEP

1 8082A settings:

1 REPETITION RATE 250M-100M 2 VERNIER CCW

2 Measure duty cycle

Limit > 35% < 65%

3 Turn rate vernier (2) CW.

4 Measure duty cycle at 100 MHz Limit > 35% < 65%

5 Set rep. rate (1) to 100M-10M and turn rate vernier (2) CCW.

6 Measure duty cycle at 100 MHz  $\,$  Limit > 35% < 65%

7 Turn rate vernier (2) CW.

8 Measure duty cycle at 10 MHz Limit > 40% < 60%

9 Set rep rate (1) to 10M-1M.

10 Measure duty cycle at 1 MHz  $\sim$  Limit > 40% < 60%

	80	82A		sco	OPE ———	
RATE 1	VERNIER 2	DELAY 4	VERNIER 5	 meantime	l expanded	RESULT fxd del typ 18ns
10M-1M	ccw	2n-5n	ccw	20n	5n	> 16ns < 19ns
10M-1M	CCW	2n-5n	cw	<b>20</b> n	5n	fxd del + > 5ns
10M-1M	CCW	5n-50n	CCW	<b>20</b> n	5n	fxd del + < 5ns
10M-1M	CCW	5n50n	CW	<b>20</b> n	10n	fxd del + >50ns
10M-1M	middle	50n-500n	ccw	<b>50</b> n	10n	fxd del $+ \le 50$ n

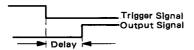
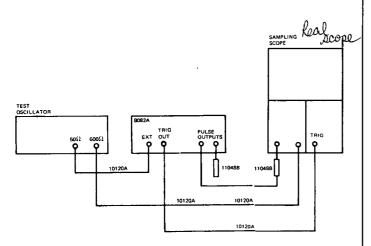


Table 5-9. Performance Check - External Functions



#### **STEP**

1 8082A settings:

4 DELAY

 $50\mu - 0.5m$ 

VERNIER

CCW

6 EXT INPUT LEVEL

middle

7 MODE SWITCH EXT. TRIG.

8 WIDTH

 $50\mu - 0.5m$ 

9 VERNIER

CCW

24 SLOPE POLARITY

POS

Test oscillator settings: 1KHz, 1V

FREQUENCY VERNIER 1

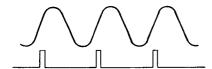
FREQUENCY RANGE

OUTPUT

1K 1V

#### **EXT TRIGGER** 2

Pulse should appear only during positive slope of sine wave. Pulse is variable by width and delay and its trig. point is variable by EXT INPUT LEVEL (6).

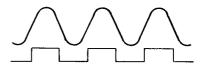


- Set SLOPE POLARITY (24) to MAN and press MAN 3 button (25): Only one pulse must occur.
- Set SLOPE POLARITY (24) to NEG. Pulse should appear only during negative slope of sine wave. Pulse is variable by width and delay and its trig, point is variable by EXT INPUT LEVEL (6).
- **EXT WIDTH** 5

Set MODE SWITCH (7) to EXT WIDTH. Set SLOPE POLARITY (24) to POS.

#### Table 5-9. (cont'd)

Pulse must only occur during the positive part of the sinewave. It should only be variable by EXT INPUT LEVEL (6), independent of width, delay and rep. rate.



- Repeat with SLOPE/POLARITY (24) set to NEG. This time a pulse must only occur during the negative part of the sinewave.
- 8 GATE

Set the 8082A as follows:

1 REPETITION RATE 10K-1K 2 VERNIER CCW 4 DELAY  $50\mu - 0.5m$ 5 VERNIER CCW 7 MODE SWITCH **GATE** 8 WIDTH  $50\mu - 0.5m$ 

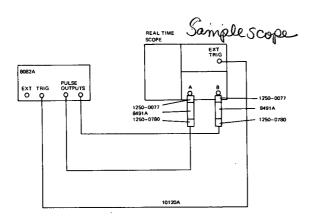
**VERNIER** middle 24 SLOPE POLARITY POS

Leading edges of output pulses must only occur during positive part of sinewave. Pulses are available by all controls (rep. rate, width, delay, ext. input level).



Set the SLOPE POLARITY (24) to NEG. Pulses must 10 only occur during negative slope of sinewave.

Table 5-10. Performance Check - Transition Time



#### STEP

1 8082A settings:

1 REPETITION RATE 250M—100M
2 VERNIER CW
4 DELAY 2n—5n
8 WIDTH 2n—5n
13 AMPLITUDE 2.0—5.0
14 AMPLITUDE 2.0—5.0
10 TRANSITION 1n—5n
11 VERNIER CCW

- 2 Adjust the width vernier for 50% duty cycle.
- 3 Adjust the scope for a full screen display, set to Expand and centre the leading edge of the pulse on the display.
- 4 Measure transition time between 10% and 90% points. <1ns
- 5 Centre trailing edge on the display and measure transition time between 10% and 90% < 1ns</p>
- Repeat 1 to 4 with NEG/POS switch (20) in NEG position.
- 7 Repeat 1 to 4 with NORM/COMPL switch (19) set to COMPL.

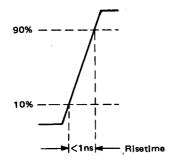
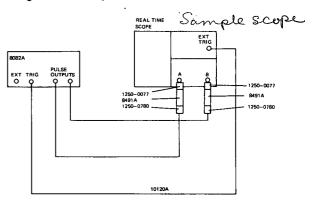
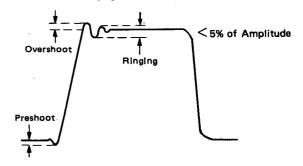


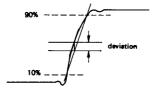
Table 5–11. Performance Check — Pre-shoot, Overshoot, Rining and Linearity



#### STEP

- 1 8082A settings:
  - 1 REP RATE 10m-1m 8 WIDTH 50n-0.5n
  - 10 TRANSITION TIME 5n-50n 19 NORM/COMPL SWITCH NORM 20 NEG/POS SWITCH POS
- 2 Adjust width vernier for a 50% duty cycle and 8 div vertically.
- 3 Adjust leading vernier 11 and trailing vernier 12 for 10ns transition time.
- 4 Measure, with reference to diagrams below, preshoot, overshoot, ringing and linearity.

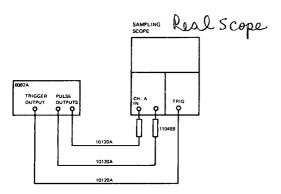




deviation from a straight line between the 10% and 90% points should not exceed 5% of the peak voltage.

- 5 Repeat with the NEG/POS switch (20) set to NEG.
- 6 Repeat with the NORM/COMPL switch (19) set to COMPL.

Table 5-12. Performance Check - Amplitude



#### STEP

- 1 8082A settings:
  - 1 REPETITION RATE 10K-1K
  - 2 VERNIER

CCW

8 WIDTH

**SQUARE WAVE** 

- 2 Set the baseline of the scope to zero.
- 3 Measure the amplitude of both outputs as follows:

8082	Α	
AMPLITUDE	VERNIER	RESULT
13, 14	15	
5.0-2.0	CW	> 5V
5.0-2.0	CCW	< 2V
2.0-1.0	CW	> 2V
2.0-1.0	CCW	< 0.8V
1.0-0.5	CW	> 1.0V
1.0-0.5	CCW	< 0.5V

- 4 Set NORM/COMPL switch (19) to COMPL and repeat step 3.
- 5 Switch either AMPLITUDE switch (13 or 14) to ECL and measure the level and amplitude.



- 6 Set AMPLITUDE to 5.0—2.0, MODE SWITCH (7) to EXT TRIG, SLOPE POLARITY (24) to MAN and adjust the scope for a baseline reference. Set OFFSET switch (17) to ON and turn OFFSET VERNIER (16) from CW to CCW.
- 7 Baseline should shift from  $\geq -2V$  to  $\geq +2V$ .

Table 5-13. Performance Check Record (1 of 4)

Hewlett-Packar Model 8082A	d Company	Tested by		
Pulse Generato Serial No		Date		
Table			Results	
No.	Check Description	Min.	Actual	Max.
5-2	REPETITION RATE VERNIER			
	250M-100M CCW	250M		
	250M—100M CW		<u> </u>	100M
	100M-10M CCW 100M-10M CW	100M		10M
	10M-1M CCW	10M		
	10M-1M CW 1M-100K CCW	444		1M
	1M-100K CCW 1M-100K CW	1M		100K
	100K-10K CCW	100K		4014
	100K-10K CW 10K-1K CCW	10K		10K
	10K-1K CW	1010		1K
5–3	Delay (slow)  RATE DELAY VERNIER WIDTH			
	1M0.1M 50n0.5μ CW 50n0.5μ	0.5μs		
	1m-0.1M 0.5µ-5µ CCW 50n-0.5µ	0.023		0.5μs
	0.1M-10K 0.5µ-5µ CW 0.5µ-5µ	5μs	-	<b>5</b>
	0.1M—10К 5µ—50µ ССW 0.5µ—5µ 10К—1К 5µ—50µ СW 0.5µ—5µ	50µs		5μs
	10K-1K 50μ-0.5m CCW 5μ-50μ			50μs
	10K—1K 50μ—0.5m CW 5μ—50μ	500μs		
5–4	Width (slow)			
	RATE WIDTH VERNIER			
	1M0.1M 50n0.5μ CW	0.5µs		
•	1M-0.1M $0.5\mu$ -5 $\mu$ CCW			0.5μs
	0.1M—10K 0.5µ—5µ CW 0.1M—10K 5µ—50µ CCW	5μs		<sup>'</sup> 5μs
	10K-10K 5μ-50μ CCW	50µs		
	10K-1K 50μ-0.5m CCW	1	1	50µs

Table 5-13. Performance Check Record (2 of 4)

Table			Results	
No.	Check Description	Min.	Actual	Max.
55	Jitter Period jitter Delay jitter Width jitter			0.1 % 0.1 % 0.1 %
56	Width (fast)  RATE WIDTH VERNIER  250M-100M 2n-5n CCW 100M-10M 2n-5n CW 100M-10M 5n-50n CCW 10M-1M 5n-50n CW 10M-1M 5n-50n CW	5ns 50ns		2ns 5ns 50ns
5–7	Delay (fast)  RATE VERNIER DELAY VERNIER  10M-1M CCW 2n-5n CCW 10M-1M CCW 2n-5n CW 10M-1M CCW 5n-50n CCW 10M-1M CCW 5n-50n CW 10M-1M middle 50n-500n CCW  * Fixed delay, typically 18ns	16ns D+5ns D+50ns	(D*)	19ns D+5ns D+50ns
5–8	Square Wave Duty cycle  RATE VERNIER  250–100M CCW 250–100M CW  100M–10M CCW 100M–10M CW  10M–1M CW	35% 35% 35% 40%		65% 65% 65% 60%

Table 5-13. Performance Checks Record (3 of 4)

Table					Results		
No.	Check	Check Description			Actual	Max.	
5–9	External F	unctions					
	MODE	SLO	PE				
	Ext trigger	POS		Outpu	ut pulse during positi	ve slope	
	Ext trigger	MAN		Single	pulse.		
	Ext.trigger	NEG	;	Outpu	ut pulse during negat	ive stope.	
	Ext width	POS		Outpu of inp	ut pulse during positi out.		
	Ext width	Ext width NEG Output pulse during of input.			negative part		
			ng edges of output d ve part of input.	ges of output during rt of input.			
	Gate	NEG	ì		ng edges of output d ive part of input.	uring	
5–10	Transition Time	NEG/POS	NORM/COMPL				
	Leading edge Trailing edge Leading edge Trailing edge Leading edge Trailing edge Leading edge Leading edge Trailing edge	POS POS NEG NEG NEG POS POS	NORM NORM NORM NORM COMPL COMPL COMPL COMPL			1 ns 1 ns 1 ns 1 ns 1 ns 1 ns 1 ns	

Table 5-13. Performance Check Record (4 of 4)

	01				Results	
Table No.	Ch	eck Description	on	Min.	Actual	Max.
5–11	Preshoot, Ov	ershoot, Rinir	ng and Linearity			
		NEG/POS	NORM/COMPL			
	Preshoot	POS	NORM		,	5%
		NEG	NORM	•		5%
		NEG	COMPL			5%
		POS	COMPL			5%
	Overshoot	POS	NORM			5%
		NEG	NORM			5%
		NEG	COMPL			5%
		POS	COMPL			5%
	Ringing	POS	NORM			5%
	rinigirig	NEG	NORM			5%
		NEG	COMPL			5%
		POS	COMPL			5%
	Linearity	POS	NORM			5%
		NEG	NORM			5%
		NEG	COMPL		ļ	5%
		POS	COMPL			5%
5–12	Amplitude					
5-12	AMPLITUDE	VERNIER	NORM/COMPL			
	5.0-2.0	CW	NORM	5V		
	5.0-2.0	CCW	NORM			2V
	2.0-1.0	CW	NORM	2V		
	2.0-1.0	CCW	NORM			V8.0
	1.0-0.5	CW	NORM	1.0V		
	1.0-0.5	CCW	NORM			0.5V
	5.0-2.0	CW	COMPL	5V		<b>2</b> V
	5.02.0 2.01.0	CCW	COMPL COMPL	2V		2 V
	2.0—1.0 2.0—1.0	CCM	COMPL	"		0.8V
	1.0-0.5	CW	COMPL	1.0V		*
	1.0-0.5	ccw	COMPL			0.5V
	ECL			HI-0.45V typ		
				LO-0.85V typ		
		OFFSET VE	RNIER			
	5.0-2.0	CW		-2V		
		CCW		+2V		

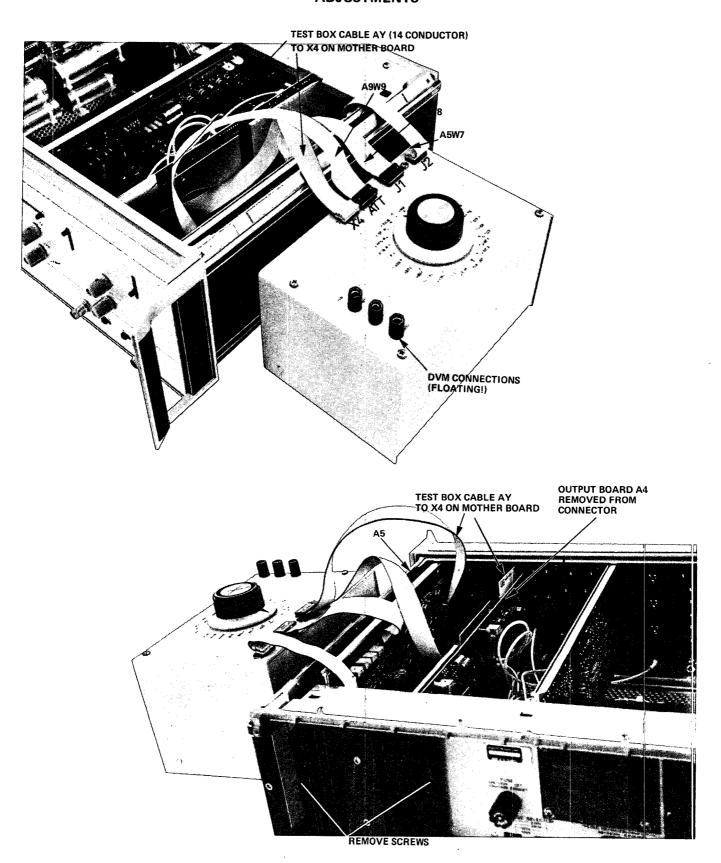


Figure 5-1. Connections between Test Box 15265A and 8082A.

# 5-41 INTERNAL CHECKS AND ADJUSTMENTS

5–42 The internal checks and adjustments section (Tables 5–14 to 5–26) gives the procedure for adjusting a serviceable instrument to bring it within specification. The checks should be performed in the order in which they appear. A summary of adjustments and selectable components is presented in Table 5–14. Figure 5–2 at the end of this section gives the locations of the adjustments.

**NOTE:** Ensure that BOTH outputs of the 8082A are terminated by a 50  $\Omega$  load whenever pulse measurements or adjustments are to be made.

# 5-43 TROUBLESHOOTING USING THE 15265A TEST BOX

5–44 The Test Box is designed to facilitate troubleshooting and adjustments of the current sources in Board A5. It is connected in place of Board A4 and simulates the load presented to Board A5. A switch on the Test Box selects the parameter for monitoring on the externally-connected DVM. The checks and adjustments which can be performed by the Test Box are summarized in Table 5–16.

5-45 To connect the Test Box, refer to Figure 5-1 and use the following procedure:

Switch 8082A off. Remove the 8082A top cover. Remove the two screws from the Output Amplifier (board A4) heat sink on the rear of the 8082A. Unplug the Output Amplifier from its connector by about 2 cm and carfully push the connector-end of the board to one side.

Disconnect the three ribbon cables:

A5 W8 from A4 J1 A5 W7 from A4 J2 A9 W9 from A5 J-ATT

Connect the extender board to the X4 socket (socket from which board A4 has been disconnected).

Connect the other extender cable to the J-ATT connector on board A5.

Connect the ends of cables J1, J2, J-ATT and X5 to the Test Box as shown in Figure 5–1.

Connect DVM (floating mode, auto range) and verify operation of Test Box by performing first check in Table 5–16.

#### 5-46 SAFETY CHECK

5-47 This check (Table 5-27) should be performed following the internal checks and adjustments to verify the instrument safety.

#### 5-48 TROUBLESHOOTING TIPS

5-49 The quadruple AND gate A3 U6 in the reprate circuit can be damaged if the -5V or -10V supplies are shorted to ground.

5–50 Instruments with serial numbers 1410G00430 and below may be liable to latch-up when switching transition times from 5–50 $\mu$  to 50 $\mu$ –0.5m. If this occurs, insert diode CR 37 (partnumber 1901–0040) in series with A5 Q37 (anode to collector).

# WARNING

Any interuption of the protective (grounding) conductor inside or outside the instrument or disconnection of the protective earth terminal is likely to make the apparatus dangerous. Intentional interruption is prohibited. Any adjustment, maintenance, and repair of the opened instrument under voltage should be avoided as much as possible and, when inevitable, should be carried out only by a skilled person who is aware of the hazard involved. The opening of covers or removal of parts, except those to which access can be gained by hand, may expose live parts, and also accessible terminals may be live.

Capacitors inside the instrument may still be charged even if the instrument has been disconnected from its source of supply.

Make sure that only fuses with the required rated current and of the specified type (normal blow, time delay, etc.) are used for replacement. The use of repaired fuses and the short-circuiting of fuseholders must be avoided.

Whenever it is likely that the protection has been impaired, the instrument must be made inoperative and be secured against any unintended operation.

Table 5-14. Summary of adjustable and factory-selected components

A2 R7	Component	Adjusts	Table in which adjustment is described
AZ R10 AZ R21 AZ R29 AZ R36 AZ R37 AZ R38 A			
A2 R21 A2 R29 A2 R29 A2 R29 A2 R29 A2 R29 A3 R6 A3 R6 A3 R6 A3 R7 A3 R87 A3 R87 A3 R87 A3 R87 A3 R87 A3 R87 A4 R38* A4 R42* Double pulse A4 R43 A4 R44 A4 R44 A4 R44 A4 R44 A4 R44 A4 R44 A5 R214 A6 R214 A7 R232 A7 R14 A7 R233 A7 R14 A7 R233 A7 R14 A7 R233 A7 R15 A7 R16 A7 R17 A7 R16 A7 R17 A7 R16 A7 R17 A7		+10V power supply	5–17
A2 R29  A3 R6  A3 R6  A3 R77  A3 R67  A3 R77  A3 R87  A4 R38  A5 R87  A6 R42  A7 R38  A7 R38  A8 R42  A8 R43  A8 R43  A8 R43  A8 R44  A9 R44  A9 R44  A9 R24  A1 R24  A4 R24  A4 R24  A5 R16  A5 R162  A5 R123  A5 R121  A5 R122  A5 R121  A5 R122  A5 R121  A5 R122  A5 R121  A5 R122  A5 R121  A6 R121  A6 R213  A7 R124  A8 R24  A9 R254  A9 R254  A9 R254  A9 R254  A9 R254  A9 R254  A9 R161  A9 R254  A9 R254  A9 R254  A9 R254  A9 R254  A9 R254  A9 R255  A9 R161  A9 R254  A9 R254  A9 R254  A9 R254  A9 R254  A9 R254  A9 R255  A9 R161  A9 R254  A		· · · · · · · · · · · · · · · · · · ·	5—17
A3 R6 A3 R5 A3 R5 A3 R77* A3 R87 A3 R87 A3 R87 A3 R87 A3 R92* Duty cycle in gate mode 5-25 A4 R38* Maximum delay Double pulse Width adjust A4 R240  A4 R240  A5 R214 A6 R222 A6 R232 A7 R186* A7 R187 A7 R188 A8 R16 A8 R17  A8 R16 A8 R17  A8 R16 A8 R17  Integrator buffer amplifier (slow ranges) 5-21		* * * * * * * * * * * * * * * * * * * *	5—17
A3 R5 A3 R77* A3 R87 A3 R87 A3 R87 A3 R87 A3 R92*  Duty cycle in gate mode  A3 R87 A3 R92*  A4 R28*  Maximum delay  Double pulse  A4 R24  A4 R88*  Max width A5 -19  A5 R23  A4 R253  A5 R142*  A5 R148  A5 R161 A5 R162 A5 R230 A5 R17 A5 R172 A5 R136 A5 R217 A5 R136 A5 R217 A5 R172 A5 R173 A5 R172 A5 R173 A5 R174 A5 R186 A5 R217 A5 R172 A5 R171 A5 R172 A5 R80 A5 R81 A5 R815 A6 R81 A6 R816 A6 R817  Integrator buffer amplifier (slow ranges)  5-21	A2 R29	-25V power supply	5–17
A3 R5 A3 R77* SW duty cycle < 100 MHz rep rate	A3 R6	Max rep rate	5–18
A3 R87 A3 R82* A3 R82* A4 R38* A4 R38* A4 R42* Double pulse A5 R213 A5 R147 A5 R142* A5 R147 A5 R148 A5 R148 A5 R115 A6 R162 A6 R117 A6 R162 A6 R117 A6 R162 A6 R16 A6 R117 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R17 A6 R16 A6 R17 A6 R16 A6 R17 A6 R16 A6 R16 A6 R17 A6 R16 A6 R17 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A6 R16 A6 R16 A6 R17 A6 R16 A7 R1			518
A3 R87 A3 R92* A4 R38* A4 R38* A4 R42* Double pulse A4 R43 A4 R88* A4 R84 A4 R240  A4 R240  A5 R142  A4 R214 A6 R222 A7 R188* A8 R232 A8 L(CR32) A7 L(CR32) A8 L(CR33) A7 R12*  A8 R147 A5 R148 A5 R147 A5 R148 A5 R161 A5 R162 A5 R161 A5 R162 A5 R161 A5 R171 A5 R	A3 R77*	SW duty cycle < 100 MHz rep rate	5–18
A4 R38* A4 R38* A4 R38* A4 R34* Double pulse A4 R88* A4 R88* A4 R88* A4 R88* A4 R240  Midth adjust A5 R142  A4 R214 AF Risetime (fast), rolloff, overshoot neg baseline shift A5 R213 A5 R161 A5 R172 A5 R171 A5 R171 A5 R172 A5 R171 A5 R171 A5 R172 A5 R80 A5 R810 A5 R80 A5 R810 A5 R817 A5 R80 A5 R810 A5 R817 A5 R80 A5 R810 A5 R811 A5 R811 A5 R811 A5 R816 A5 R		Duty cycle in gate mode	
A4 R42* A4 R43 A4 R88* A4 R88* A4 R89* A4 R240  Min width  A4 R214 A4 R214 A4 R232 A4 L(CR32) A4 L(CR32) A4 L(CR33) A5 R142* A5 R148 A5 R148 A6 R162 A7 R161 A7 R161 A7 R17 A8 R10 A8 R1			
A4 R43 A4 R88* A4 R240       Width adjust Max width Min width       5-24         A4 R240       Min width       5-19         A4 R240       Min width       5-24         A4 R214 A4 R232 A4 L(CR32) A4 L(CR33)       Risetime (fast), rolloff, overshoot neg baseline shift       5-20         A4 R253 A5 R142*       Output amplifier Slow transition time in the first integrator range (1-5µs)       5-20         A5 R147 A5 R148       Integrator (working window)       5-16 (8), 5-20         A5 R161 A5 R162 A5 R213 A5 R230       A8-adjust (working window)       5-16 (8)         A5 R213 A5 R230       Integrator minimum current       5-22         A5 R136 A5 R217       Dual slope, slope equivalence       5-22 (5)         A5 R171 A5 R172 A5 R210       Integrator (slow ranges)       5-16 (5), 5-22         A5 R80 A5 R81 A5 R811 A5 R115       Positive pulse baseline tracking       5-23         A8 R16 A8 R16 A8 R17       Integrator buffer amplifier (slow ranges)       5-21			
A4 R88* A4 R240       Max width Min width       5–19 5–24         A4 R214 A4 R232       Risetime (fast), rolloff, overshoot neg baseline shift       5–20         A4 L(CR32) A4 L(CR33)       Risetime (fast), overshoot       5–20         A4 R253       Output amplifier       5–20         A5 R142*       Slow transition time in the first integrator range (1–5μs)       5–20         A5 R147 A5 R148       Integrator (working window)       5–16 (8), 5–20         A5 R161 A5 R162       A8-adjust (working window)       5–16 (8)         A5 R213 A5 R230       Integrator minimum current       5–22         A5 R136 A5 R217       Dual slope, slope equivalence       5–22 (5)         A5 R171 A5 R172       Integrator (slow ranges)       5–16 (5), 5–22         A5 R210 A5 R80 A5 R81 A5 R115       Internal supply voltage for integrator       5–16 (10) 5–20         A8 R16 A8 R16 A8 R17       Integrator buffer amplifier (slow ranges)       5–21	1	· · · · · · · · · · · · · · · · · · ·	
A4 R214 A4 R232 A4 L(CR32) A4 L(CR33) A4 R253 A5 R142* A5 R148 A5 R161 A5 R230 A5 R162 A5 R213 A5 R230 A5 R217 A5 R171 A5 R171 A5 R172 A5 R171 A5 R171 A5 R172 A5 R171 A5 R17			
A4 R214 A4 R232 A4 L(CR32) A4 L(CR33) A4 L(CR33) A4 R253 A5 R142*  A5 R142*  A5 R148  A6 R148  A7 R250  A8 R161 A7 R162  A7 R250  A7 R213 A7 R230  A8 R230  A9 R230			
A4 R214 A4 R232       Risetime (fast), rolloff, overshoot neg baseline shift       5–20         A4 L(CR32) A4 L(CR33)       Risetime (fast), overshoot       5–20         A4 R253       Output amplifier       5–20         A5 R142*       Slow transition time in the first integrator range (1–5μs)       5–20         A5 R147 A5 R148       Integrator (working window)       5–16 (8), 5–20         A5 R161 A5 R162       A8-adjust (working window)       5–16 (8)         A5 R213 A5 R230       Integrator minimum current       5–22         A5 R136 A5 R217       Dual slope, slope equivalence       5–22 (5)         A5 R171 A5 R172       Integrator (slow ranges)       5–16 (5), 5–22         A5 R80 A5 R81 A5 R81 A5 R811 A5 R115       Positive pulse baseline tracking       5–23         A8 R16 A8 R17       Integrator buffer amplifier (slow ranges)       5–21	A4 R240	Min width	5–24
A4 R232       neg baseline shift       5-20         A4 L(CR32)       Risetime (fast), overshoot       5-20         A4 R253       Output amplifier       5-20         A5 R142*       Slow transition time in the first integrator range (1-5μs)       5-20         A5 R147       Integrator (working window)       5-16 (8), 5-20         A5 R148       Integrator (working window)       5-16 (8)         A5 R161       A8-adjust (working window)       5-16 (8)         A5 R213       Integrator minimum current       5-22         A5 R213       Dual slope, slope equivalence       5-22 (5)         A5 R136       Dual slope, slope equivalence       5-22 (5)         A5 R171       Integrator (slow ranges)       5-16 (5), 5-22         A5 R210       Internal supply voltage for integrator       5-16 (10) 5-20         A5 R80       A5 R81       Positive pulse baseline tracking       5-23         A8 R16       Integrator buffer amplifier (slow ranges)       5-21			:
A4 L(CR32) A4 L(CR33) A4 L(CR33) A5 R142*  A5 R142*  A5 R142*  A5 R147 A5 R148  A8-adjust (working window)  A5 R230 A5			520
A4 L(CR33)  A4 R253  Output amplifier  A5 R142*  Slow transition time in the first integrator range (1 – 5μs)  A5 R147  A5 R148  A5 R161  A5 R162  A8-adjust (working window)  A5 R213  A5 R230  A5 R230  A5 R136  A5 R217  A5 R171  A5 R171  A5 R172  A5 R172  A5 R171  A5 R172  A5 R172  A5 R171  A5 R172  A5 R171  A5 R172  A5 R171  A5 R171  A5 R171  A5 R171  A5 R171  A5 R172  A5 R171	A4 R232	neg baseline shift	<b>3</b> –20
A4 L(CR3)  A4 R253  A5 R142*  Slow transition time in the first integrator range (1-5μs)  A5 R147  A5 R148  Integrator (working window)  A5 R161  A5 R162  A8-adjust (working window)  A5 R213  A5 R230  A5 R230  Integrator minimum current  A5 R136  A5 R217  A5 R171  A5 R172  A5 R171  A5 R172  A5 R172  A5 R210  Integrator (slow ranges)  A5 R20  A5 R80  A5 R81  A5 R80  A5 R81  A5 R81  A5 R816  A8 R16  A8 R16  A8 R16  A8 R16  A8 R17  Integrator buffer amplifier (slow ranges)  5-20  5-16 (8), 5-20  5-21	A4 L(CR32)	5	
A5 R142*  Slow transition time in the first integrator range (1 – 5μs)  A5 R147 A5 R148  Integrator (working window)  A5 R161 A5 R162  A8-adjust (working window)  A5 R213 A5 R230  Integrator minimum current  A5 R136 A5 R217  Dual slope, slope equivalence  A5 R171 A5 R172  A5 R172  A5 R210  Integrator (slow ranges)  A5 R210  A5 R80 A5 R81 A5 R81 A5 R815  A8 R16 A8 R17  Integrator buffer amplifier (slow ranges)  5-20  5-16 (8), 5-20  5-16 (8)	A4 L(CR33)	Hisetime (tast), overshoot	5–20
A5 R142*  Slow transition time in the first integrator range (1 – 5μs)  A5 R147 A5 R148  Integrator (working window)  A5 R161 A5 R162  A8-adjust (working window)  A5 R213 A5 R230  Integrator minimum current  A5 R166 A5 R217  Dual slope, slope equivalence  A5 R171 A5 R172  A5 R172  A5 R210  Integrator (slow ranges)  A5 R210  A5 R80 A5 R81 A5 R81 A5 R815  A8 R16 A8 R17  Integrator buffer amplifier (slow ranges)  5-20  5-16 (8), 5-20  5-16 (8)	A4 R253	Output amplifier	5–20
A5 R147 A5 R148 A5 R161 A5 R162 A8-adjust (working window) A5 R213 A5 R230 A5 R230 A5 R217 A5 R171 A5 R172 A5 R172 A5 R172 A5 R210 A6 R20 A7 R210 A8 R210 A8 R31 A7 R215 A8 R31 A7 R315 A8 R316 A8 R316 A8 R316 A8 R316 A8 R316 A8 R317 A7 Integrator (slow ranges) A8 R316 A8 R316 A8 R316 A8 R316 A8 R317 A7 Integrator buffer amplifier (slow ranges) A7 R31 A8 R316 A8 R316 A8 R317 A8 R316 A8 R317 A8 R316 A8 R317 A8 R317 A8 R316 A8 R317 A8 R317 A8 R316 A8 R317 A8 R31	A5 R142*	Slow transition time in the	
A5 R148  A5 R161 A5 R162  A8-adjust (working window)  A5 R213 A5 R230  A5 R230  A5 R136 A5 R217  A5 R171  A5 R171  A5 R172  A5 R210  A5 R210  A5 R80 A5 R81 A6 R81 A7  A8 R86 A8 R86 A8 R86 A8 R87		first integrator range (1-5µs)	520
A5 R161 A5 R162 A8-adjust (working window) A5 R213 A5 R230 A5 R230 A5 R136 A5 R217 A5 R171 A5 R171 A5 R172 A5 R172 A5 R210 A5 R210 A5 R80 A5 R81 A5 R80 A5 R81 A6 R81 A7 Integrator buffer amplifier (slow ranges) A8 R81 A8 R81 A8 R816 A8 R817		Integrator (working window)	E 10 (0) E 00
A5 R162 A5 R213 A5 R230 Integrator minimum current  5-22 A5 R136 A5 R217 Dual slope, slope equivalence 5-22 (5) A5 R171 A5 R172 Integrator (slow ranges)  5-16 (5), 5-22 A5 R210 Internal supply voltage for integrator  A5 R80 A5 R81 A5 R115  A8 R16 A8 R17 Integrator buffer amplifier (slow ranges)  5-16 (8)  5-16 (8)  5-16 (8)  5-22  5-22  5-22  5-23  5-24  5-25  5-26  5-26  5-27  5-28  5-29  5-20  5-20  5-20  5-20  5-20  5-21	A5 R148	mitegrator (working whitelet)	5-16 (8), 5-20
A5 R213 A5 R230  A5 R136 A5 R217  Dual slope, slope equivalence  5-22 (5)  A5 R171 A5 R171 A5 R172  Integrator (slow ranges)  5-16 (5), 5-22  A5 R80 A5 R81 A5 R81 A5 R15  A8 R16 A8 R16 A8 R17  Integrator buffer amplifier (slow ranges)  5-21	A5 R161	AQ adicat (condition coincide )	- 45 (5)
A5 R230  A5 R136 A5 R217  Dual slope, slope equivalence  5-22  5-22  5-22  5-22  5-22  5-22  5-22  5-22  5-22  5-22  5-26  5-22  5-26  5-22  5-26  5-20  A5 R80 A5 R81 Positive pulse baseline tracking A5 R115  A8 R16 A8 R16 A8 R17  Integrator buffer amplifier (slow ranges)  5-21	A5 R162	A8-adjust (working window)	5–16 (8)
A5 R136 A5 R217 Dual slope, slope equivalence  5-22 (5)  A5 R171 A5 R172 A5 R210 Integrator (slow ranges)  5-16 (5), 5-22  Internal supply voltage for integrator  5-16 (10) 5-20  A5 R80 A5 R81 A5 R115  A8 R16 A8 R17 Integrator buffer amplifier (slow ranges)  5-21	A5 R213	Integrator minimum accura	5 00
A5 R217  A5 R171  A5 R172  A5 R210  A5 R80  A5 R81  A5 R816  A8 R16  A8 R17  Dual slope, slope equivalence  5-22 (5)  5-16 (5), 5-22  5-16 (10) 5-20  5-16 (10) 5-20  5-23  5-23  5-21	A5 R230	integrator minimum current	5–22
A5 R171 A5 R172	A5 R136	Post dans dans as it is	
A5 R172	A5 R217	Duai stope, stope equivalence	5–22 (5)
A5 R172	A5 R171		
A5 R210 Internal supply voltage for integrator 5–16 (10) 5–20  A5 R80 A5 R81 Positive pulse baseline tracking 5–23  A8 R16 A8 R17 Integrator buffer amplifier (slow ranges) 5–21		Integrator (slow ranges)	5-16 (5), 5-22
A5 R80 A5 R81 Positive pulse baseline tracking 5–23  A8 R16 A8 R17 Integrator buffer amplifier (slow ranges)  5–21		Internal supply voltage for integrator	5_16 /10\ E 20
A5 R81 Positive pulse baseline tracking 5–23 A5 R115  A8 R16 A8 R17 Integrator buffer amplifier (slow ranges) 5–21	ļ	, and the state of	3-10 (10/ 5-20
A5 R115  A8 R16 A8 R17  Integrator buffer amplifier (slow ranges)  5-21	i i	Positive pulse beseling tracking	E_23
A8 R16 A8 R17 Integrator buffer amplifier (slow ranges) 5-21	l l	Ositive pulse paseille tracking	5-23
A8 R17 Integrator buffer amplifier (slow ranges) 5–21			
A8 H17	A8 R16		r 24
* Factory-selected	A8 R17	integrator butter amplifier (slow ranges)	5-21
	* Factory-selected		
	, , , , , , , , , , , , , , , , , , , ,		•
,			

Table 5-15. Test Equipment and Accessories for Internal Checks and Adjustments

INSTRUMENT	BRIEF SPECIFICATION	RECOMMENDED MODEL
Pulse Generator	10 MHz square wave output with 50% duty cycle	HP 8011A
Counter	Frequency range 0-50 MHz	5245L
	Prescaler plug-in	5252A
Oscilloscope		HP 180A with plug-ins 1801A and 1820A
Sampling Oscilloscope	Dual Channel, 1 GHz bandwidth, 1mV/div. sensitivity, sweep speeds 10ns/div. to 2s/div. 50- $\Omega$ input impedance.	HP 180A with plug-in 1810A
Digital Voltmeter	100V range to 4 significant figures. Accuracy $\pm 0.05\%$ $\pm 1$ digit.	HP 3440A with plug-in 3443A
Test Oscillator	Frequency range 10 Hz - 10 MHz	HP 651A
Test Oscillator	Frequency range 10 to 500 MHz	HP 3200B
Test Box		15265A

# **ACCESSORIES**

$50\Omega$ co-axial cable terminated with BNC male connectors (4 required)	HP 10120A
Connector BNC male to N female (2 required)	HP 1250-0077
Connector BNC male to N male (2 required)	HP 1250-0780
50 $\Omega$ Feed-through termination (2 required)	HP 11048B/C
Pulse Adder	HP 15104A
20dB Attenuator, $50\Omega$ (2 required)	HP 8491A

# Table 5-16. Test and Adjustments Performed by Test Box 15265A

# Classification of tests:

Class A These tests can only be performed using the Test Box.

Class B Conventional methods can be used instead.

Class C The Test Box checks a sub-function of Board A5. A final adjustment using the complete 8082A (i.e.

with Board A4 operational) is necessary.

NOTE: DVM must be floating.

Test No.	Class	Purpose	Selector setting	DVM reading
1		Self test	-14V	14V ± 0.5V
2	В	Transition time switch	S9A	
		function		<b>/</b>
		1n5n		<80mV
		5n-0.5m	İ	−25V ± 100mV
		(approx equal to A5 R166/167		,
		voltage to ground).		
3	В	Current source values:		<10mV
	1	transition time switch 1n-5n	Ω51	<10mV
			IQ48	285 ± 25mV
		transition time switch 5n—50n	<sup>I</sup> Q51	285 ± 25mV
		$(I_{Q51} \approx V_{A5R211}/147\Omega)$	ΙΩ48	85 ± 10mV
		$(I_{Q48} \approx V_{A5R213}/147\Omega)$	IDR	03 = 101114
		(For this measurement, connect collector A5 Q58 via approx	. 510 $\Omega$ to ground.	)
		(I <sub>DR</sub> ≈ V <sub>A4 R180/</sub> 38.3Ω)		(10mV = 1mA).
				DVM
Test	Class	Purpose	Selector	reading
No.			setting	
4		Trans time switch 1n-5n.		510mV ± 40mV
4a	В	Leading edge vernier CCW	ISUM	90mV ± 30mV
		leading edge vernier CW	ISUM	10mV ≈ 1mA
		(I <sub>SUM</sub> ≈ voltage across A4 R174 divided by 13,3 ohm)	1	255mV ± 20mV
. <b>4</b> b	В	Leading edge vernier CCW	LE	45mV ± 15mV
		leading edge vernier CW	<sup>1</sup> L€	10mV ≈ 1mA
_		(I <sub>LE</sub> ≈ voltage across A4 L21 divided by R <sub>L21</sub> ohm)	ITE	255mV ± 20mV
4c	В	Leading edge vernier CCW		45mV ± 15mV
		leading edge vernier CW (I <sub>TE</sub> ≈ voltage across A4 L20 divided by R <sub>L20</sub> ohm)	ITE	10mV ≈ 1mA
<b>4</b> d	В	Leading edge vernier CCW	IDR	285mV ± 30mV
40		Leading edge vernier CCW (For this measurement, connect collector of A5 Q58 via approx 510 $\Omega$ to ground.)		nd.)
		leading edge vernier CW	IDR	20mV + 10mV / 5mV
		(For this measurement, connect collector of A5 Q58 via appr		
		(IDR ≈ voltage across A4 R180 divided by 38,3 ohm)		10mV ≈ 1mA
		(Value depends on A5 R142 – factory selected.)		
4e	A, C	Rotating leading edge vernier		
		from CW to CCW	DIFI	constant
		preadjust A5 R136	DIF	$\Delta$ U ± 20mV 100mV ≈ 1mA
	-	preadjust A5 R217	DIFI	minimum
		,,		- 25mV ± 15mV
	1	l	i	1

Table 5-16. (Continued)

Test No.	Class	Purpose	Selector setting	DVM reading
5	A C	Leading and trailing edge vernier CCW, on switching trans time from 1n-5n to greater ranges no difference in ILE (ITE) should be present: adjust A5 R172 adjust A5 R171 (See Table 5-22) Trans time switch to 5n-50n. Leading and trailing edge vernier CW. Tests 4e and 5 must be done first:	ILE ITE	ΔU±1mV ΔU±1mV 10mV≈1mA
		preadjust A5 R213 preadjust A5 R230 (see Table 5–22)	ILE ITE	9mV ± 1mV 9mV ± 1mV 10mV ≈ 1mA
Test No.	Class	Purpose	Selector setting	DVM reading
7	Α	Check Test 4e in the 5-50n	DIFI	as Test 4e
8	В	Clamp voltages adjust: Adjust A5 R148	UCIL	-14.78V ± 5mV
	1	(voltage A5 Q32 emitter to ground) Adjust A5 R147	UCIH	-14.50V ± 5mV
		(voltage A5 Q31 emitter to ground) adjust A5 R161 (depends on A5 R147)	U <sub>СВН</sub>	-13.54V ± 5mV
		(voltage A5 Q35 emitter to ground) adjust A5 R162 (depends on A5 R148)  NOTE: refer to Table 5-20	U <sub>CBL</sub>	-14.33V ± 5mV
Test No.	Class	Purpose	Selector setting	DVM reading
9	В	Norm/compl voltages: switching the norm/comp-switch  (U <sub>NC1</sub> ≈ voltage from A5 X5 pin 1 to ground) (U <sub>NC2</sub> ≈ voltage from A5 X5 pin 2 to ground)	U <sub>NC1</sub> U <sub>NC2</sub>	-11.75V/-11.05V ± 100m <sup>1</sup> -11.05V/-11.75V ± 100m <sup>1</sup>
10	Б	Internal integrator supply voltage: adjust A5 R210 (UOUT ≈ voltage emitter A5 Q30 to ground)	<sup>U</sup> OUT	-6.90V -7.2 to -6.77V is permissible. See Table 5-20).

Table 5–16. (Continued)

Test No.	Class	Purpose	Selector setting	DVM reading	
11	В	Amplitude vernier check: amplitude vernier CW (attenuator not in ECL-mode).	IV801 IV802 IV40	0V + 0mV/- 0V + 0mV/- 0V + 0mV/-	-20mV -60mV
		amplitude vernier CCW (attenuator not in ECL-mode).	I <sub>V</sub>   V801   V802   V40   I <sub>V</sub>	2.1V ± 3.4V ±	-80mV 0.2V 0.2V 0.2V 0.3V
		attenuator in ECL-mode	V V801 V802 V40	3.35V ± 1.35V ± 3.4V ±	0.3V 0.15V 0.3V 0.25V
•		Scale factors:			
1		$^{I}$ V801, $^{I}$ V802: 5V $\approx$ 80mA $^{5}$ V $^{4}$ 0mA $^{5}$ V: 5V $\approx$ 200mA			
		$\begin{array}{lll} \text{IV801} & \approx & \text{VA5 Q20 EMITTER-GND/5 ohm.} \\ \text{IV802} & \approx & \text{VA5 Q21 EMITTER-GND/5 ohm.} \\ \text{IV40} & \approx & \text{VA5 Q21 EMITTER-GND/10 ohm.} \\ \text{IV} & \approx & \text{IV801 + IV802 + IV40} \\ \end{array}$	:		
Test No.	Class	Purpose	Selector setting	DVM reading	
12	С	Pos pulse: tracking offset	IPOS A	typ. 1.9/5.3V	
		This adjustment (A5 R80, R81, R115) must be made with the whole instrument (see Table 5–23).	IPOS B	typ. 1.9/5.3V	
13	В	Ext dc-offset: ext offset on, vernier CCW + CW (not ECL)	IDC A	± 2.25V ± 2.25V	±0.15V ±0.15V
		(can be measured at each of the 2 8082A pulse output jacks, which must be terminated by 50 ohm).			
		in ECL position	IDC A	-0.45V -0.45V	± 50mV ± 50mV

## Table 5-17. Power Supply

#### STEP

#### 1 8082A settings:

1 REPETITION RATE	250M-100M
2 VERNIER	CCW
3 NORM/DOUBLE	NORM
4 DELAY	2n-5n
5 VERNIER	CCW
6 EXT INPUT LEVEL	middle
7 MODE SWITCH	EXT TRIG
8 WIDTH	2n-5n
9 VERNIER	CCW
10 TRANSITION TIME	1n-5n
11 LEADING VERNIER	CCW
12 TRAILING VERNIER	CCW
13 AMPLITUDE	0.4-1
14 AMPLITUDE	0.4-1
15 AMP VERNIER	CW
16 OFFSET VERNIER	middle
17 OFFSET SWITCH	OFF
19 NORM/COMPL	NORM
20 NEG/POS SWITCH	NEG
24 SLOPE POLARITY	POS

- 2 Set the DVM to auto-range and connect it between GND and the voltage TP's.
- 3 Measure and adjust the following points:

TP + 10V	Adjust A2 R7 to $+ 10V \pm 20mV$
TP - 5V	Adjust A2 R10 to $-5V \pm 10 \text{mV}$
TP - 10V	Adjust A2 R21 to $-10V \pm 20mV$
TP 25V	Adjust A2 R29 to $-25V \pm 30 \text{mV}$

Table 5-18. Rep. Rate

#### STEP

1 8082 settings:

3 DOUBLE /NORMAL	NORMAL
4 DELAY	2n-5n
5 VERNIER	CCW
7 MODE SWITCH	NORM
8 WIDTH	2n-5n
9 VERNIER	CCW
10 TRANSITION TIME	1n-5n
11 LEADING VERNIER	CCW
12 TRAILING VERNIER	CCW

Counter Setting:

Sensitivity 1V

Max. count rate 350 MHz

2 Measure and adjust the frequency as follows:

8082	2A ,	COUN- TER		
REP. RATE	VERNIER 2		ADJUST	RESULT
250M-100M 100M-10M	ccw cw	0.1 ms 0.1 ms	A3 R6 A3 R5	255 ± 0.5 MHz 9.3 ± 0.1 MHz

A3 R6 affects highest rep rate only (CCW).

A3 R5 affects all ranges below and including 100M-10M (CW).

A3 R77 affects duty cycle at 100MHz (10MHz)

Table 5-19. Delay and Width (Verniers CW)

#### STEP

1 8082A settings:

1 REPETITION RATE 10M-1M

2 VERNIER

CW

4 DELAY

as required, but not 2n-5n

**5** VERNIER

CW

8 WIDTH

as required, but not 2n-5n

9 VERNIER

CW

- 2 A4 R38 affects the delay in all ranges (except 2n–5n) when the vernier is CW. Range of values for A4 R38 is 100 k $\Omega$  upwards, increase in resistance increases delay. This adjustment is done at the factory.
- 5 Max width in all ranges (except 2n–5n) can be increased when the vernier is CW) by increasing A4 R88. Range of values is 100 k $\Omega$  upwards. This adjustment is done at the factory.

Table 5-20. Amplitude, Risetime, Overshoot (1n-5n Transition Time)

8082A settings:

10M-1M (VERNIER for 5 MHz) **1 REPETITION RATE** NORM 3 DOUBLE/NORM **EXT WIDTH** 7 MODE SWITCH SQUARE WAVE 8 WIDTH 10 TRANSITION TIME 1n-5n, VERNIERS CW 13 AMPLITUDE 2.0 - 5.014 AMPLITUDE 2.0 - 5.018,21 Both outputs must be terminated by 50 ohms at all times

NEG

20 NEG/POS SWITCH

2 Verify tests 8, 10 in Table 5-16.

3 Adjust A4 R214, R232 for max output voltages > 5.15V and < 5.35V, then optimize settings for acceptable pulse shape.

NOTE: A5 R210 may be re-adjusted if difficulty is experienced in reaching the maximum amplitude with the A4 R214/232 adjustment (-7.2V to -6.7V is permissible; 100mV variation means about 60mV output amplitude variation). Increasing voltage in negative direction increases the overshoot. See Table 5-16 test 10.

Re-adjust A4 R214 for a baseline shift > 30mV and < 70mV (worst 4 Re-adjust A4 R214 for a baseline shift > 30mV and < 70mV (worst case: both channels, norm and compl, ampl. vernier CW, neg. pulse). Observe baseline while rotating amplitude vernier between CW and CCW. The best setting has been found west pertability to be - 40mV.

Set amplitude vernier CCW (i.e. 2V output). If a hook is apparent at the start of at min. vernier the positive going edge, adjust A4 R214, then A4 R232, for an acceptable pulse shape. Repeat steps 3 and 4 adjust for best compromise.

- Turn amplitude vernier CW and transition time verniers CCW. Adjust 6 A4 R253 for  $-6.3 \pm 0.1 \text{V}$  at R253 wiper (TP 16).
- Set 8082A transition time to 1n-5ns LE-vernier CW. Select A5 R142 for a transition time > 6.5ns and < 7.5ns in the worst case of both edges. NOTE: A5 R142 has possible values 1.87 K ... 3.01 K.
- Verify tests 4, 8, 10 in Table 5-16. Measure transition times, overshoot and ringing. LE-Vernier CCW. NOTE:
  - 1. fast edges have greater overshoot and ringing than slower ones.
  - 2. positive output pulses will be slightly faster than negative ones.
  - 3. A4 R214, R232 also affect transition time and overshoot (but see steps 3, 4, 5 above).
  - 4. Transition times and overshoot are affected by the inductors (wires) parallel to A4 CR32, CR33. A 1-cm variation of wire length is allowed, corresponding to 30ps in transition time, 0.7 % absolute in overshoot.
  - 5. It is permissible to re-adjust UCIH (Table 5-16 Test 8) in the range 14.44V to — 14.51 V ± 5mV, and UCIL in the range - 14.78V to - 14.84 V ± 5mV, without re-adjusting UCBH and UCBL.

This decreases the transition times by about 40ps and increases overshoot by about 1 % absolute compared with the values given in Table 5-16 Test 8. If these adjustments are made, repeat steps 3 to 5.

# Table 5–21. Amplitude, Risetime, Overshoot (Slower Transition Times)

## Table 5-22. Pulse Shape and Transition Times

STEP

1 8082A settings: as previous table, except:

1/2 REP RATE about 2 MHz
10 TRANSITION TIME 5n-50n, VERNIERS CW
13/14/15 AMPLITUDE max.

- 2 Verify tests 3b, 4e, 5, 6, 7, 8, 10 of Table 5-16.
- 3 Adjust A8 R16, R17 for max output voltage > 5.1V then optimize settings for acceptable pulse shape.
- 4 If possible, re-adjust A8 R16 for a baseline shift  $\approx$  40mV, ampl. vernier CW (worst case: both channels, norm and compl).
- 5 Set amplitude vernier CCW (i.e. 2V output). If a hook is apparent at the start of the positive going edge, adjust A4 R16, then A4 R17, for an acceptable pulse shape. Repeat steps 3 and 4 and adjust for best compromise.
- With max output voltage, and with transition time verniers CW, compare the output amplitudes (in both channels and for norm and compl. pulses) in the fastest transition time range with those in any of the slower ranges. If the pulse amplitudes are not within 100mV of each other, increase the smaller amplitude (step 3 of this, or previous, table).

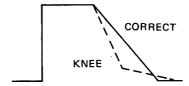
STEP

1 8082A settings:

10 TRANSITION TIME 11 LEADING VERNIER 12 TRAILING VERNIER 5n-50n CCW

CCW

2 If trailing edge has a knee, adjust A5 R136, R217 for a constant slope.



3 Set leading edge vernier CW. Rotate trailing edge vernier, observing leading edge.

Re-adjust A5 R136 for a constant leading edge slope.

4 Set trailing edge vernier CW. Rotate leading edge vernier observing trailing edge.

Re-adjust A5 R136 for a constant trailing edge slope. (A 5% variance in slope is usual).

- 5 Increase rep. rate until waveform is triangular (may possibly be distorted). Verify that the amplitude decreases.
- 6 Re-adjust A5 R171, R172 for a clean triangular waveform.
- 7 Repeat steps 2 to 4.
- 8 Set both transition time verniers CW and adjust A5 R213 (230) for leading (trailing) edge times of 65ns. (This adjustment affects only the CW transition time of range 5n-50n and slower).
- 9 Repeat steps 2 to 8 if adjustment was made in step 8.

**PULSE** 

#### **ADJUSTMENTS**

Table 5-23. Positive Pulse Baseline

#### STEP

- Set 8082A for positive output pulses. 1
- Observe right channel baseline shift while rotating amplitude vernier. Adjust A5 R81 for minimum baseline shift (a fixed dc offset may be present).
- Adjust A5 R80 for zero dc offset in right channel. 3
- Adjust A5 R115 for zero dc offset in left channel.
- Carry out septs 1 to 4 for normal and compl modes, with transition times of 1n-5n and 5n-50n, and with transition time verniers CCW and CW. Re-adjust A5 R80, R81, R115 for the best compromise. Baseline shift should not exceed ± 100mV in the 5-2V attenuator range.

Table 5-24. Width

#### STEP

8082A settings:

1 REP RATE	ABOUT 20 MHz
8 WIDTH	2-5ns
9 WIDTH VERNIER	CW
10 TRANSITION TIME	1 —5ns
11 VERNIER	CCW

- 2 Set A4 R43 for a 7ns pulse width.
- Set 8082A to max rep rate, min delay, min width. Set A4 R240 for a pulse width of about 2.00 ns or about 50 %duty cycle. Optimize adjustments for the worst case of norm/compl. right/left channel.
- Set pulse width selector to SW. Observe pulse amplitude and baseline shift while varying rep rate between 250 MHz and 100 MHz. (±3 % variance is usual).

Table 5-25. Double Pulse

#### STEP

8082A settings:

1, 2 REP RATE	10 MHz approx
8, 9 WIDTH	Min
3 DOUBLE PULSE/NORM	DOUBLE PULS
4 DELAY	5n-50n
5 VEDNIER	CCM

- Verify pulses are equal in width. First pulse width can be adjusted by selecting A4 R42 values in the range 1.6 to 10 k $\Omega$ . This adjustment is done at the factory.
- If A4 R42 is changed, repeat tables 5-24 and 5-25.

Table 5-26. Gate

#### STEP

8082A settings:

1 REP RATE	Max
8, 9 WIDTH	Min
7 MODE SWITCH	GATE

- Drive 8082A from a 5 MHz, 50 % duty cycle source (approx values).
- Adjust A3 R87 for a correct first pulse.
- Set the 8082A to SQUARE WAVE.
- Re-adjust A3 R87 and A4 R240, if necessary. (If re-adjusted, verify step 3).
- If A3 R87 adjustment is not successful, connect resistor between A3 J3 (SW output, A3 U2 pin 7) and -10V (at A3 C13). Values lie in range 1.2 k $\Omega$  to 5.6 k $\Omega$ .

NOTE: For instruments with serial numbers 1410G00270 and below: disconnect ground leads at A4 Q69 and Q70 of the coax cables which link the width circuit (A4 U4) to the level shifter (A4 Q69, Q70).

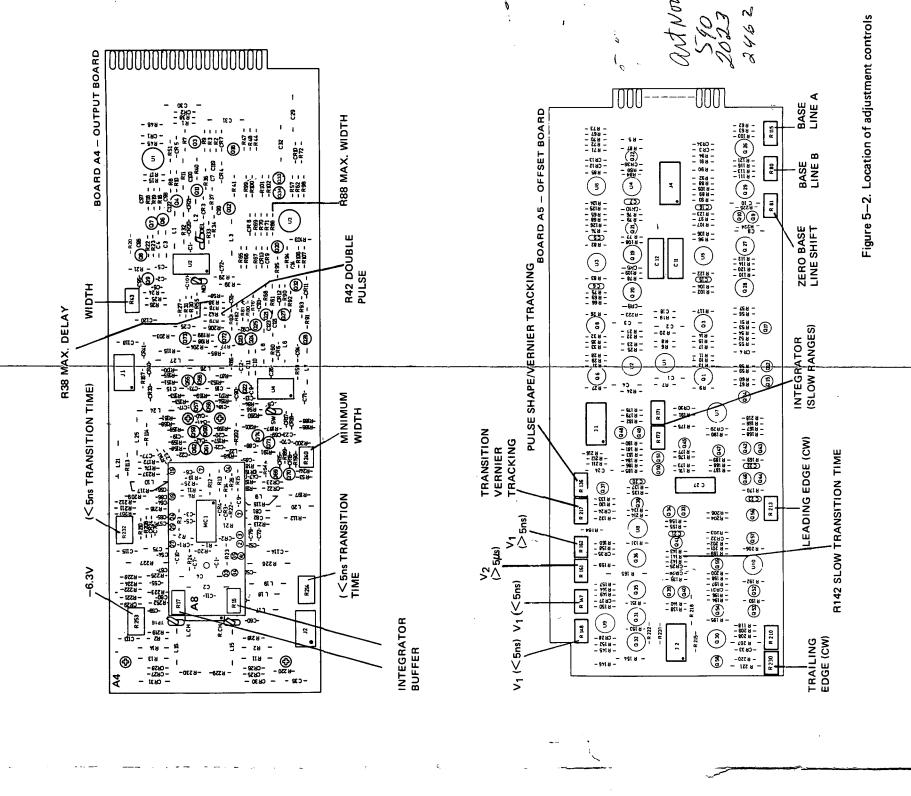
Repeat steps 2 to 5.

Set ampl. vernier (15) CCW (i.e. 2V). Vary the frequency and duty cycle of the gate source and observe the output, from both channels of the 8082A, while varying the frequency (from 250 MHz -100 MHz) in the following modes: NEG, POS, NORM, COMP and SQUAREWAVE, PULSE. The pulse fluctuation should be less than 5 % while the pulse width should not exceed 2.3 ns.

## **SAFETY CHECK**

#### Table 5-27. Safety Check

- 1 Disconnect power cord from line, visually inspection interior for any sign of abnormal internally generated heat, such as discolored printed circuit boards or components, damaged insulation, or evidence of arcing. Determine cause and remedy.
- 2 Check resistance from 8082A cabinet to ground pin on power plug with suitable ohmmeter. The reading must be less than one ohm. Flex the power cord while making this measurement to detect any intermittent discontinuity. Check internal ground connections on boards and frame. Also check resistance of any front or rear panel ground terminals marked  $\frac{1}{2}$
- 3 Check resistance from 8082A cabinet to line and neutral (tied together) with the power switch on and the power source disconnected. The minimum acceptable resistance is two megohms. Replace any component which results in a failure or refer to production Memo or Service Note issued by product division for alternate action.
- 4 Check line fuse to verify that the proper value is installed.
- 5 Check that the plastic safety cover is installed inside the base of the 8082A, below the line fuse.
- 6 Check that all coaxial and flat cables inside the 8082A are properly connected. Check that all boards and the heatsink on the chassis are properly connected. Make sure that board A8 is properly connected to board A4.
- 7 Inform Hewlett-Packard (internally, the responsible product division) of any repeated failures in the above tests or any other safety features.



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10000001

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+10V ADJUST

-5V ADJUST

-10V ADJUST

–25V ADJUST BOARD A2 - POWER SUPPLY

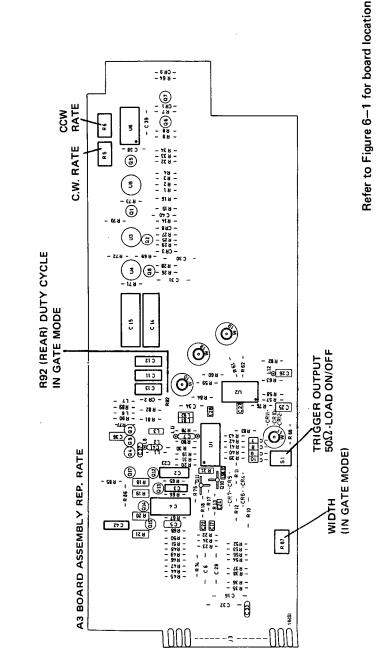
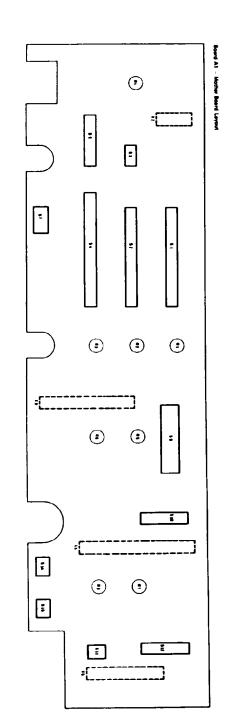
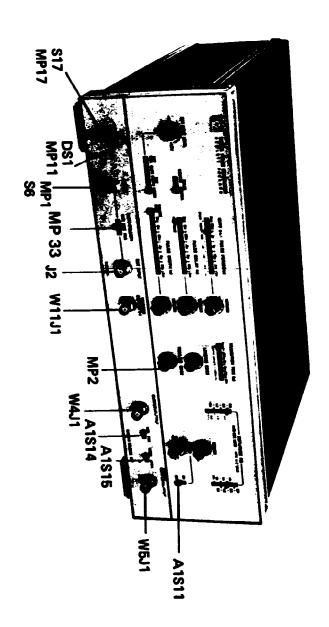


Figure 6-1. Mainframe parts identification





SECTION 6-

-DIAGRAMS AND REPLACEABLE PARTS

### INTRODUCTION <u>[</u>

sales representative or to:

quidance only and failure to observe identical results Waveforms shown with the circuits are included for should not be automatically taken as indication of a fault. Tables 6-1 and 6-2 provide information relating to the replaceable parts lists and the circuit diagrams. This section contains the circuits, component location diagrams and the lists of replaceable parts.

# ORDERING INFORMATION 6-3

### General **6**-4

The replaceable parts tables list parts in alpha-numerical order of their reference designators and indicate the description and HP stock number of each part, together with any applicable notes. 6-5

## To order a replacement part, address order or enquiry either to your authorized Hewlett-Packard 9-9

Mountain View, California 94040 CUSTOMER SERVICE Hewlett-Packard Company, 333 Logue Avenue, or, in Western Europe, to:

Hewlett-Packard (Schweiz) SA Rue du Bois-du-Lan 7

1217 Meyrin 2 Geneva

- Specify the following information for each part: 6-7
- Model and complete serial number of instrument. 8
- Hewlett-Packard stock number.
- Circuit reference stock number. Description G C Q

To order a part not listed, give a complete description of the part and include its function and location.

Table 6–1. Component Designators

								Components mounted on an assembly are identified by	prefixing the component reference designator by the	assembly designator. Thus, for example, A4CR9 is diode	9 on assembly 4.		Designators of components mounted on the frame	receive no prefix.
= micro-circuit	6n <sub>l</sub> d	<ul><li>transistor</li></ul>	= resistor	= thermistor	<ul><li>switch</li></ul>	<ul> <li>transformer</li> </ul>	terminal board	<ul><li>test point</li></ul>	<ul><li>vacuum, tube, neon</li></ul>	bulb, photocell, etc.	<ul> <li>voltage regulator</li> </ul>	= cable	= socket	= crystal
11	u	M	B	H	M		91	H	H	孟	H	H	H	M
<b>)</b>	۵	a	Œ	ВT	တ	<b>-</b>	<b>18</b>	4	>		<b>&gt;</b>	≯	×	<b>&gt;</b>
= assembly	motor	= battery	= capacitor	= coupler	= diode	- delay line	= lamp	= fuse	= filter	= heater	= jack/connector	= relay	= inducer	= meter

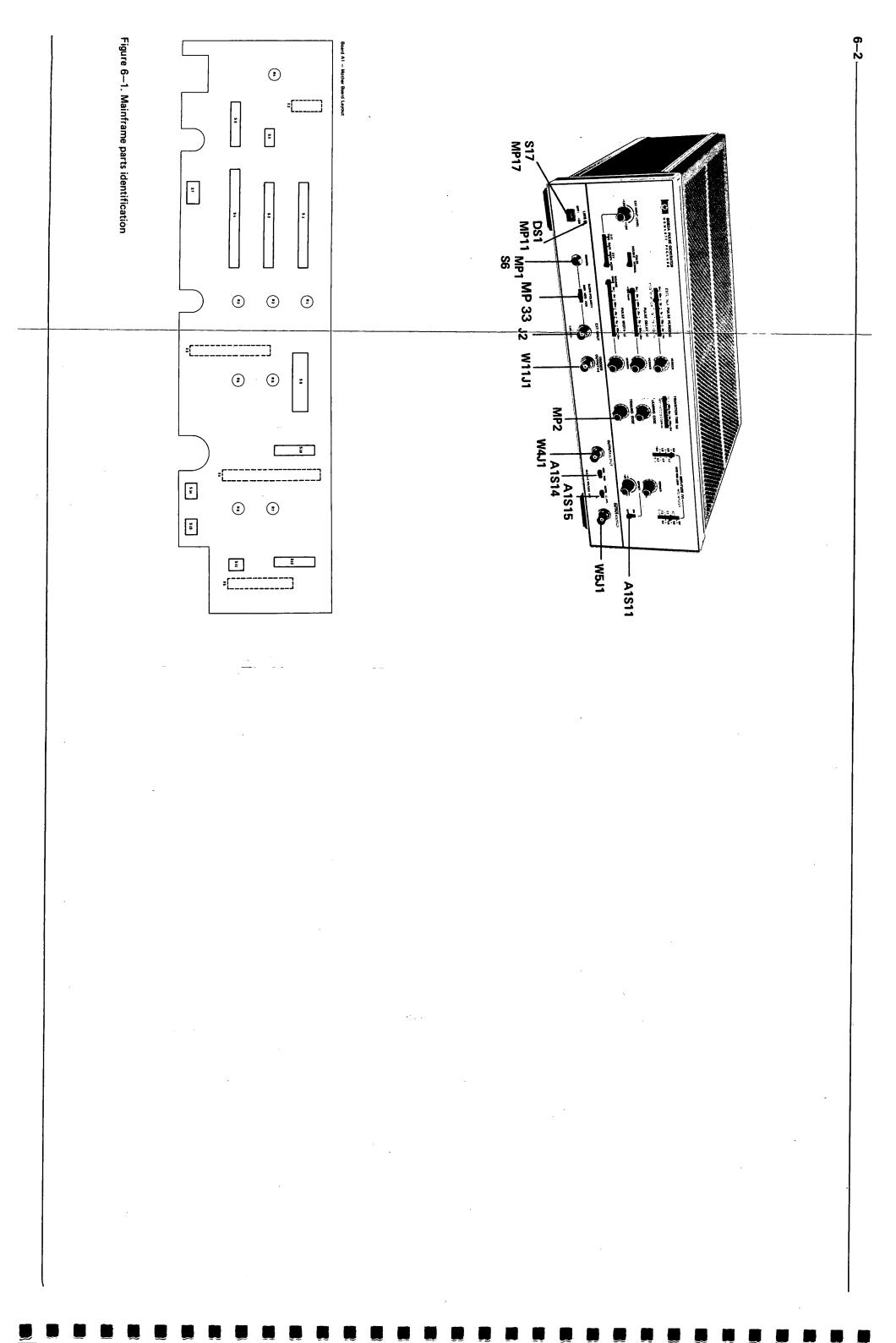
Table 6-2. Manufacturers' Code Numbers and Abbreviations for Parts List

Abbreviations

Σ	AMPERE(S)	ı	HENRY(IES)	NPN	NEGATIVE POSITIVE	RW<	REVERSE WORKING
ASSEMBLY	>	Ŧ	MERCURY		NEGATIVE		VOLTAGE
		Ŧ.	HEWLETT PACKARD	NSR	NOT SEPARATELY	!	
30ARD(S)	Si	<b>1</b> 2	HERTZ		REPLACEABLE	8.B	SLOW BLOW
DER	BINDER HEAD					SC B	SILICON CONTROLLED
BANDPASS	ASS	<u>.</u>	INTERMEDIATE FREO				RECTIFIER
		MPG	IMPREGNATED	080	ORDER BY	SE	SELENIUM
Ę	CENT1 (10.2)	INCD	INCANDESCENT		DESCRIPTION	SEC	SECOND(S)
CARBON	·	INCL	INCL UDE(S)	Ю	OVAL HEAD	SECT	SECTION(S)
Z	COUNTERCLOCKWISE	INS	INSULATION(ED)	ŏ	OXIDE	ى تە	SILICON
Ş	CERAMIC	<u>►</u> Z	INTERNAL			SIL	SILVER
夁	CABINET MOUNT ONLY			۵	PEAK	ಸ	SLIDE
â	COAXIAL	¥	KILO (103)	<u>გ</u>	PRINTED (ETCHED)	ಹಿ	SINGLE POLE
ш	COEFFICIENT	<b>K</b> G	KILOGRAM		CIRCUIT(S)	<u>چ</u>	SPECIAL
Š	COMPOSITION			F	PICOFARADS	ST	SINGLE THROW
ź	CONNECTOR(S)	LB	POUND(S)	H	PHILLIPS	STD	STANDARD
≠	CATHODE RAY TUBE	3	LEFT HAND	≥	PEAK INVERSE		
న	CLOCKWISE	Ľ.	LINEAR TAPER		VOLTAGE(S)	Δ	TANTALUM
		507	LOGARITHMIC TAPER	š	POSITIVE NEGATIVE	2	TIME DELAY
ច	DEC! (10.1)	LPF	LOW PASS FILTER(S)		POSITIVE	TFL	TEFLON
۲	DEPOSITED CARBON	LVR	LEVER	8	PART OF	<b>1</b> 5	TOGGLE
)	DOUBLE POLE			<b>0</b> 80	PORCELAIN	THYR	THYRISTOR
⊃	DOUBLE THROW	Σ	MILL! (10 <sup>-3</sup> )	Š	POSITION(S)	=	TITANIUM
		MEG	MEGA (10 <sup>6</sup> )	5	POTENTIOMETER(S)	TNLDIO	TUNNEL DIODE(S)
ш	ELECTROLYTIC	MET FILM	METAL FILM	d- d-	PEAK TO PEAK	짇	TOLERANCE
ठ	ENCAPSULATED	MET OX	METAL OXIDE	PRGM	PROGRAM	TRIM	TRIMMER
F	EXTERNAL	MFR	MANUFACTURER	&	POLYSTYRENE		
		MINAT	MINIATURE	<b>≩</b>	PEAK WORKING	<b>-</b>	MICRO (10.6)
æ	-ARAD(S)	MOM	MOMENTARY		VOLTAGE		
_	FIELDEFFECT	MTG	MOUNTING			>	VOLTS
₹	TRANSISTOR(S)	¥	MYLAR	RECT	RECTIFIER(S)	VAR R	VARIABLE
4	FLAT HEAD			R	RADIO FREQUENCY	<b>VDC</b> ₩	DC WORKING VOLT(S)
ب	FILLISTER HEAD	z	NANO (10.9)	R	RADIO FREQUENCY		
FIXED	۵	S/C	NORMALLY CLOSED		INTERFERENCE	₹	WATT(S)
		Z	NEON	Ŧ	ROUND HEAD	<b>*</b>	WITH
ĕ	GIGA (109)	0/ <b>Z</b>	NORMALLY OPEN		9	<u>≥</u>	WORKING INVERSE
É	GERMANIUM	NOP PO	NEGATIVE POSITIVE		RIGHT HAND		VOLTAGE
GLASS	Š		ZERO (ZERO TEMPER	SMO M	RACK MOUNT ONLY	<b>0/</b> ≱	WITHOUT
ಠ	GROUNDED		ATURE COEFFICIENT)	RMS	ROOT MEAN SOUARE	₹	WIREWOUND
;				<u>}</u>	, , , , , , , , , , , , , , , , , , , ,		

Manufacturer's Code Numbers

	HANUFAL TURER NAME	ADONESS	<b>\$000</b>
DEUTSCHE VITRUMM GMBH L CO	7 00	GERMANY	
ILLUMINATED PRODUCTS INC MAP INC	ž	MARKET CA	17105
ALLEN-BRADLEY CO		HILMAUKEE WI	53212
TEXAS INSTA INC SEMICOND CAPAT DIV	NO CAPAT UIV	DALLAS 1X	75231
FERROXCUBE CORP		SAUGERTIES MY	171
ACA CORP SOLID STATE DAY	<b>710</b>	SOMMERVILLE NJ	2
AUI PYROFILM CORP		MAIPPARY NJ	1946
MUTORULA SEMICONDUCTOR PRUDUCTS	t PRUDUCTS	PHOEMIX A2	2000
FAIMCHILD SEMICONDUCTOR DIV	N 0 14	MOUNTAIN VIEW CA	į
TRM INC BOOME DIV		BOONE NC	<b>20407</b>
		DOVER IN	03120
	^10 E	RALEIGH MC	2 2 2
U S CAPACITOR CORP		BURGANK CA	120
MEPCO/FLECTRA CORP		MINERAL WELLS TX	7557
STAMPORD APPLIED ENGINEERING INC	ERING INC	SANTA CLARA CA	456
GOWANDA ELECTHONILS CORP	•	CONANDA NY	<u> </u>
CUMMING GLASS WORKS (B	(BRADFORD)	BRADPORD PA	101
SPECIALTY COMMECTOR CO INC	) i c	INDIANAPOLIS IN	+4327
HENLETT-PACHAND CO CORPORATE	PORATE HU	PALO ALTO CA	37
BULRNS INC TRIMPOT PROU UIV	N 010	RIVERSIDE CA	45907
SPRAGUE ELECTRIC CO		NORTH ADAMS MA	01247
BUSSMAN MFG DIV OF MCGRAW-EDISON CO	SAMEDISON CO		1361
TRM ELEK COMPONENTS C	CINCH DILV	ELK GROVE VILLAGE IL	<b>1</b>
BECKMAN INSTRUMENTS INC HELIPOT DIV	NC HELIPOT DIV	FULLERTON CA	92634
TRM INC PHILADELPHIA DIV	<b>01</b> v	_	1010
LITTELFUSE INC		DES PLAINES IL	11001
SH COMPANY		ST PAUL PP	10155
C-W INDUSTRIES		WARRINSTER PA	164
u		CHICAGO IL	
ALCO ELECTRONIC PRUD	PAUDUCTS INC	LAWRENCE MA	010



SECTION 6-

-DIAGRAMS AND REPLACEABLE PARTS

### INTRODUCTION <u>1</u>

sales representative or to:

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Mountain View, California 94040 CUSTOMER SERVICE Hewlett-Packard Company, 333 Logue Avenue, or, in Western Europe, to:

Hewlett-Packard (Schweiz) SA Rue du Bois-du-Lan 7

1217 Meyrin 2 Geneva Specify the following information for each

part:

- Model and complete serial number of instrument. 8
- Hewlett-Packard stock number.
- Circuit reference stock number.
- Description G G E

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Table 6–1. Component Designators

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= micro-circuit	Bnjd	transistor	= resistor	thermistor	switch	<ul><li>transformer</li></ul>	<ul><li>terminal board</li></ul>	= test point	= vacuum, tube, neon	bulb, photocell, etc.	<ul> <li>voltage regulator</li> </ul>	cable	= socket	crystal
M	II	M	H	H	ŧŧ		H	11	11	3		Ħ	11	M
>	۵	a	Œ	F	S	<b>-</b>	<b>18</b>	٩	>		<b>%</b>	3	×	>
= assembly	motor	= battery	= capacitor	= coupler	= diode	- delay tine	= lamp	= fuse	= filter	heater	= jack/connector	relay	= inducer	meter
H	Ħ	ii						M		H	II	H	H	N
∢	8	ВТ	ပ	ပ္ပ	ဌ	٦	DS	щ	工	<u>=</u>	_	¥	_	Σ

ble $6-2$ . Manufacturers' Code Numbers and Abbreviations for Parts List	
Table 6–2. Manufacturers' Cod	Abbreviations

<	13/3030144	1	DENDYSEC	Non	AVITION AVITAGAN	/WG	SNINBOW BORENER
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	X (2) (4) (4)	: 3	MEBCIBX	: :	NEGATIVE		VOLTAGE
ASST	ASSEMBL 1	2 :	MERCON !	9			100
		Ì	HEWLETT PACKARD	ZZZ	NOT SEPANAIELY		
윱	BOARD(S)	<b>7</b>	HERTZ		REPLACEABLE	S C C	SLOW BLOW
Æ	BINDER HEAD					SCB R	SILICON CONTROLLED
8	BANDPASS	Ŧ	INTERMEDIATE FREG				RECTIFIER
		MPG	IMPREGNATED	080	ORDER BY	S	SELENIUM
U	CENT1 (10 ?)	INCD	INCANDESCENT		DESCRIPTION	SEC	SECOND(S)
CAR	CARBON	INCL	INCLUDE(S)	F	OVAL HEAD	SECT	SECTION(S)
CCW	COUNTERCLOCKWISE	INS	INSULATION(ED)	ŏ	OXIDE	2	SILICON
CER	CERAMIC	ī	INTERNAL			SIL	SILVER
CWO	CABINET MOUNT ONLY			۵	PEAK	ಸ	SLIDE
COAX	COAXIAL	¥	KILO (10 <sup>3</sup> )	<b>ج</b>	PRINTED (ETCHED)	ჵ	SINGLE POLE
COEF	COEFFICIENT	<b>K</b> G	KILOGRAM		CIRCUIT(S)	چ ھ	SPECIAL
COMP	COMPOSITION			7	PICOFARADS	ST	SINGLE THROW
CONN	CONNECTOR(S)	<b>LB</b>	POUND(S)	PH	PHILLIPS	STD	STANDARD
CRT	CATHODE-RAY TUBE	3	LEFT HAND	₹	PEAK INVERSE		
<u>₹</u>	CLOCKWISE	L N	LINEAR TAPER		VOLTAGE(S)	TA	TANTALUM
		10G	LOGARITHMIC TAPER	PN PN	POSITIVE NEGATIVE	2	TIME DELAY
٥	DECI (10.1)	PF	LOW PASS FILTER(S)		POSITIVE	TFL	TEFLON
DEPC	DEPOSITED CARBON	LVR	LEVER	8	PART OF	<b>1</b> 6L	TOGGLE
ది	DOUBLE POLE			<b>8</b>	PORCELAIN	THYR	THYRISTOR
ΤΟ	DOUBLE THROW	Σ	MILLI (10 <sup>.3</sup> )	S S	POSITION(S)	=	TITANIUM
		MEG	MEGA (10 <sup>6</sup> )	5	POTENTIOMETER(S)	TNLDIO	TUNNEL DIODE(S)
ELECT	ELECTROLYTIC	MET FILM	METAL FILM	<b>b</b> .	PEAK-TO-PEAK	<u></u> 전	TOLERANCE
ENCAP	ENCAPSULATED	MET OX	METAL OXIDE	PRGM	PROGRAM	TRIM	TRIMMER
EXT	EXTERNAL	MFR	MANUFACTURER	&	POLYSTYRENE		,
		MINAT	MINIATURE	<b>}</b>	PEAK WORKING	>	MICRO (10.6)
Ŀ	FARAD(S)	MOM	MOMENTARY		VOLTAGE		
FET	FIELD-EFFECT	MTG	MOUNTING			>	VOLTS
	TRANSISTOR(S)	¥	MYLAR	RECT	RECTIFIER(S)	<b>/</b> A	VARIABLE
Ŧ	FLAT HEAD			Æ	RADIO FREQUENCY	<b>VDCN</b>	DC WORKING VOLT(S)
FILH	FILLISTER HEAD	z	NANO (10.9)	E.	RADIO FREQUENCY		
FXD	FIXED	S/Z	NORMALLY CLOSED		INTERFERENCE	}	WATT(S)
		¥	NEON	Ī	ROUND HEAD	<b>*</b>	WITH
ပ	GIGA (109)	0/ <b>Z</b>	NORMALLY OPEN		OR	<b>≥</b>	WORKING INVERSE
36	GERMANIUM	o O	NEGATIVE POSITIVE		RIGHT HAND		VOLTAGE
ฮ	GLASS		ZERO (ZERO TEMPER	SW OM OM	RACK MOUNT ONLY	<b>⊘</b> / <b>X</b>	WITHOUT
GRD	GROUNDED		ATURE COEFFICIENT)	RMS	ROOT MEAN SOUARE	₹	WIREWOUND

Manufacturer's Code Numbers

Ī																																l
5		CD024	1718	53212	78231	121	1/100	07461	1560	1	20607	03120	2766	10614	76067	45650	2	16761	<b>++127</b>	12.1	12907	01247	43617	10091	15434	<b>8</b> 101	1001	10166	1011	3	100	
ALLON E SS	GER MANY	ANATEIN CA	HARA ISBURGO PA	HILMAKEE WI	DALLAS TX	SAUGERTIES MY	SOMMERVILLE NJ	MAIPPARY NO	PHOEMEX AZ	MOUNTAIN VIEW CA	BOOME NG	DOVER NO	RALETON MC	BURBANK CA	MINERAL MELLS TX	SANTA CLARA CA	CONANDA NY	BRADPOND PA	INDIANAPOLIS IN	PALO ALTO CA		NORTH ADAMS MA		ELK GROVE VILLAGE IL	FULLENTON CA	_	DES PLAIMES IL	ST PAUL IN	WARMINSTER PA	CHICAGO IL	LAWRENCE MA	
MANGEACTURER NAME	DEUTSCHE VITRUM GABH L CD	ILLUMINATED PRODUCTS INC	AMP 1MC	ALLEN-BRADLEY CO	TEMAS INSTR INC SEMICOND CHPMT DIV	PLARDXCUBE CORP	RCA CORP SOLID STATE DAY	ADI PYROFILM CORP	MUTORULA SEMICONUNCTOR PREDUCTS	FAIRCHILD SEMICONDUCTOR DIV	TR# 1MC BOOME DIV	CLARUSTAT MFG CO IMC	COANING GL WK ELEC CAPAT UIV	U S CAPACITOR CORP	MEPCO/FLECINA CORP	STANFORD APPLIED ENGINEERING INC	GOMANDA ELECTRONICS CORP	COMMING GLASS WORRS (BRADFORD)	SPECIALTY CONNECTOR CO INC	MENLETT-PACKAND CO CORPORATE NO	BULKNS INC TRIMPOT PROD DIV	SPRAGUE ELECTRIC CD	BUSSMAN MPG DIV OF MCGRAH-EUISUN CO	TAM BLEK COMPONENTS CINCH DRV	BECKMAN INSTRUMENTS INC HELIPOT DIV	TRM INC PHILADELPHIA DIV	LITTELFUSE INC	SH COMPANY	C-# INUUSTRIES		ALCO ELECTRONIC PRODUCTS INC	
9	5005	00201	<b>3077</b>	17110	C1.45	07114	02735	03868	4713	69710	11902	14051	16241	16246	10261	13867	24426	24542	24431	78480	16426	<b>\$0 2 0 4</b>	70+14	71745	73138	226.	75415	76381	19727	68628	4214	

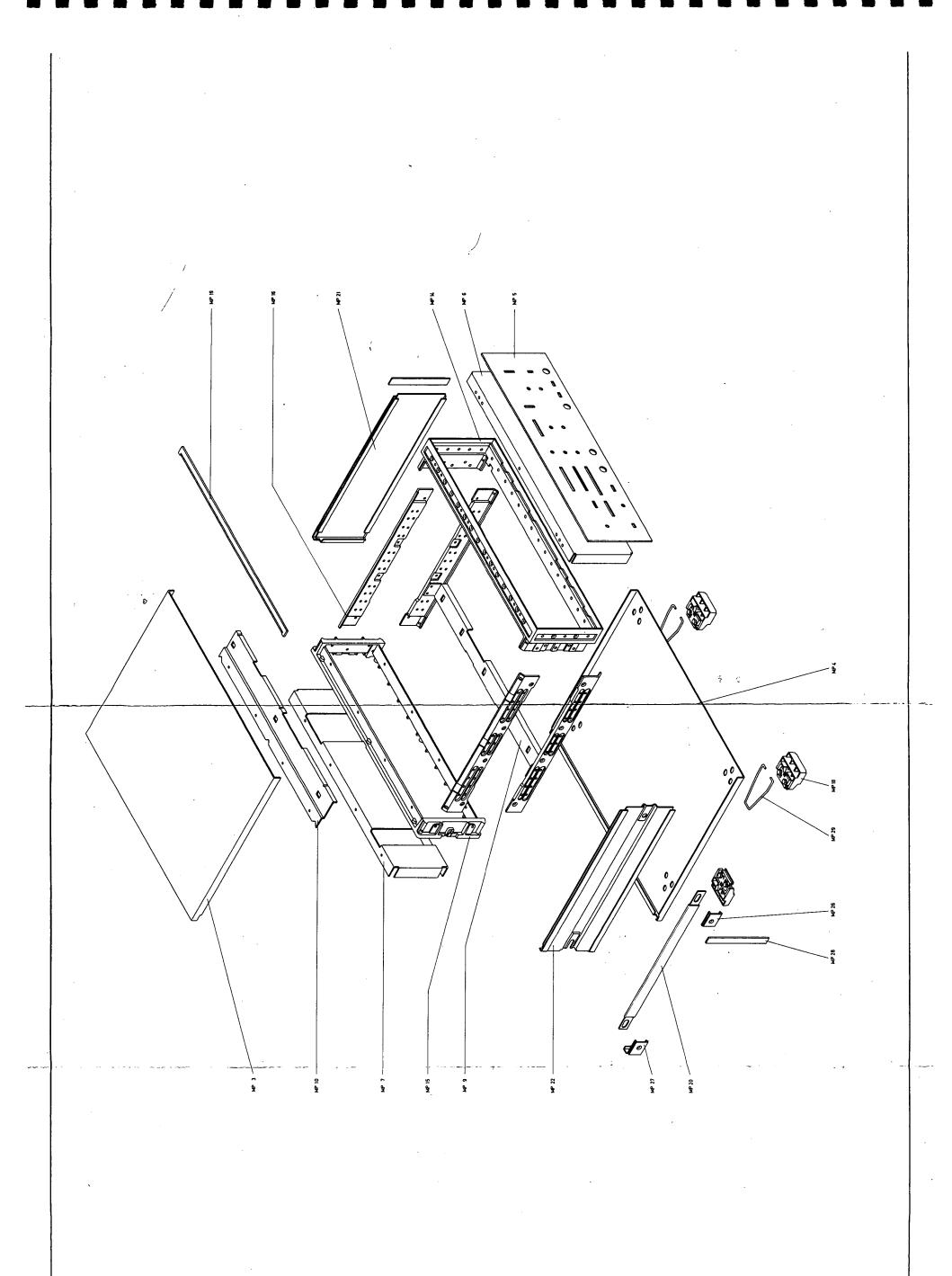


Figure 6–1. (cónt'd)

		-		,										
	DESCRIPTION		IC SENTER IC VERNIER/OUTP CBL AY-PWR SHLD. CBL AY-SHLD IT	CBL AY-SHLD II BD PC CBL AY-SHLD V	FUSEHOLDER BODY FUSEHOLD CAP/UL	nurner washer negrene						,		
	C H-P PART D NUMBER	3101		8 08082-61604 6 08082-26507 1 08082-61607	8 2110-0564 9 2110-0565	0600-00H	-		î					f
Replaceable Parts	REFERENCE DESIGNATOR	R8 S6 S17	00 00 00 00 00 00 00 00 00 00 00 00 00	W5 - W7 W11	XF1 XF2	XF4		<b>~</b>						
Table 6–3. Rep	DESCRIPTION	BD AY MOTHER BD AY PWR SUP BD AY REP RATE BD AY OUTPUT BD AY OFFSET	CBL AY-XFMR BD AY AMP BUFFER BD AY ATTENUATOR	LAMP INCD 5V	FUSE 15A 250V	BEZEL-PB KNOB KNOB BASE PTR COVER-TOP COVER-BOITOM PANEL-FRONT	PANEL-SUB PANEL AY-REAR HEAT SINK BRACKET BD PC BRACKET BD PC	LENS PILOT LIGHT TILT STAND CVR XFMR OLV BLK FRAME-FRONT FRAME-REAR	CORNSTRUT-UNIHRD KNOB-PBIN POWER FOOT TRIM STRIP-IOP SIRAP-HANDLE AY	COVER SIDE COVER SIDE 12IN HEAT SINK HEAT SINK SPACER	CAP, HANDLE FRONT CAP, HANDLE-REAR IRIM SIDE 5.25 FOOT REAR	PLATE MTG COVER SAFETY COVER SWITCH SCCR CATAL XSTR 2N5194 SI XSTR 2N5194 SI XSTR 2N5194 SI XSTR 2N5194 SI	R-VAR 50K 10% R-VAR 50K 10% R-VAR 50K 10% R-VAR 5K 20% .5W R-VAR 10K 10%	R-VAR 10K 10% R-VAR 5K 20% .5W Washer Gry 4
	C H-P PART D NUMBER	5 08082-66501 6 08082-66502 6 08082-66503 7 08082-66504 8 08082-66505	5 08082-61601 1 08082-66508 2 08082-66509	2140-009	1 2110-0202 3 1250-0519	0370-0914 0370-1005 08015-04102 08015-04103 08082-00201	08082-00202 08082-60204 08082-21101 08082-01202 08082-01201	1450-0404 1460-1345 5000-8908 5020-8803 5020-8804	5020-8835 5040-1124 5040-7201 5040-7202 5060-9802	5060-9855 5060-9878 08082-01101 68082-21102 08082-24701	5040-7219 5040-7220 5001-0439 5040-7221 08082-20501	08082-04105 08082-04101 08082-04102 2360-0361 1853-0212 1853-0212 1853-0212	2100-3861 2100-3861 2100-3861 2100-2492 2100-3859,	2100-3859. 2100-2492 30 <b>50-</b> 0016
FRAME	REFERENCE DESIGNATOR	A1 A2 A3 A5	A A 8 6 6 9 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	S1 1	F1 1	MP1 MP2 MP3 MP4 MP5	MP6 MP7 MP8 6 MP9 6 MP10 5	MP11 - 4 MP12 5 MP13 0 MP14 6 MP15 7	MP16 MP17 MP18 MP19 MP20	MP21 MP22 MP23 MP24 MP24 MP25	MP26 MP27 1 MP28 MP29 2 MP30	MP31 MP32 MP332 MP333 11 MP334 MP334 MP334 MP334 MP334 MP336 MP336 MP336 MP336 MP336 MP336 MP336 MP336 MP336 MP336 MP337	RR1 RR2 YR3 YR4 YR5 YR6 YR6 YR6 YR6 YR6 YR6 YR6 YR6 YR6 YR6	R6 9 R7 4 M P36

DESCRIPTION		-F 905'1% -TRMR 500	-F 2.67K1 -F 619 1% -TRMR 500	R-F 1.82K1% R-F 1K1% 125W F R-F .51 5% 2W PW R-F 562 1% 125W R-F 825 1% 125W	R-F 1K1X .125W F R-F 6.19K1X R-F .51 5X 2W PW R-F 100 1X .125W R-F 909 1X .125W	R-TRMR 500 10% R-F 2.67K1% R-F 2.43K1% R-F 1K1% .125W F	-F 7.5K 1%.1Z -F .33 5% 2W -F 100 1%.12! -F 8.25K1% -VAR 1K .5W -F 3.01K1%	R-F 150 1% .5W	IC-LIN VOLT REG IC-LIN VOLT REG IC-LIN VOLT REG IC-LIN VOLT REG	BD AY REP RATE	C-F 33PF 5% 200V C-F .022UF10%250 C-F 1500PF 400V C-F .22UF 50V	-F 2.2UF 20 -F 33UF 10V -F .001UF 1 -F .001UF 1 -F .001UF 1	-F .100F 20% -F .100F 20% -F .100F 20% -F 1000F 12V -F 1000F 12V	C-F 10UF 20V C-F .001UF 100V C-F .010UF 20X C-F .010UF 20X C-F .010UF 20X	C-F .010UF 20X C-F .001UF 100V C-F .001UF 100V C-F .001UF 100V C-F .010UF 20X	C-F .010UF 20X C-F .010UF 20X C-F .001UF 100V C-F .001UF 100V C-F 33UF 10V
H-P	NUMBER	0757-042 2100-335	0698-349 0757-041 2100-335	0757-0429 0757-0280 0811-0929 0757-0417 0757-0421	0757-0280 0757-0290 0811-0929 0757-0401	2100-3351 0698-3492 0757-0431 0757-0280	0757-044 0812-006 0757-040 0757-044 2100-335	0757-0801	1820-0439 1820-0439 1820-0439 1820-0439	08082-66503	0160-4386 0160-3716 0160-0597 0160-3646	0180-019 0180-022 0160-387 0160-387 0160-387	0160-421 0160-421 0160-421 0180-003 0180-003	0180-0374 0160-3878 0160-4209 0160-4209	0160-4209 0160-3878 0160-3878 0160-3878	0160-4209 0160-4209 0160-3878 0160-3878
rts FERENCE	ESIGNATOR	2 R6 2 R7	R8 R9 R10	A2 R11 2 A2 R12 3 A2 R13 3 A2 R14 8 A2 R15 A2	A2 R16 3 A2 R17 5 A2 R18 3 A2 R19 0 A2 R20 5	A2 R21 6 A2 R22 9 A2 R23 6 A2 R24 3	2 R25 2 R25 2 R27 2 R28 2 R29 830	A2 R31 4	A2 U1 0 A2 U2 0 A2 U3 0 A2 U4		A3 C1 1 A3 C2 1 A3 C3 0 A3 C4 6	3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	33 C11 3 C12 3 C13 6 C14 6 C15	A3 C16 A3 C17 6 A3 C17 9 A3 C19 9	A3 C22 A3 C22 A3 C22 A3 C24 6 A3 C24 6 6	A3 C25 A3 C26 A3 C27 A3 C28 A3 C28
Table 6-3. Replaceable	i	BD AY MOTHER	CONN PC 12CONT R CONN PC2X15.156D CONN PC 36CONT R CONN PC 12CONT	AY-PC AY-PC AY-PC AY-PC	DE AY-PC SLIDE DP3 DE AY-PC SLIDE DPD	DE AY~PO SLIDE DI SLIDE DI	BD AY PWR SUP C-F 3600UF 30V C-F 100UF 12V C-F 6000UF 15V C-F 100UF 12V	-F 3600UF 3	C-F 100UF 12V C-F 2600UF 40V C-F 40UF 50V C-F 1000PF 1000V C-F 1000PF 1000V	C-F 1000PF 1000V C-F 1000PF 1000V C-F 10UF 20V C-F 10UF 20V	-F 33UF 1 -F .22UF/ -F 10UF 2	DIO-PWR 400V IA DIO-PWR 400V IA DIO-PWR 400V IA DIO-PWR 400V IA DIO-PWR 400V IA	10-PWR 400V 1 10-PWR 400V 1 10-PWR 400V 1 10-PWR 400V 1 10-PWR 400V 1	DIO-FWR 400V 1A DIO-PWR 400V 1A DIO-PWR 400V 1A DIO-PWR 400V 1A DIO-PWR 400V 1A	-F 825 1% .12 -F 1K1% .125W -F 6.19K1% -F .39 5% 2W -F 100 1% .12	
C H-P PART	NUMBER	08082-66501	1251-0472 1251-2035 1251-2026 1251-1626	5040-110 5040-110 5040-110 5040-110	5040-110 3101-131 08015-61 5040-110 3101-159	5040-110 3101-159 3101-159	08082-66502 0180-2340 0180-039 0180-2352 0180-0039	0180-234	0180-0039 0180-2171 0180-0050 0160-3456 0160-3456	0160-3456 0160-3456 0180-0374 0180-0374	0180-022 0160-372 0180-037	1901-0731 1901-0731 1901-0731 1901-0731 1901-0731	1901-073 1901-073 1901-073 1901-073 1901-073	1901-0731 1901-0731 1901-0731 1901-0731 1901-0731	0757-042 0757-028 0757-029 0811-166 0757-040	
FERENCE	十		0 to 4 to	H 0 6 4 1	S5 S7 S8 S10 S10 S11 0	S12 S14 S15	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		Ġ	C111 C122 C13 C13	15 17 116	CR1 7 CR2 7 CR3 7 CR4 7 CR5 7	R6 R7 R8 R3 R10	CR12 7 CR13 7 CR13 7 CR14 7 CR15 7	-1 0 E 4 D	
REFER		A1	A1 A1	A D 1.1	A PI A PI A PI		A2 A2 A2	A2	<b>A A A B B B B B B B B B B</b>	A2 A2 A2	A2 A2	22222 <b>44</b> 44	7 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	0000 0000 0000	<b>A A A</b> A A A A A A A A A A A A A A A A	

P PART DESCRIPTION REFER UMBER  UMBER  C-F 10UF 20V 180-0374 C-F 10UF 25V 180-0291 180-0291 180-0291 180-0291 180-0291 180-0291 180-0291 180-0291 C-F 10F 35V A3 180-0291 C-F 20F 15V A3 910-0040 DIO SI .05A 30V A3 901-0040 DIO SI .05A 30V A3 901-0040 DIO SI .05A 30V A3 A3 A3 A3 A4 A4 A5	C   H-P PART   DESCRIPTION   REFERENCE
P PART DESCRIPTION DESIGNATION	P PART DESCRIPTION REFERENCE UMBER  180-0374 C-F 10UF 20V A3 R1 180-0116 C-F 10UF 20V A3 R1 180-0291 C-F 10UF 20% A3 R1 160-4209 C-F .010UF 20% A3 R1 160-0291 C-F 1UF 35V A3 R1 180-0291 C-F 1UF 35V A3 R2 911-0040 DIO SI .05A 30V A3 R2 901-0040 DIO SI .05A 30V A3 R2
ESCRIPTION  REFER  10 F 20V  -F 10 F 20V  -F 10 F 20V  -F 10 F 20V  -F 10 F 35V  -F 30 F 30V  -F 20 F 15V  -F 20 F 15V  -F 20 F 15V  -F 20 F 15V  -F 30 A3  -F 10 SI .05A 30V  -F 30 A3  -F 10 SI .05A 30V  -F 30 A3  -F	ESCRIPTION  REFERENCE DESIGNATO  - F 10UF 20V  - F 10UF 20V  - F 10UF 35V  - F .010UF 20%  - A3  R1  - F .010UF 35V  A3  R1  - F .010UF 35V  A3  R1  - F .01UF 35V  A3  R2  10 GE 25V .1A  A3  R2  10 SI .05A 30V  A3  R3  ACK RECEP STRAI  A3  R3  R3  R4
NE B	FERENCE R1
	R10 R110 R110 R111 R112 R113 R114 R15 R16 R17 R19 R19 R20 R21 R22 R23 R23 R23 R24 R26 R26 R27 R26 R27 R27 R28 R28 R28 R33 R33 R35 R35 R35 R35 R35 R35 R35 R35
C H-P PART D NUMBER  4 0757-0710 3 0757-0420 3 0757-0420 1 0698-4418 3 0757-0438 3 0757-0438 3 0757-0438 3 0757-0438 3 0757-0420 0 0757-0394 0 0757-0394 0 0757-0394 0 0757-0421 9 0698-4453 4 0698-4453 4 0698-4453 2 0757-0411 2 0757-0411 3 0698-3157 0 0757-0411 3 0698-3446 9 0757-0412 0 0757-0413 3 0698-3446 9 0757-0473 4 0757-0273 4 0757-0273 4 0757-0273	

Table 6—3. Replaceable Parts

Table 6-3. Replaceable Parts (cont'd)

		rable 0-3. Replacea			ı -	1	
REFERENCE DESIGNATOR	C H-P PART D NUMBER	DESCRIPTION	REFEI DESI	RENCE SNATOR		H-P PART NUMBER	DESCRIPTION
A3 R76 A3 R77 A3 R78 A3 R81 A3 R82	0 0757-0401 9 0757-0476 0 0757-0394 0 0757-0394 0 0757-0394	R-F 100 1% .125W R-F 301K 1% .125 R-F 51.1 1% R-F 51.1 1% R-F 51.1 1%	A4 A4 A4 A4 A4	C40 C41 C42 C47 C50	4 4 4	0160-3470 0160-3470 0160-3470 0160-3470 0160-3470	C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V
A3 R83 A3 R84 A3 R85 A3 R86 A3 R87	3 0757-0438 0 0757-0394 2 0757-0346 6 0757-0283 1 2100-3207	R-F 5.11K1% R-F 51.1 1% R-F 10 1% .125W R-F 2K1% .125W F R-VAR 5K 10%	A4 A4 A4 A4 A4	C52 C54 C55 C57 C58	4 4 4	0160-3470 0160-3470 0160-3470 0160-3470 0160-3470	C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V
A3 R88 A3 R89 A3 R90 A3 R91	0 0757-0394 6 0698-4455 6 0698-4455 9 0757-0434	R-F 51.1 1% R-F 536 1% .125W R-F 536 1% .125W R-F 3.65K1"	A4 A4 A4 A4 A4	C59 C60 C61 C64 C65	4 4	0160-3470 0160-3470 0160-3470 0160-3470 0160-3470	C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V
A3 S1 A3 U1 A3 U2 A3 U3 A3 U4	3 3101-1341 6 5081-3011 5 5081-3010 7 1826-0111 7 1826-0111	SW SLIDE SPDT  IC DIG REP RATE IC SEALED PKG IC-DUAL OF AMPL IC-DUAL OP AMPL	A4 A4 A4 A4 A4	C66 C67 C69 C70 C71	4 0 0	0160-3470 0160-3470 0160-0571 0160-0571 0160-3470	C-F .01UF 50V C-F .01UF 50V C-F 470PF.0% CER C-F 470PF20% CER C-F .01UF 50V
A3 U5 A3 U6 A3 VR3 A3 VR8	7 1826-0111 5 1820-0054 2 1902-0049 3 1902-3002	IC-DUAL OF AMPL IC-DUAL OP AMPL IC 7400N EQUIV DIO-ZNR 6.19V 5% DIO 2.37V 5%	A4 A4 A4 A4 A4	C72 C73 C75 C76 C77	4 8 4	0160-3470 0160-3470 0180-0197 0160-3470 0160-3470	C-F .01UF 50V C-F .01UF 50V C-F 2.2UF 20V C-F .01UF 50V C-F .01UF 50V
			A4 A4 A4 A4 A4	C78 C79 C81 C82 - C83	4 8 8	0160-3470 0160-3470 0180-0197 0180-0197 0150-3470	C-F .01JF 50V C-F .01UF 50V C-F 2.2UF 20V C-F 2.2UF 20V C-F .01UF 50V
A4	08082-66504	BD AY OUTPUT	A4 A4 A4 A4 A4	C85 C86 C87 C88 C90	4 4	0160-3470 0160-3470 0160-3470 0160-3470 0160-3470	C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V
A4 C1 A4 C2 A4 C3 A4 C4 A4 C5	4 0160-3470 3 0160-4386 2 0180-0349 6 0180-2050 4 0160-4220	C-F .01UF, 50V C-F 33PF 5% 200V C-F .82UF 35V C-F .082UF 35V C-F 8200PF 5%	A4 A4 A4 A4 A4	C91 C92 C93 C94 C95	4 4 4	0160-3470 0160-3470 0160-3470 0160-3470 0160-3470	C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V
A4 C6 A4 C7 A4 C8 A4 C9 A4 C10-	4 0160-3884 4 0160-3470 4 0160-3470 3 0160-3875 2 0180-0349	C-F 680PF 100V C-F .01UF 50V C-F .01UF 50V C-F 22PF 5% 200V C-F .82UF 35V	A4 A4 A4 A4 A4	C96 C97 C98 C99 C100	4 4 4	0160-3470 0160-3470 0160-3470 0160-3470 0160-3470	C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V
A4 C11 A4 C12 A4 C13 A4 C14 A4 C15	6 0180-2050 4 0160-4220 4 0160-3884 4 0160-3470 2 5080-1089	C-F .082UF 35V C-F 8200PF 5% C-F 680PF 100V C-F .01UF 50V CAPACITOR-SELECT	A4 A4 A4 A4 A4	C101 C112 C113 C114 C118	4 4 4	0160-3470 0160-4212 0160-4212 0160-4212 0160-4212	C-F .01UF 50V C-F .068UF 20% C-F .068UF 20% C-F .068UF 20% C-F .068UF 20%
A4 C16 A4 C17 A4 C18 A4 C19 A4 C20	2 5080-1089 0 0160-5042 0 0160-5042 4 0160-5278 4 0160-5278	CAPACITOR-SELECT C-F .082UF C-F .082UF C-F 8200PF 50V C-F 8200PF 50V	A4 A4 A4 A4 A4	C119 C120 C121 C122 C123	4 4 4	0160-3470 0160-4212 0160-3470 0160-3470 0160-3470	C-F .01UF 50V C-F .068UF 20% C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V
A4 C21 A4 C22 A4 C23 A4 C25 A4 C26	5 0160-5279 5 0160-5279 3 0160-4386 4 0160-4212 4 0160-3470	C-F 820PF 50V C-F 820PF 50V C-F 33PF 5% 200V C-F .068UF 20% C-F .01UF 50V	A4 A4 A4 A4 A4	CR2 CR3 CR4 CR5 CR6	1 8 1	1901-0040 1901-0040 1910-0022 1901-0040 1901-0040	DIO SI .05A 30V DIO SI .05A 30V DIO GE 5V 3.5NS DIO SI .05A 30V DIO SI .05A 30V
A4 C29 A4 C31 A4 C31 A4 C32 A4 C35	6 0180-2795 7 0180-0229 6 0180-2795 1 0180-0116 4 0160-4212	C-F 39UF 15V C-F 33UF 10V C-F 39UF 15V C-F 6.8UF 35V TA C-F .068UF 20%					
			<u> </u>				

Table 6-3. Replaceable Parts (cont'd)

REFERENCE DESIGNATOR	C H-P PART D NUMBER	DESCRIPTION	REFERENCE DESIGNATOR	C H-P PART D NUMBER	DESCRIPTION
A4 CR7	1 1901-0040	DIO SI .05A 30V	A4 Q20	2 1853-0036	XSTR SI 2N3906
A4 CR9	1 1901-0040	DIO SI .05A 30V	A4 Q21	2 1853-0036	XSTR SI 2N3906
A4 CR10	1 1901-0040	DIO SI .05A 30V	A4 Q22	1 1854-0215	XSTR SI 2N3904
A4 CR11	8 1910-0022	DIO GE 5V 3.5NS	A4 Q23	1 1854-0215	XSTR SI 2N3904
A4 CR12	1 1901-0040	DIO SI .05A 30V	A4 Q23	1 1854-0215	XSTR SI 2N3904
A4 CR13	1 1901-0040	DIO SI .05A 30V	A4 Q25	1 1854-0215	XSTR SI 2N3904
A4 CR15	7 1901-0533	DIO HOT CARR	A4 Q27	1 1854-0215	XSTR SI 2N3904
A4 CR16	7 1901-0533	DIO HOT CARR	A4 Q28	1 1854-0215	XSTR SI 2N3904
A4 CR17	8 1901-0518	DIO HOT CARRIER	A4 Q32	2 1853-0036	XSTR SI 2N3906
A4 CR17	8 1901-0518	DIO HOT CARRIER	A4 Q33	1 1854-0215	XSTR SI 2N3904
A4 CR20	1 1901-0040	DIO SI .05A 30V	A4 Q34	1 1854-0215	XSTR SI 2N3904
A4 CR21	1 1901-0040	DIO SI .05A 30V	A4 Q55	2 1853-0036	XSTR SI 2N3906
A4 CR25	3 1901-0050	DIO SW 80V 200MA	A4 Q56	2 1853-0036	XSTR SI 2N3906
A4 CR26	3 1901-0050	DIO SW 80V 200MA	A4 Q57	2 1853-0036	XSTR SI 2N3906
A4 CR27	3 1901-0050	DIO SW 80V 200MA	A4 Q58	2 1853-0036	XSTR SI 2N3906
A4 CR28	3 1901-0050	DIO SW 80V 200MA	A4 Q59	2 1853-0036	XSTR SI 2N3906
A4 CR32	7 1901-0533	DIO HOT CARR	A4 Q60	2 1853-0036	XSTR SI 2N3906
A4 CR33	7 1901-0533	DIO HOT CARR	A4 Q61	2 1853-0036	XSTR SI 2N3906
A4 CR39	1 1901-0040	DIO SI .05A 30V	A4 Q62	2 1853-0036	XSTR SI 2N3906
A4 CR40	1 1901-0040	DIO SI .05A 30V	A4 Q69	2 1853-0284	TRANSISTOR
A4 CR41 A4 L1 A4 L2 A4 L3 A4 L6	1 1901-0040 4 5081-1972 4 5081-1972 4 5081-1972 4 5081-1972	DIO SI .05A 30V  INDUCTANCE 2BEAD INDUCTANCE 2BEAD INDUCTANCE 2BEAD INDUCTANCE 2BEAD	A4 Q70 A4 Q71 A4 Q72 A4 Q73 A4 Q74	2 1853-0284 2 1853-0036 2 1853-0036 1 1854-0215 2 1853-0036	TRANSISTOR XSTR SI 2N3906 XSTR SI 2N3906 XSTR SI 2N3904 XSTR SI 2N3906
A4 L8 A4 L9 A4 L10	4 5081-1972 4 5081-1972 4 5081-1972 4 5081-1972 4 5081-1972	INDUCTANCE 2BEAD INDUCTANCE 2BEAD INDUCTANCE 2BEAD INDUCTANCE 2BEAD	A4 R1 A4 R2 A4 R3 A4 R7 A4 R8	7 0757-0424 4 0757-0273 8 0698-3160 9 0757-0442 9 0757-0442	R-F 1.1K1% .125W R-F 3.01K1% R-F 31.6K1% R-F 10K1% .125W R-F 10K1% .125W
A4 L11	8 9140-0118	COIL-CHOKE 500UH COIL-CHOKE 500UH COIL-CHOKE 500UH COIL-CHOKE 500UH INDUCTANCE 3BEAD	A4 R9	9 0757-0418	R-F 619 1% .125W
A4 L12	8 9140-0118		A4 R10	9 0757-0418	R-F 619 1% .125W
A4 L13	8 9140-0118		A4 R11	6 0757-0720	R-F 243 1% 1/4W
A4 L14	8 9140-0118		A4 R15	6 0757-0449	R-F 20K1% .125W
A4 L15	5 5081-1973		A4 R16	5 0683-1055	R-F 1M5% .25W CC
A4 L16	5 5081-1973	INDUCTANCE 3BEAD INDUCTANCE 3BEAD INDUCTANCE 3BEAD INDUCTANCE 3BEAD COIL-FXD 3.3 MH	A4 R17	5 0757-0274	R-F 1.21K1%
A4 L17	5 5081-1973		A4 R18	5 0757-0274	R-F 1.21K1%
A4 L18	5 5081-1973		A4 R19	6 0757-0449	R-F 20K1% .125W
A4 L19	5 5081-1973		A4 R20	5 0683-1055	R-F 1M5% .25W CC
A4 L20-	8 9100-1665		A4 R21	5 0683-1055	R-F 1M5% .25W CC
A4 L21	8 9100-1665	COIL-FXD 3.3 MH FERRITE BEAD FERRITE BEAD INDUCTANCE 2BEAD COIL-FXD 3.3 MH	A4 R22	5 0757-0274	R-F 1.21K1%
A4 L22	3 9170-0029		A4 R23	6 0757-0449	R-F 20K1% .125W
A4 L23	3 9170-0029		A4 R24	5 0683-1055	R-F 1M5% .25W CC
A4 L24	4 5081-1972		A4 R25	6 0757-0449	R-F 20K1% .125W
A4 L25	8 9100-1665		A4 R26	5 0757-0274	R-F 1.21K1%
A4 L26	4 5081-1972	INDUCTANCE 2BEAD COIL-FXD 3.3 MH FERRITE BEAD FERRITE BEAD COIL-CHOKE 1 UH	A4 R27	1 0698-3113	R-F 100 5% .125W
A4 L27	8 9100-1665		A4 R29	0 0757-0394	R-F 51.1 1%
A4 L28	3 9170-0029		A4 R30	6 0757-0283	R-F 2K1% .125W F
A4 L29	3 9170-0029		A4 R31	4 0757-0273	R-F 3.01K1%
A4 L30	6 9140-0158		A4 R32	6 0757-0720	R-F 243 1% 1/4W
A4 L31 A4 MP3 A4 MP4 A4 MP5 A4 MP6	6 1600-0457 6 1600-0457 7 1600-0341 7 1600-0341	COIL-CHOKE 1 UH CTCT 14 FINGER CTCT 14 FINGER CTCT 9FINGER CTCT 9FINGER	A4 R33 A4 R34 A4 R36 A4 R37 A4 R38	0 0757-0394 0 0757-0401 9 0698-0084 9 0698-0084 7 0757-0440	R-F 51.1 1% R-F 100 1% .125W R-F 2,15K 1%.125 R-F 2,15K 1%.125 R-F 7.5K 1%.125W
A4 Q3 A4 Q4 A4 Q6 A4 Q7	2 1853-0036 2 1853-0036 1 1854-0215 1 1854-0215	XSTR SI 2N3906 XSTR SI 2N3906 XSTR SI 2N3904 XSTR SI 2N3904	A4 R40 A4 R41 A4 *R42 A4 R43 A4 R44	8 0698-3441 8 0698-3441 3 0757-0438 2 2100-3274 3 0757-0438	R-F 215 1% .125W R-F 215 1% .125W R-F 5.11K1% R-VAR 10K 10% R-F 5.11K1%
A4 Q8	1 1854-0215	XSTR SI 2N3904	A4 R45	2 0757-0289	R-F 13.3K1%
A4 Q9	1 1854-0215	XSTR SI 2N3904	A4 R46	6 0757-0407	R-F 200 1% .125W
A4 Q11	1 1854-0215	XSTR SI 2N3904	A4 R47	6 0757-0283	R-F 2K1% .125W F
A4 Q12	1 1854-0215	XSTR SI 2N3904	A4 R48	6 0698-3150	R-F 2.37K1%
A4 Q16	2 1853-0036	XSTR SI 2N3906	A4 R51	6 0757-0283	R-F 2K1% .125W F

Table 6-3. Replaceable Parts (cont'd)

REFER	REFERENCE C H-P PART DESCRIPTION REFERENCE C H-P PART DESCRIPTION								DESCRIPTION
DESIG		D	NUMBER	DESCRIPTION		GNATOR		NUMBER	DESCRIPTION
A4 A4 A4 A4 A4	R53 R54 R55 R56 R57	0 0	0698-3378 0698-3378 0757-0394 0757-0434 0757-0438	R-F 51 5% .125W R-F 51 5% .125W R-F 51.1 1% R-F 3.65K1% R-F 5.11K1%	A4 A4 A4 A4 A4	R168 R169 R170 R171 R172	5 5 8	0698-5999 0698-5999 0698-5999 0698-6750 0698-3454	R-F 4.7K5% .125W R-F 4.7K5% 12 W R-F 4.7K5% .125W R-F 220K10% R-F 215K1% .125W
A4 A4 A4 A4	R58 R59 R60 R61 R62	0 ( 6 (	0698-3378 0757-0394 0757-0720 0757-0720 0757-0442	R-F 51 5% .125W R-F 51.1 1% R-F 243 1% 1/4W R-F 243 1% 1/4W R-F 10K1% .125W	A4 A4 A4 A4 A4	R174 R180 R186 R187 R188	000	0698-3427 0698-3435 0698-3378 0698-3378 0698-6750	R-F 13.3 1% R-F 38.3 1% R-F 51 5% .125W R-F 51 5% .125W R-F 220K10%
A4 A4 A4 A4 A4	R65 R66 R67 R68 R69	9 (	0757-0442 0757-0442 0757-0418 0757-0418 0757-0283	R-F 10K1% .125W R-F 10K1% .125W R-F 619 1% .125W R-F 619 1% .125W R-F 2K1% .125W	A4 A4 A4 A4	R189 R190 R191 R195 R196	6 4 0	0698-5180 0698-5180 0698-3447 0698-3378 0698-3378	R-F 2K5% .125w R-F 2K5% .125w R-F 422 1% 1/8w R-F 51 5% .125w R-F 51 5% .125w
A4 A4 A4 A4 A4	R70 R71 R72 R76 R77	4 0 7 0 6 0	0698-3160 0757-0273 0757-0424 0757-0449 0757-0449	R-F 31.6K1% R-F 3.01K1% R-F 1.1K1% .125W R-F 20K1% .125W R-F 20K1% .125W	A4 A4 A4 A4 A4	R197 R198 R199 R200 R201	7 0 8	0698-4413 0757-0200 0698-3154 0698-6750 0698-6750	R-F 154 1% .125W R-F 5.62K1% R-F 4.22K 1% R-F 220K10% R-F 220K10%
A4 A4 A4 A4 A4	R78 R79 R80 R81 R82	6 C	0757-0449 0757-0449 0757-0274 0757-0274 0757-0274	R-F 20K1% .125W R-F 20K1% .125W R-F 1.21K1% R-F 1.21K1% R-F 1.21K1%	A4 A4 A4 A4 A4	R202 R203 R204 R206 R207	3 2	0757-0416 0757-0438 0698-4428 0757-0411 0757-0405	R-F 511 1% .125W R-F 5.11K1% R-F 1.69K1% R-F 332 1% .125W R-F 162 1% .125W
A4 A4 A4 A4 A4	R83 R84 R85 R85 R <del>8</del> 6-	4 (	0757-0274 0698-4073 0698-4073 0757-0438 0698-4073	R-F 1.21K1% R-F 1M10% .125W R-F 1M10% .125W R-F 5.11K1% R-F 1M10% .125W	A4 A4 A4 A4 A4	R208 R210 R211 R212 R213	6 9 3	0757-0416 0757-0449 0757-0442 0757-0438 0698-3438	R-F 511 1% .125W R-F 20K1% .125W R-F 10K1% .125W R-F 5.11K1% R-F 147 1% .125W
A4 A4 A4 A4	R87 R90 R90 R91 R92	8 0	0698-4073 0698-3441 0757-0438 0698-3441 0698-0084	R-F 1M10% .125W R-F 215 1% .125W R-F 5.11K1% R-F 215 1% .125W R-F 2,15K 1%.125	A4 A4 A4 A4	R214 R216 R217 R218 R219	3 7 7	2100-3350 0698-3438 0698-4125 0698-4125 0757-0290	R-VAR 200 10% R-F 147 1% .125W R-F 953 1% .125W R-F 953 1% .125W R-F 6.19K1%
A4 A4 A4 A4	R93 R95 R98 R100 R101	9 0	0698-0084 0757-0289 0757-0434 0757-0438 0757-0438	R-F 2,15K 1%.125 R-F 13.3K1% R-F 3.65K1% R-F 5.11K1% R-F 5.11K1%	A4 A4 A4 A4 A4	R220 R221 R222 R223 R224	7 1 9	0757-0290 0698-0082 0757-0444 0757-0442 0757-0442	R-F 6.19K1% R-F 464 1% .125W R-F 12.1K1%.125W R-F 10K1% .125W R-F 10K1% .125W
A4 A4 A4 A4 A4	R102 R103 R106 R107 R112	6 0	0757-0434 0757-0405 0757-0283 0698-3150 0757-0442	R-F 3.65K1% R-F 162 1% .125W R-F 2K1% .125W F R-F 2.37K1% R-F 10K1% .125W	A4 A4 A4 A4	R225 R226 R227 R228 R229	8 8 6	0757-0069 0757-1001 0757-1001 0757-0069 0698-4435	R-F 121 1% .25W R-F 56.2 1% .5W R-F 56.2 1% .5W R-F 121 1% .25W R-F 2.49K1%
A4 A4 A4 A4	R113 R114 R115 R116 R117	9 0	0757-0442 0757-0442 0757-0442 0698-0082 0698-0082	R-F 10K1% .125W R-F 10K1% .125W R-F 10K1% .125W R-F 464 1% .125W R-F 464 1% .125W	A4 A4 A4 A4	R230 R232 R237 R240 R241	0 2	0698-4435 2100-3349 0757-0394 2100-3274 0698-5180	R-F 2.49K1% R-VAR 100 -+10% R-F 51.1 1% R-VAR 10K 10% R-F 2K5% .125W
A4 A4 A4 A4 A4	R141 R146 R151 R152 R153	1 0	0698-3113 0698-3113 0698-4073 0698-4073 0698-4073	R-F 100 5% .125W R-F 100 5% .125W R-F 1M10% .125W R-F 1M10% .125W R-F 1M10% .125W	A4 A4 A4 A4	R242 R243 R244 R245 R252	9 8 8	0698-3111 0698-3111 0757-1001 0757-1001 0757-0394	R-F 30 5% .125W R-F 30 5% .125W R-F 56.2 1% .5W R-F 56.2 1% .5W R-F 51.1 1%
A4 A4	R154 R155		)698-4073 )698-4073	R-F 1M10% .125W R-F 1M10% .125W	A4 A4	R253 R260		2100-3349 0757-0394	R-VAR 100 -+10% R-F 51.1 1%
A4 A4 A4	R156 R157 R158	4 0	0698-4073 0698-4073 0698-4073	R-F 1M10% .125W R-F 1M10% .125W R-F 1M10% .125W	A4 A4 A4 A4	U1 U2 U3 U4	7 2		IC-DUAL OP AMPL -3co9IC-DUAL OP AMPL -onic Sealed PKG IC Sealed PKG
A4 A4 A4 A4	R159 R161 R163 R165 R167	0 0	0698-5176 0698-5176 0698-5176 0698-5176 0698-5999	R-F 510 5% .125W R-F 510 5% .125W R-F 510 5% .125W R-F 510 5% .125W R-F 4.7K5% .125W	A4 A4 A4 A4 A4	VR1 VR8 VR19 VR22 VR23	6 6 5 9	1902-0126 1902-0126 1902-3137 1902-0202 1902-0202	DIO 2.61V 5% .4W DIO 2.61V 5% .4W DIO 8.06V 2% .4W DIO-ZNR 15V 5% DIO-ZNR 15V 5%

A 4 (X1) 5040-9314 LOCK Clip 14 PO1
A4 (X2) 5040-9314 LOCK Clip 14 PO1
Table 6-3. Replaceable Parts (cont'd)

A4(x	Table 6—3. Replaceable Parts (cont'd)									
	RENCE GNATOR		H-P PART NUMBER	DESCRIPTION		FERENCE SIGNATOR	C D	H-P PART NUMBER	DESCRIPTION-	
A4 A4 A4 A4 A4	VR24 VR29 VR30 VR31 VR42	6 6 6	1902-0126 1902-3104 1902-0522 1902-0522 1902-0064	DIO 2.61V 5% .4W DIO 5.62V 5% .4W DIO 6V 5% 5W DIO 6V 5% 5W DIO 7.5V 5% .4W	A5 A5 A5 A5 A5	Q1 Q3 Q6 Q8 Q9	232	1853-0045 1854-0448 1853-0045 1854-0448 1853-0036	XSTR 2N4036 SI XSTR SI NPN XSTR 2N4036 SI XSTR SI NPN XSTR SI 2N3906	
A4 A4 A4 A4 A4	W3 W10 W16 W17 W18	0 9 9	08082-61603 08082-61606 08082-61605 08082-61605	CBL AY-SHLD I CBL AY-SHLD IV CBL AY-SHLD III CBL AY-SHLD III CBL AY-SHLD III	A5 A5 A5 A5 A5	Q10 Q12 Q14 Q15 Q17	2	1853-0036 1853-0036 1853-0036 1853-0036 1853-0036	XSTR SI 2N3906 XSTR SI 2N3906 XSTR SI 2N3906 XSTR SI 2N3906 XSTR SI 2N3906	
A4 A4 A4	₩19 ₩2-7-1 X2	8	08082-61605 1200-0548 1200-0588	CBL AY-SHLD III SKT IC <del>14CONT</del> /6CowT SOCKET IC	A5 A5 A5 A5	Q19 Q20 Q21 Q22			XSTR 2N4036 SI XSTR 2N4036 SI XSTR SI 2N3905 XSTR SI 2N3904	
A	,		08082-66505	BD AY OFFSET	A5 A5 A5 A5	Q26 Q27 Q28 Q29	3	1853-0045 1853-0045 1853-0045 1853-0045	XSTR 2N4036 SI XSTR 2N4036 SI XSTR 2N4036 SI XSTR 2N4036 SI	
A5 A5 A5 A5 A5	C1 C2 C3 C4 C5	4 4 9	0180-0309 0180-0309 0180-0309 0180-0309 0160-4209	C-7 4.7UF 10V C-7 4.7UF 10V C-7 4.7UF 10V C-7 4.7UF 10V C-F .010UF 20%	A5 A5 A5 A5 A5	Q30 Q31 Q32 Q33 Q34	7 1 1	1854-0039 1854-0039 1853-0051 1854-0215 1854-0215	XSTR 2N3053 SI  XSTR 2N3053 SI  XSTR SI 4037  XSTR SI 2N3904  XSTR SI 2N3904	
A5 A5 A5 A5 A5	C6 C7 C9 C10 C11	3	0160-4209 0160-4209 0180-0374 0180-0374 0180-0039	C-F .010UF 20% C-F .010UF 20% C-F 10UF 20V C-F 10UF 20V C-F 100UF 12V	A5 A5 A5 A5	Q35- Q36 Q37 Q38	1 2 2	1853-0051 1853-0051 1853-0036 1853-0036	XSTR SI 4037  XSTR SI 4037  XSTR SI 2N3906  XSTR SI 2N3906	
A5 A5 A5 A5	C12 C15 C16 C20 C21	9 9	0180-0039 0160-4209 0160-4209 0160-4209 0160-4209	C-F 100UF 12V C-F .010UF 20% C-F .010UF 20% C-F .010UF 20% C-F .010UF 20%	A5 A5 A5 A5 A5	Q39 Q40 Q41 Q42 Q43	1 1 1	1854-0215 1854-0215 1854-0215 1854-0215 1854-0215	XSTR SI 2N3904 XSTR SI 2N3904 XSTR SI 2N3904 XSTR SI 2N3904 XSTR SI 2N3904	
A5 A5 A5 A5	C22 C23 C24 C27	8	0180-0197 0180-0197 0180-0291 0180-0058	C-F 2.2UF 20V C-F 2.2UF 20V C-F 1UF 35V C-F 50UF 25V	A5 A5 A5 A5	Q44 Q45 Q46 Q47	1 1 1 1	1854-0215 1854-0215 1854-0215 1854-0215	XSTR SI 2N3904 XSTR SI 2N3904 XSTR SI 2N3904 XSTR SI 2N3904	
A5 A5 A5 A5 A5	CR4 CR6 CR8 CR10	1 1 1	1901-0040 1901-0040 1901-0040 1901-0040	DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V	A5 A5 A5 A5	Q48 Q49 Q50	1 1 2	1853-003F 1854-0215 1854-0215 1853-0036	XSTR SI 2N3906 XSTR SI 2N3904 XSTR SI 2N3904 XSTR SI 2N3906	
A5 A5 A5 A5 A5	CR12 CR13 CR26 CR27 CR28	1 1 1	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040	DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V	A5 A5 A5 A5	Q52 Q53 Q54 Q55	2 1	1853-003- 1853-003- 1853-0036 1854-0215	XSTR SI 2N3906 XSTR SI 2N3906 XSTR SI 2N3906 XSTR SI 2N3904	
A5 A5	CR29 CR30		1901-0040	DIO SI .05A 30V	A5 A5 . A5	Q56 Q57 Q58	2	1853-0036 1853-0036 1854-0215	XSTR SI 2N3906 XSTR SI 2N3906 XSTR SI 2N3904	
A5 A5 A5 A5	CR31 CR32 CR33 CR34	1 1 1 1	1901-0040 1901-0040 1901-0040	DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V	A5 A5 A5 A5 A5	R5 R6 R7 R8 R9	9 9	0757-0281 0757-0442 0757-0442 0698-4477 0698-4477	R-F 2.74K1% R-F 10K1% .125W R-F 10K1% .125W R-F 10.5K1% R-F 10.5K1%	
A5 A5	CR36 CR37 MP1	1	1901-0040 1901-0040 1205-0011	DIO SI .05A 30V DIO SI .05A 30V HT-SINK XSTR	A5 A5 A5	R10 R11 R13	0 2	0698-0085 0698-3495 0757-0384	R-F 2.61K1% R-F 866 1% .125W	
A5 A5 A5	MP3 MP6 MP8	0 0 0	1205-0011 1205-0011	HT-SINK XSTK HT-SINK XSTR HT-SINK XSTR HT-SINK XSTR HT-SINK XSTR	A5 A5	R14 R15	0	0757-0384 0698-0085	R-F 20 1% !25W R-F 20 1% .125W R-F 2.61K1%	
A5 A5 A5 A5	MP26 MP27 MP28 MP29	00	1205-0011 1205-0011 1205-0011	HT-SINK XSTR HT-SINK XSTR HT-SINK XSTR	A5 A5 A5 A5 A5	R17 R18 R19 R20 R21	1 9 9	0698-3495 0757-0452 0757-0442 0757-0442 0757-0442	R-F 866 1% .125W R-F 27.4K1% R-F 10K1% .125W R-F 10K1% .125W R-F 10K1% .125W	

Table 6-3. Replaceable Parts (cont'd)

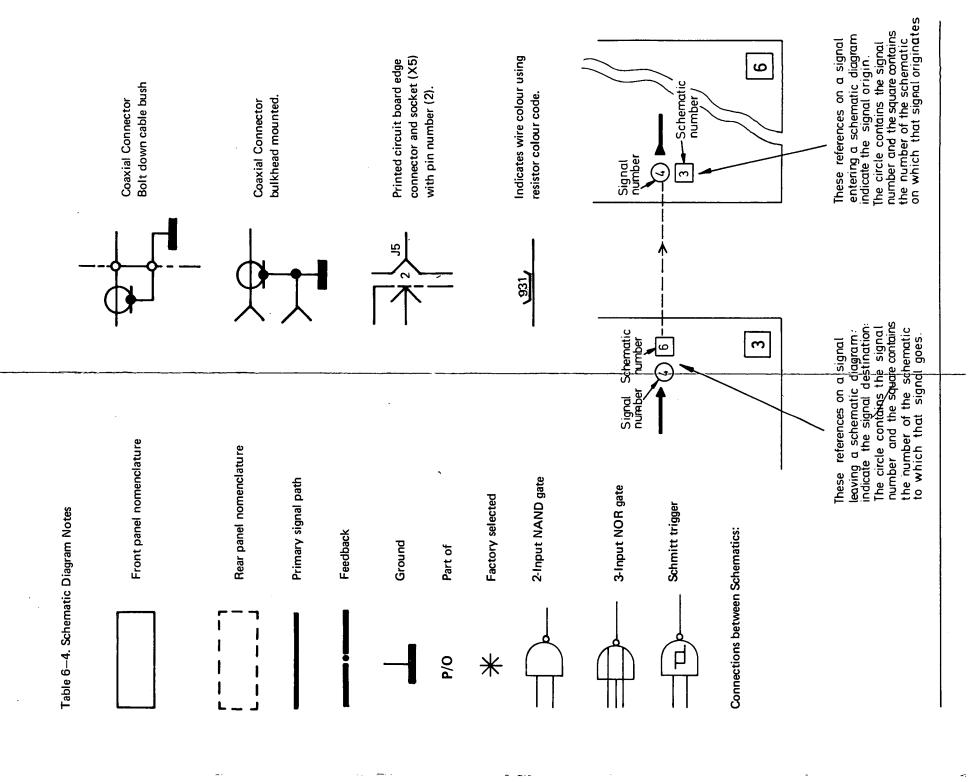
DEFEDENCE		LU D DADE	DECEDIDATON	DEREDENCE	]	I
REFERENCE DESIGNAT		H-P PART NUMBER	DESCRIPTION	REFERENCE DESIGNATOR	D NUMBER	DESCRIPTION
A5 R2 A5 R2 A5 R2	23   9 24   9 25   9	0757-0442 0757-0442 0757-0442 0757-0442 0698-4477	R-F 10K1% .125W R-F 10K1% .125W R-F 10K1% .125W R-F 10K1% .125W R-F 10.5K1%	A5 R111 A5 R112 A5 R113 A5 R114 A5 R115	0 0698-4433 2 0757-0346 2 0757-0346 3 0757-0280 1 2100-3273	R-F 2.26K1% R-F 10 1% .125W R-F 10 1% .125W R-F 1K1% .125W F R-VAR 2K 10%
A5 R A5 R A5 R	28 2 29 0 31 8	0698-4477 0698-3495 0698-0085 0757-0384 0757-0384	R-F 10.5K1% R-F 866 1% .125W R-F 2.61K1% R-F 20 1% .125W R-F 20 1% .125W	A5 R116 A5 R117 A5 R118 A5 R120 A5 R121	0 0757-0419 8 0698-3152 8 0698-3152 2 0757-0346 2 0757-0346	R-F 681 1% .125W R-F 3.48K 1% R-F 3.48K 1% R-F 10 1% .125W R-F 10 1% .125W
A5 R A5 R A5 R A5 R	35 0 51 7 52 7 53 0	2 0698-3495 0698-0085 7 0757-0317 7 0757-0317 0 0757-0419	R-F 866 1% .125W R-F 2.61K1% R-F 1.33K1% R-F 1.33K1% R-F 681 1% .125W	A5 R122 A5 R123 A5 R124 A5 R125 A5 R126	9 0757-0442 9 0757-0442 2 0698-4469 9 0757-0442 5 0683-6245	R-F 10K1% .125W R-F 10K1% .125W R-F 1.15K1% R-F 10K1% .125W R-F 620K5% .25W
A5 R A5 R A5 R	55 1 56 2 57 3 58 1	2 0698-3437 L 0757-0402 2 0757-0403 3 0757-0420 L 0757-0452	R-F 133 1% .125W  R-F 110 1% .125W  R-F 121 1% .125W  R-F 750 1% .125W  R-F 27, 4K1%	A5 R130 A5 R131 A5 R132 A5 R133 A5 R134	1 0698-3444 9 0757-0418 3 0757-0438 1 0698-3444 4 0757-0398	R-F 316 1% .125W R-F 619 1% .125W R-F 5.11K1% R-F 316 1% .125W R-F 75 1% .125W
A5 R A5 R A5 R A5 R	60 6 61 1 62 6 63 1	0757-0452 0757-0978 1 0757-0452 0757-0449 1 0757-0442	R-F 27.4K1%  R-F 95.3K1%  R-F 27.4K1%  R-F 20K1% .125W  R-F 27.4K1%	A5 R135 A5 R136 A5 R137 A5 R138 A5 R139	9 0698-4367 6 2100-3351 6 0757-0407 9 0757-0442 4 0757-0405	R-F 20.5 1% R-TRMR 500 10% R-F 200 1% .125W R-F 10K1% .125W R-F 162 1% .125W
A5 R A5 R A5 R	66 9 67 9 68 4	3 0757-0442 3 0757-0442 9 0757-0442 4 0698-3455 5 0757-0465	R-F 10K1% .125W R-F 10K1% .125W R-F 10K1% .125W R-F 261K1% .125W R-F 100K1% .125W	A5 R140 A5 R141 A5 *R142 A5 R143 A5 R144	9 0757-0442 1 0698-3262 4 0757-0281 0 0757-0419 1 0757-0452	R-F 10K1% .125W R-F 40.2 1% R-F 2.74K1% R-F 681 1% .125W R-F 27.4K1%
A5 R A5 R A5 R	71 72 73 74	5 0757-0449 3 0757-0470 3 0757-0454 5 0757-0449 7 0698-0082	R-F 20K1% .125W  R-F 162K 1% .125W  R-F 33.2K1%.125W  R-F 20K1% .125W  R-F 464 1% .125W	A5 R145 A5 R146 A5 R147 A5 R148 A5 R149	9 0698-0084 0 0698-4483 7 2100-3352 7 2100-3352 2 0698-3156	R-F 2,15K 1%.125 R-F 18.7K1%.125W R-VAR 1K .5W R-VAR 1K .5W R-F 14.7K1%
A5 R A5 R A5 R A5 R	176 177 178	7   0698-0082 7   0698-0082 2   0757-0346 2   0757-0346 2   0757-0346	R-F 464 1% .125W R-F 464 1% .125W R-F 10 1% .125W R-F 10 1% .125W R-F 10 1% .125W	A5 R150 A5 R151 A5 R152 A5 R153 A5 R153	7 0757-0416 0 0757-0394 9 0757-0442 7 0757-0416 6 0757-0069	R-F 511 1% .125W R-F 51.1 1% R-F 10K1% .125W R-F 511 1% .125W R-F 121 1% .25W
A5 R A5 R A5 R A5 R	181 182 183 184	7 2100-3352 7 2100-3352 6 0757-0283 6 0757-0283 8 0698-3558 7 0698-0082	R-VAR 1K .5W R-VAR 1K .5W R-F 2K1% .125W F R-F 2K1% .125W F R-F 4.02K1% R-F 464 1% .125W	A5 R155 A5 R156 A5 R157 A5 R158 A5 R159	8 0698-3558 8 0698-3558 6 0757-0407 0 0757-0401 9 0757-0442	R-F 4.02K1% R-F 4.02K1% R-F 200 1% .125W R-F 100 1% .125W R-F 10K1% .125W
A5 R A5 R A5 R A5 R	186 187 188	7 0698-0082 1 0698-3155 7 0698-3226 8 0757-0433 7 0757-0200	R-F 464 1% .125W R-F 4.64K 1%.125 R-F 6.49K1% R-F 3.32K1% R-F 5.62K1%	A5 R160 A5 R161 A5 R162 A5 R163 A5 R164	8 0757-0433 8 2100-3353 2 2100-3274 9 0757-0442 6 0757-0712	R-F 3.32K1% R-VAR 20K .5W R-VAR 10K 10% R-F 10K1% .125W R-F 90.9 1% .25W
A5 R A5 R A5 R A5 R	R91 R92 R93	1 0698-4442 6 0757-0449 6 0757-0449 3 0757-0280 8 0757-0417	R-F 4.42K1% R-F 20K1% .125W R-F 20K1% .125W R-F 1K1% .125W F R-F 562 1% .125W	A5 R165 A5 R166 A5 R167 A5 R168 A5 R169	9 0757-0997 9 0757-0442 9 0757-0442 5 0757-0290 5 0757-0290	R-F 39.2 1% 1/2W R-F 10K1% .125W R-F 10K1% .125W R-F 6.19K1% R-F 6.19K1%
A5 R A5 R A5 R	R97 R102 R103	8 0757-0417 3 0698-4428 3 0698-4428 6 0757-0449 6 0757-0449	R-F 562 1% .125W R-F 1.69K1% R-F 1.69K1% R-F 20K1% .125W R-F 20K1% .125W	A5 R170 A5 R171 A5 R172 A5 R173 A5 R173	5 0757-0290 1 2100-3207 1 2100-3207 3 0698-4486 3 0698-4486	R-F 6.19K1% R-VAR 5K 10% R-VAR 5K 10% R-F 24.9K1% R-F 24.9K1%
A5 R A5 R A5 R A5 R	R106 1107 1108	8 0757-0417 8 0757-0417 2 0757-0346 2 0757-0346 0 0698-4433	R-F 562 1% .125W R-F 562 1% .125W R-F 10 1% .125W R-F 10 1% .125W R-F 2.26K1%	A5 R175 A5 R176 A5 R177 A5 R178 A5 R178	3 0757-0438 9 0757-0442 6 0757-0407 9 0757-0442 6 0757-0407	R-F 5.11K1% R-F 10K1% .125w R-F 200 1% .123W R-F 10K1% .125W R-F 200 1% .125W

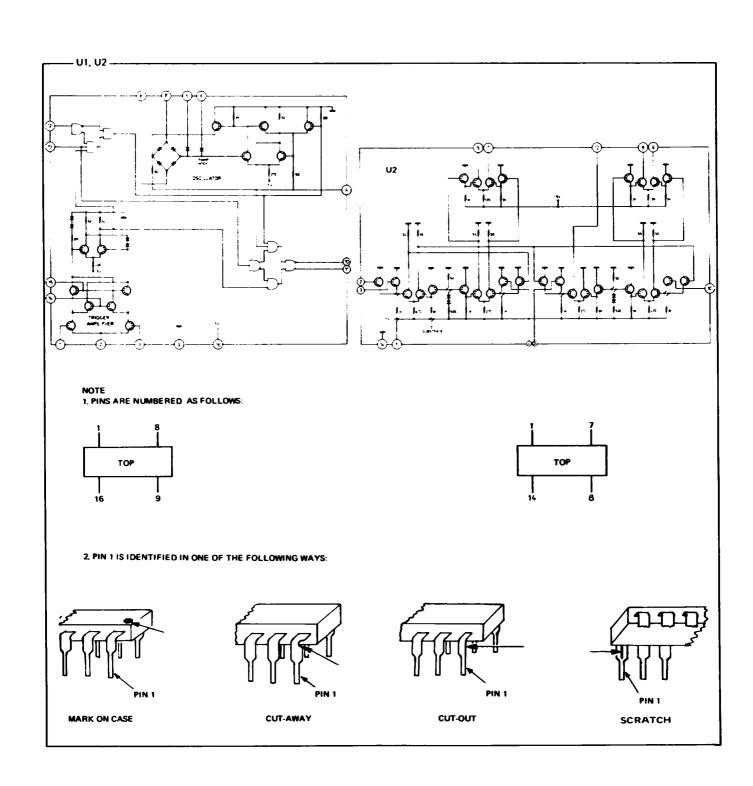
Table 6-3. Replaceable Parts (cont'd)

Ŧ			Table 0-3. Replace	<u> </u>	<del>-</del>	<del>                                     </del>	r
REFER	RENCE GNATOR	C H-P PART D NUMBER	DESCRIPTION	REFERENCE DESIGNATOR		H-P PART NUMBER	DESCRIPTION
A5 A5 A5 A5 A5	R180 R181 R182 R183 R184	2 0698-3156 1 0757-0428 9 0757-0278 8 0698-3558 9 0757-0442 6 0757-0407	R-F 14.7K1% R-F 1.62K 1% R-F 1.78K1% R-F 4.02K1% R-F 10K1% .125W	A6 J1 A6 T1	4		CBL AY-XFMR BD PC XFMR-POWER
A5 A5 A5 A5	R186 R187 R188 R189	9 0757-0442 9 0757-0442 3 0757-0438 6 0757-0407	R-F 200 1% .125W R-F 10K1% .125W R-F 10K1% .125W R-F 5.11K1% R-F 200 1% .125W	A6 X1	6	1251-0333	CONN PC 20CONI
A5 A5 A5 A5 A5	R190 R191 R192 R193 R194	6 0757-0465 6 0757-0465 9 0757-1094 3 0698-3446 0 0698-3435	R-F 100K1% .125W R-F 100K1% .125W R-F 1.47K1% R-F 383 1% .125W R-F 38.3 1%	A8 C1		08082-66508 0160-3470	BD AY AMP BUFFER
A5 A5 A5 A5 A5	R195 R196 R197 R198 R199	9 0757-0418 9 0757-0442 3 0757-0438 8 0698-0083 9 0757-0442	R-F 619 1% .125W R-F 10K1% .125W R-F 5.11K1% R-F 1.96K1% R-F 10K1% .125W	A8 C2 A8 C3 A8 C4 A8 C5	4 5 4 4	0180-1746 0160-3470 0180-1746 0160-3470	C-F 15UF 20V TA C-F .01UF 50V C-F 15UF 20V TA C-F .01UF 50V C-F .01UF 50V
A5 A5 A5 A5 A5	R200 R201 R202 R203 R204	8 0757-0441 3 0757-0438 8 0698-0083 9 0757-0442 0 0698-3435	R-F 8.25K1% R-F 5.11K1% R-F 1.96K1% R-F 10K1% .125W R-F 38.3 1%	A8 C7 A8 C9 A8 C10 A8 C-11	4 4 4	0160-3470 0160-3470 0160-3470 0160-3470	C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V C-F .01UF 50V
A5 A5 A5 A5 A5	R205 R206 R207 R208 R209	3 0698-3446 9 0757-1094 0 0757-0500 7 0698-3440 0 0757-0394	R-F 383 1% .125W R-F 1.47K1% R-F 30.1 1% .25W R-F 196 1% .125W R-F 51.1 1%	A8 CR2 A8 CR3 A8 CR4 A8 MP1	7 7 7	1901-0533 1901-0533 1901-0533 1260-0364	DIO HOT CARR DIO HOT CARR DIO HOT CARR CONNECTOR LEAD
A5 A5 A5 A5 A5	R210 R211 R212 R213 R214	2 2100-3349 3 0698-3438 3 0698-3438 2 2100-3274 6 0757-0283	R-VAR 100 -+10% R-F 147 1% .125W R-F 147 1% .125W R-VAR 10K 10% R-F 2K1% .125W F	A8 R1 A8 R2 A8 R3 A8 R4 A8 R11	4 5 3	0698-5174 0698-4411 0757-0274 0757-0280 0757-0280	R-F 200 5% .125W R-F 140 1% .125W R-F 1.21K1% R-F 1K1% .125W F R-F 1K1% .125W F
A5 A5 A5 A5 A5	R215 R216 R217 R218 R220	1 0757-0452 1 0757-0452 0 2100-3355 9 0757-0723 0 0757-0394	R-F 27.4K1% R-F 27.4K1% R-VAR 100K R-F 365 1% 25W R-F 51.1 1%	A8 R12 A8 R15 A8 R16 A8 R17 A8 R19	4 5 2	0757-0274 0698-4411 2100-3350 2100-3349 0698-5176	R-F 1.21K1% R-F 140 1% .125W R-VAR 200 10% R-VAR 100 -+10% R-F 510 5% .125W
A5 A5 A5 A5 A5	R221 R222 R223 R224 R225	9 0757-0723 2 0757-0388 8 0698-3152 0 0757-0401 0 0757-0401	R-F 365 1% 25W R-F 30.1 1% R-F 3.48K 1% R-F 100 1% .125W R-F 100 1% .125W	A8 R20 A8 R21 A8 R22 A8 R23	6 6 4	0698-5174 0698-5180 0698-5180 0757-0273	R-F 200 5% .125W R-F 2K5% .125W R-F 2K5% .125W R-F 3.01K1%
A5 A5	R230 R231	2 2100-3274 8 0698-3152	R-VAR 10K 10% R-F 3.48K 1%	A8 R25 A8 R26 A8 R13	5 5	0698-3381 0698-3381 0757-0387	R-F 3.01K1% R-F 150 5% .125W R-F 150 5% .125W R-F 27.4 1%
A5 A5 A5 A5 A5	V1 V2 V3 V4 U5	7 1826-0111 7 1826-0111 7 1826-0111 7 1826-0111 7 1826-0111	IC-DUAL OP AMPL	A8 R14 A8 U1	1	0757-0387 5081-3027	R-F 27.4 1%
A5 A5 A5 A5 A5	U6 U7 U8 U9 U10	7 1826-0111 7 1826-0111 7 1826-0111 7 1826-0111 7 1826-0111	IC-DUAL OP AMPL				
A5 A5 A5 A5	VR3 VR24 VR25 VR35	3 1902-3268 6 1902-0184 7 1902-3139 3 1902-0579	DIO 26.1V 5% .4W DIO 16.2V 5% .4W DIO ZNR 8.25V 5% DIO 5.11V 5% 1W				·
A5 A5	₩7 ₩8	5 5081-1957 4 5081-1956	CBL RB 14C 191MM CBL RB 16C 165MM				
A5	X 4	6 1200-0588	SOCKET IC 16 CONT				

A5(x4) 5040-9316 LOCK Clip 16pol

											•					
DESCRIPTION	BD AY ATTENUATOR	DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V	DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V	DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V DIO SI .05A 30V	JACK RECEP STRAI	RELAY 12V .5A RELAY 12V .5A RELAY 12V .5A RELAY 12V .5A	COIL FXD COIL FXD COIL FXD COIL FXD INDUCTANCE 3BEAD	COIL-CHOKE 500UH	R-F 150 1% .125W R-F 150 1% .125W R-F 150 1% .125W R-F 150 1% .125W R-F 37.4 1%	R-F 115 12 125W R-F 115 12 125W R-F 115 12 125W R-F 115 12 125W R-F 115 12 125W	R-F 51.1 1% .25W R-F 51.1 1% .25W R-F 75 1% .125W R-F 75 1% .125W R-F 20 1% .125W	R-F 20 1% .125W R-F 20 1% .125W R-F 20 1% .125W R-F 3.32K1% R-F 3.32K1%	R-F 3.32K1% R-F 3.32K1% R-F 432 1% .25W R-F 432 1% .25W R-F 432 1% .25W	R-F 432 1% .25W	CBL RB 16C 210MM	
H-P PART NUMBER	08082-66509	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040	1901-0040 1901-0040 1901-0040 1901-0040	1250-0835 1250-0835	0490-1034 0490-1034 0490-1034 0490-1034	9100-0346 9100-0346 9100-0346 9100-0346 5081-1973	5081-1973 9140-0118 9140-0118	0757-0284 0757-0284 0757-0284 0757-0284 0698-4377	0698-4377 0698-4406 0698-4406 0698-4406 0698-4406	0757-0706 0757-0706 0757-0398 0757-0398 0757-0384	0757-0384 0757-0384 0757-0384 0757-0433	0757-0433 0757-0433 0757-0337 0757-0337 0757-0337	0757-0337	5081-1959	
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REF	·	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	A99 A99 A99	A9 A9	49 49 49	449 449 469	A A 9	A 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	A99 A99 A99	A A B B B B B B B B B B B B B B B B B B	A99 A99 A99	A99 A99 A99	49	А9	





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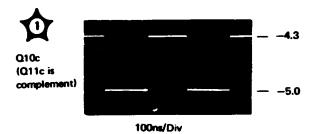
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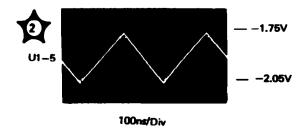
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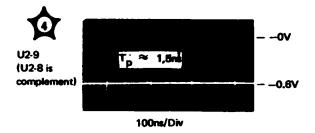
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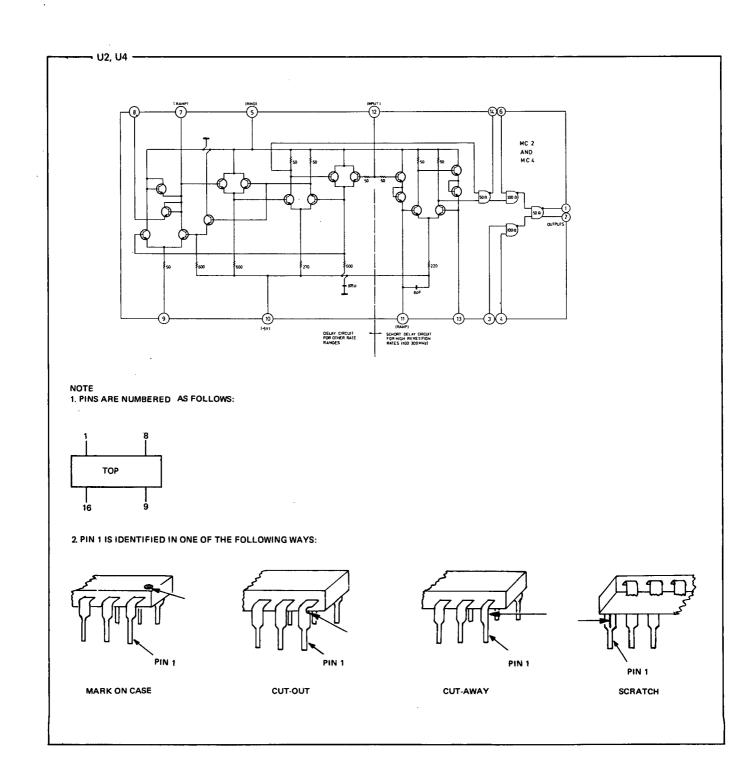
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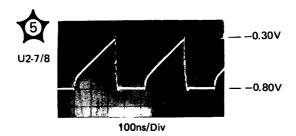


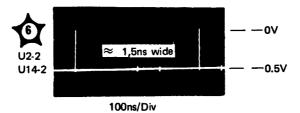


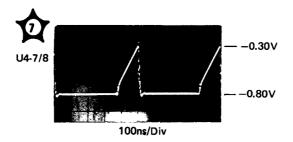


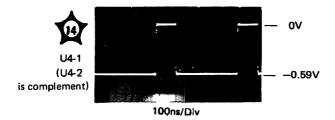


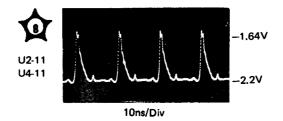




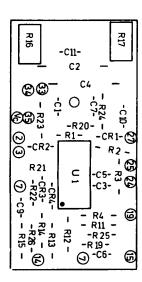


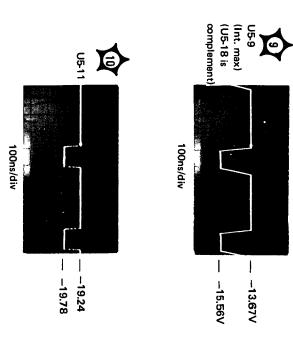






Board A8 — Buffer Amplifier Layout





U5-14 (U5-15 is complement)

> -8 -8

-9.7V

100ns/div

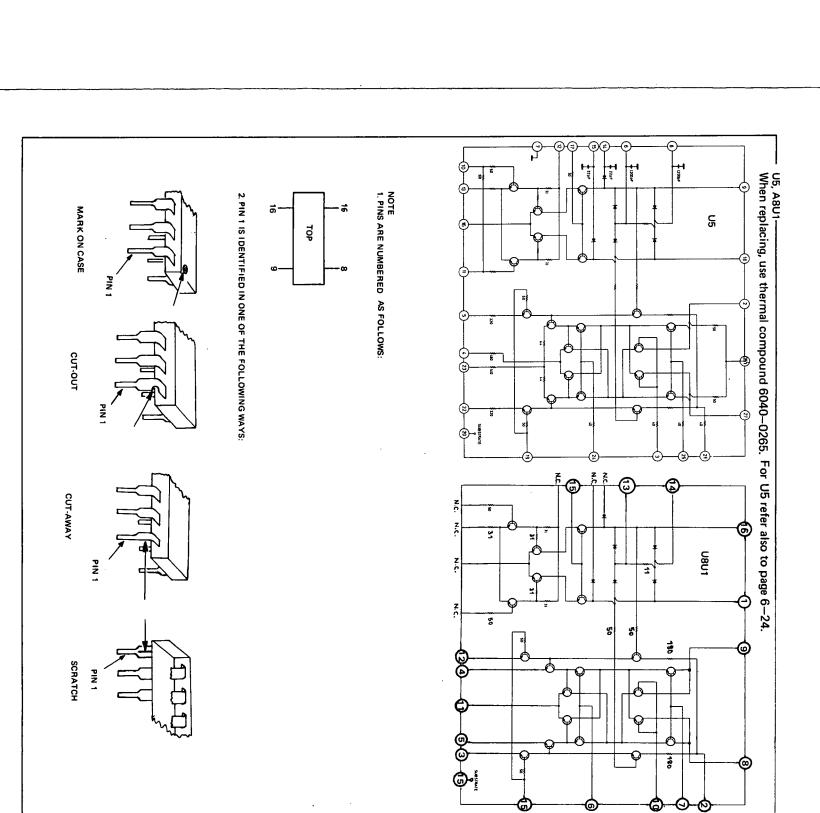
**E** 

U4-2 (U5-27 is ∞mplement

-- - 14.72V

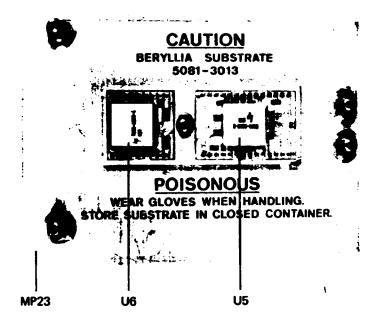
— -12.83V

100ns/div



SLOPE GENERATOR—PART OF BOARD A4 AND BUFFER AMPLIFIER—BOARD A8

U5, U6



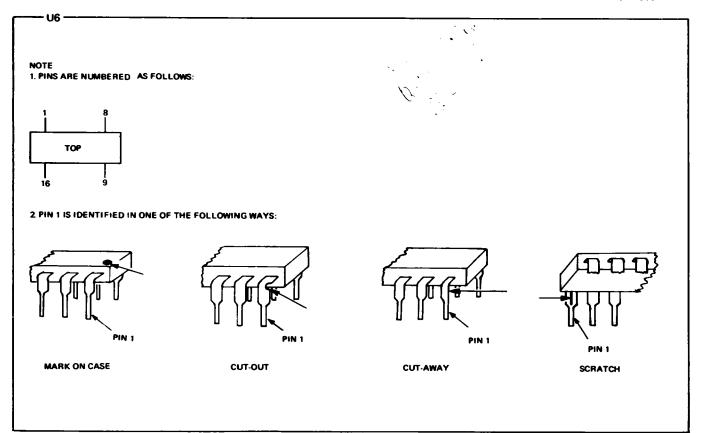
CAUTION Ensure U5, U6 are oriented as shown

NOTE: Disregard poisonous materials CAUTION on MP23. (Beryllia is non-toxic when solid and in fired ceramic). Risk lies in breathing particles. This is only possible if the substrate is:

1. ground with a very hard abrasive

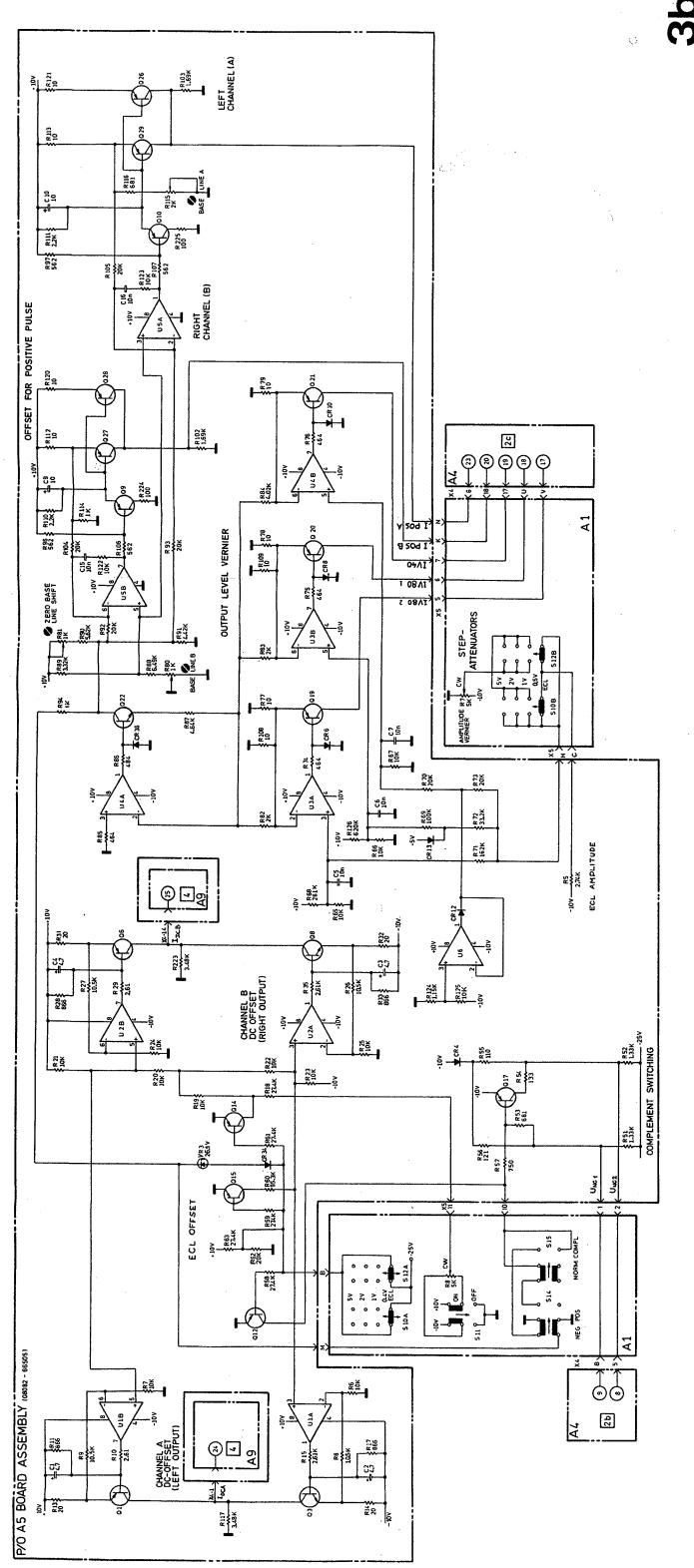
2. heated to 800%C in damp air

Neither procedure is necessary and both must be avoided.



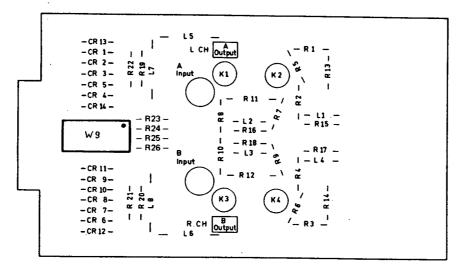
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							GRID LOC C-2 C-2 D-3
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		JS P. Circuit		2	l		GRID CC 44 PF 24 PF 24 P
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		- 873 -	3000	000	- 862 - - 863 -		GRID COC COC COC COC COC COC COC COC COC CO
人		- R71 - - R72 - - R70 - - R67 -	- 58 - - 288 - (20)	— СВЗТ — — СВЗ — — ВВТ —	- 8 103 - C	,	REF DESI R199 R200 R200 R200 R200 R200 R201 R211 R211
		- CB 13 - - CB 15 - - B 82 -	- 984 - - 984 - - 684 -	- 892 - - 890 - - 891 -	- 111 A		179 F-3 180 F-3 181 F-3 182 F-3 182 F-3 184 E-3 186 E-3 187 E-4 190 F-4 190 F-4 195 C-5 195 C-5 195 C-5 196 C-4 195 C-5 196 C-4 196 C-
		(9) - 7018 -	(I)	→ 68 A − − 68 A − − 68 A − − 68 A − − − 601 A − − 88 A − − − 88 A − − − − − − − − −	- C 10 - B 500		
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ш		EAR)	- Z818 - 27	CR29 - R175 -			RFF G L L L L L L L L L L L L L L L L L L
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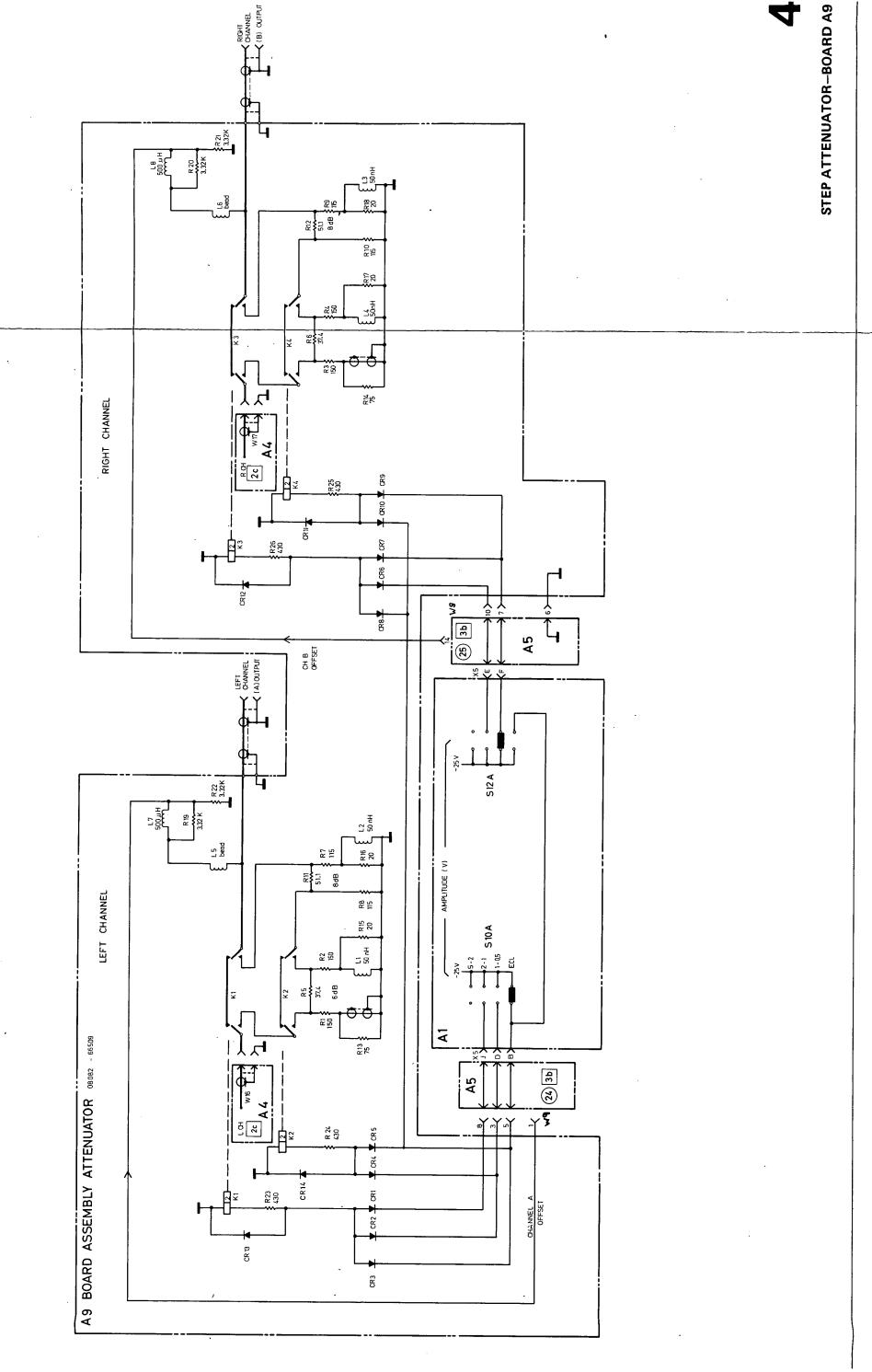
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I		- 869 - - 876 - - 876 - - 873 - - 873 - - 873 -	- CB8 10 - H533 - 11 - C3 - 12 - C3 - 13 - C3 - 14 - C3 - 15 - C3 - 16 - C3 - 17 - C3 - 18 - C3 - 18 - C3 - 19 - C3 - 10 - C3 -	8 LII 8 - 8 -	- 25 8		REF GRID DESIG LOC R115 K-5 R116 H-4 R120 K-5 R121 K-5 R123 J-4 R124 J-2 R126 L-2 R130 E-2 R131 D-3 R133 E-2 R133 E-2 R134 E-3 R136 E-2 R133 E-2 R134 E-3 R135 E-3 R136 E-2
<u> </u>	7	- 828 - - 829 - - 829 -	<u> </u>	10 - (7)	- 85 8 - (20) - 65 8 - (30) - 19 8 - (30) - 19 8 - (30)		K-2 R91 CC CC CESIG COC CC CC CESIG COC CC
ш		C 22 - R 212 - 3 - R 216 - 7 - R 216 - 7	81 19 60 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	CH29 - R175 - R191 - R190 - R175 - R177 - R177 - R176 - R1	8918 - 1 6918 - 1 7773	3a	GRID REF H-5 BR72 H-5 BR74 H-5 BR74 H-5 BR74 H-5 BR74 H-5 BR74 H-5 BR74 H-5 BR74 H-5 BR84 H-2 BR84 H-2 BR84 H-2 BR84 H-2 BR86 H-2 BR
Ш	<b>4</b>	21172 - 2117 2132 - 2117 2132 - 2117 2132 - 2117	(07.3) - 261 8 - - 961 8 - 8 - (3) 3 - 761 8 - 8 - 712 8 -	C 27 8206 – (2) 8204 – 8156 – 8206 – (2) (2)	- • •	See Service Sheet	GRID REF G-4 R53 G-4 R55 G-4 R55 H-3 R56 H-3 R56 H-3 R56 G-3 R60 G-3 R60 G-2 R65 G-2 R65 G-3 R69 G-2 R65 G-2 R
	Offset Board Layout	- 091 2 - 951 2 - 651 8 - 251 2 - 7711 - 671	8 - 891 81 - 189	6832 - 68173 -			REF         GRID         REF           DESIG         LOC         DESIG           DQ49         F-3         R15           G50         E-3         R15           G51         E-3         R17           G52         C-5         R19           G53         C-5         R19           G54         C-5         R20           G55         D-4         R21           G56         E-5         R24           G56         E-5         R24           G57         D-5         R24           G63         R-3         R28           R6         G-3         R29           R1         G-4         R31           R1         G-4         R35           R1         G-4         R35           R1         G-4         R35           R1         G-4         R45
0	Board A5	α - 0511 α - 0511 α - 0511 σ - 0784 σ - 0784	032 033 031 - C C C C C C C C C C C C C C C C C C	R 196 - R 215 - R 198	- 8 118 8 50	licates pin 1	MEF GRID RE COSIGNO DESIGNO DE DESIGNO DESIGNO DE DE DESIGNO DE DESIGNO DE DESIGNO DE DESIGNO DE DE DESIGNO DE DESIGNO DE DE DESIGNO DE DE DE DE DE DE DESIGNO DE
B	·	- 971 8	- 751 H		- R 221 - 230 - 231 - 230 - 23	• Ind	D REF GRID CR36 LOC CR37 F2 CR36 LOC CR37 F2 CR36 CR37 CR36 CR36 CR37 CR36 CR37 CR36 CR37 CR36 CR37 CR37 CR37 CR37 CR37 CR37 CR37 CR37
A			·				GG3 CR3 K-4 H-3 CR4 G-5 G-3 CR3 K-4 H-3 CR4 G-5 G-3 CR8 H-3 H-2 CR10 J-3 J-5 CR24 E-2 J-4 CR26 D-4 E-3 CR26 D-4
		2	8	7	2	9	C1 C1 C2



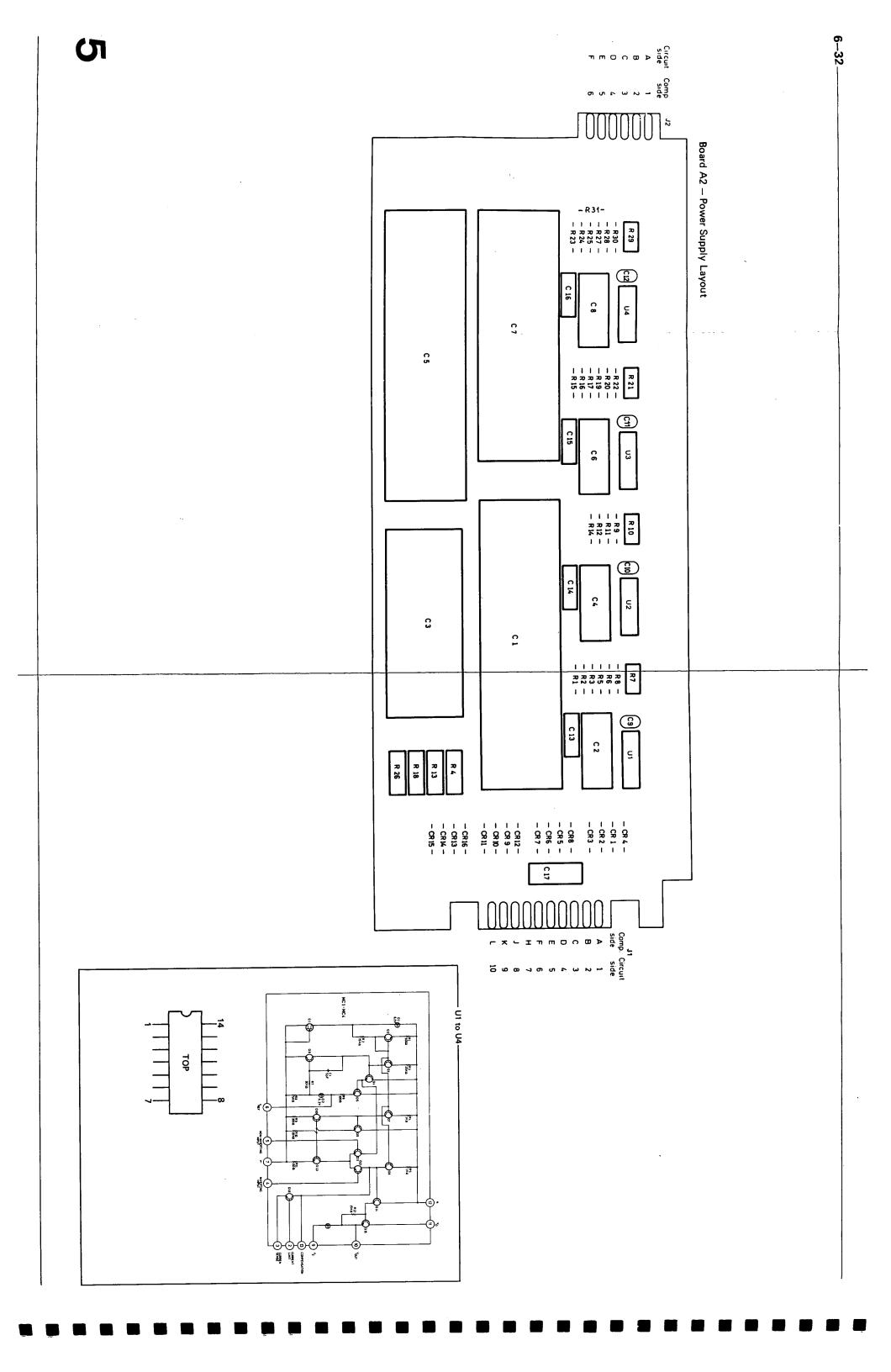
DC OF FSET AND AMPLITUDE VERNIER-PART OF BOARD A5

### Board A9 - Attenuator Layout





6-31



BACKDATING-

### 7-1 INTRODUCTION

7-2 This section contains backdating information which adapts this manual to instruments with serial numbers lower than that shown on the title page.

7-3 Changes are listed in the serial number order that they occured in the manufacture of the instrument. However, in adapting this manual to an instrument with serial number lower than that shown on the title page, apply the changes in reverse order. That is, begin with the latest change that applies to the serial number in question. Table 7-1 lists the serial numbers to which each change applies. Where changes to components occur, alter the associated schematic and layout diagram as necessary.

Table 7-1 Manual Backdating Changes.

Changes
1 to 23
2 to 23
3 to 23
4 to 23
5 to 23
6 to 23
7 to 23
8 to 23
9 to 23
10 to 23
11 to 23
12 to 23
13 to 23
14 to 23
15 to 23
16 to 23
17 to 23
18 to 23
19 to 23
20 to 23
21 to 23
22 to 23
23

### CHANGE 1 (1410G00145 and below)

A9 R23, 24, 25, 26 are replaced by a wire link. Amplitude switches S10A, S12A (Schematic 4) are connected to -10 V (instead of -25 V as in later models). A9 K1, 2, 3, 4 are of different type but may be replaced by relay part number 0490-1034 (see A9 parts list) used in later models. The following components are different from those in later models:

A5CR3	1902-0025	DIODE ZNR 10V
A5R58-63	0757-0442	R-F 10K 1%
A5R62	0757-0440	R-F 7.5K 1%

### CHANGE 2 (1410G00170 and below)

### Change frame parts list to read:

F1	1400-0084	FUSE HOLDER
S6	3101-0124	SW P-BTN SPST
Change A4 p	oarts list to rea	<b>d</b> :

Chi/,Chio	1901-0555	DIO HOT CARR.
R204	0757-0283	R-F 2K

### CHANGE 3 (1410G00270 and below)

2110-0464

Change	frame	parts	list	to	read:

F1

F1	1490-0090	WASHER NEOPRANE
F1	2190-0054	WASHER LOCK
F1	2110-0467	NUT HEX. MET
F1	2110-0465	FUSEHOLDER

Delete the following from the frame parts list:

**BODY FUSE** 

### CHANGE 4 (1410G00300 and below)

### Change A4 parts list to read:

C6	0160-4030	C-F 820 PF
C13	0160-4030	C-F 820 PF
R2	0757-0279	R-F 3.16K
R17	0757-0279	R-F 3.16K

### CHANGE 5 (1410G00315 and below)

Change A5 parts list to read:

**R88** 

0698-4471

R-F 7.15K

**R90** 

0698-4444

R-F 4.87K

### CHANGE 6 (1410G00350 and below)

Change A5 parts list to read:

R164

0757-0400

R-F 90.9

### CHANGE 7 (1410G00390 and below)

Change A5 parts list to read:

CR3

1902-3256

**DIO BKDN 23.7V** 

**R94** 

0757-0273

R-F 3.01K R-F 3.01K

0757-0273

For these instruments, R142 did not have a factory selected

value.

### CHANGE 8 (1410G00430 and below)

Delete the following components from the A5 parts list, schematics and component layouts:

CR23, CR37, R224 and R225

### CHANGE 9 (1410G00500 and below)

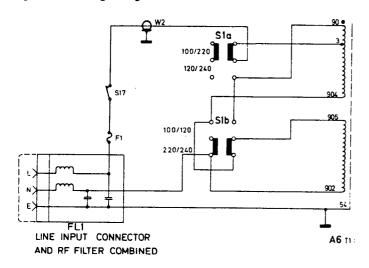
Change frame parts list to read:

MP7

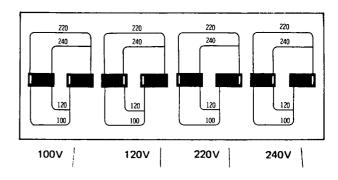
08082-00203 PANEL REAR

S1 3101-1609 SWITCH DPDT DUAL

Page 6-33, change diagram to read:



Page 2-2, replace Figure 2-4 content by:



### CHANGE 10 (1635G00515 and below)

Change the A3 parts list to read:

CORE SHIELDING BEAD L2-L5 9170-0029 L6-L10 9170-0029 L15-L19 9170-0029

CORE SHIELDING BEAD CORE SHIELDING BEAD

L24,L26

9170-0029

CORE SHIELDING BEAD

Change the A9 parts list to read:

L5.L6

9170-0029

**CORE SHIELDING BEAD** 

### CHANGE 11(1635G00560 and below)

Delete the following from the frame parts list: MP33, MP34, MP35, MP36

### CHANGE 12 (1635G00575)

Change the A4 parts list to read:

R47,R106

0757-0283

R-FXD 2K 1% .125W

R241

0698-5180

R-FXD 2K 5% .125W

Delete the following components from the A4 parts list, schematic and component layout:

L30, L31, R242 and R243

### CHANGE 13 (1635G00775 and below)

Change A3 parts list to read:

R5.R6.R87

2100-2788

**R-VAR 4.7K** 

### CHANGE 14 (1635G00795 and below)

Delete A2 R31 from parts list, schematic and component layout.

Change A5 parts list to read:

R165

0698-5890

R-F 39 OHM

### CHANGE 15 (1635G00905 and below)

Change A2 parts list to read:

C15

0180-0374

C-F 10 UF 20V

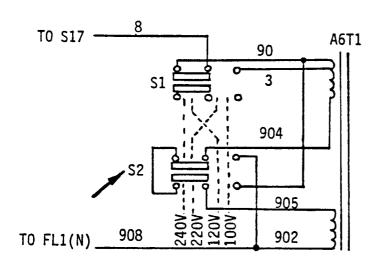
### CHANGE 16 (1635G00925 and below)

Change frame parts list to read:

W2

08082-61602 CBL AY PWR SHLD

Change schematic 5 as follows:



### CHANGE 17 (1822G01045 and below)

Change frame parts list to read:

MP7

08082-00204 PANEL REAR

Add the following parts to the frame parts list:

FL1

9135-0035

FILTER LINE

S1,S2

3101-2298

SW SLIDE

### CHANGE 18 (1822G01205 and below)

Change A4 parts list to read:

C19,C20

5080-1087

CAP SELECT

C17,C18

5080-1088

CAP SELECT

### CHANGE 19 (1822G01230 and below)

Change frame parts list to read:

MP4

08015-04103 COVER BOTTOM

### CHANGE 20 (1822G01735 and below)

Delete L13 from the A3 parts list, schematic and component layout.

Change the A4 parts list to read:

R103

0757-0407

R-F 200

R208

0757-0424

R-F 1.1K

Replace VR42 with R209 on A4 parts list, component layout and schematic.

R209

0757-0428

R-F 1.62K

Change A5 parts list to read:

C22,C23

0160-4209

C-F .01UF

### CHANGE 21 (1822G02125 and below)

Change the frame parts list to read:

R1,R2,R3 2

2100-3081

RESISTOR VAR. 50K 10%

### CHANGE 22 (1822G02275 and below)

Change the A4 parts list to read:

R225

0757-0403

R-F 121 1%

R226,R227 R228 0757-0798 0698-4409 R-F 110 1% R-F 127 1%

Delete R224 and R245 from the A4 parts list, component layout and schematic.

### CHANGE 23 (1822G02845 and below)

Change the A4 parts list to read:

J1 1200-0423 J2 1200-0424

SKT IC.16CON SKT IC 14CON